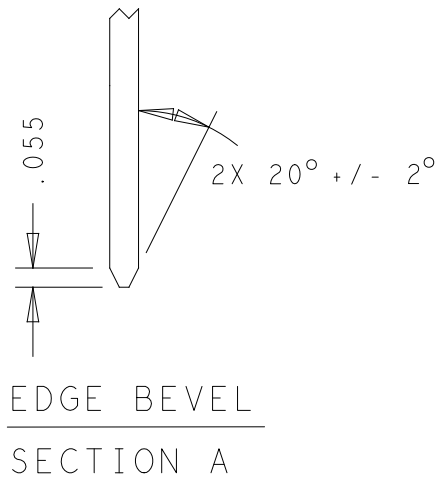
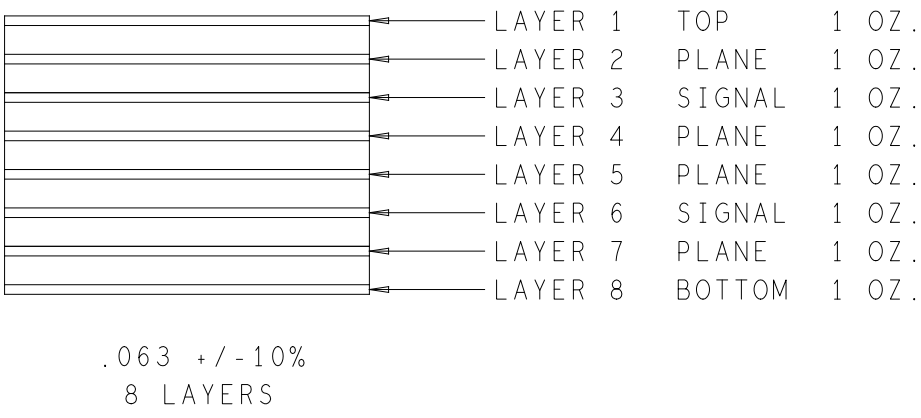




DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	FINISHED SIZE	TOLERANCE	DRILL	PLATED
+	8.059	+3.0/-7.0		PLATED
x	8.142	+3.0/-7.0		PLATED
o	12.0	+3.0/-10.0		PLATED
□	13.701	+3.0/-3.0		PLATED
F	15.748	+3.0/-3.0		PLATED
F	18.0	+3.0/-3.0		PLATED
o	28.299	+2.402/-2.402		PLATED
•	31.0	+2.0/-2.0		PLATED
H	35.0	+2.0/-2.0		PLATED
+	39.37	+1.969/-1.969		PLATED
I	40.0	+3.0/-3.0		PLATED
J	47.0	+3.0/-3.0		PLATED
△	49.213	+1.969/-1.969		PLATED
o	50.0	+3.0/-3.0		PLATED
⊖	55.0	+3.0/-3.0		PLATED
K	63.0	+2.0/-2.0		PLATED
L	35.0	+2.0/-2.0		NON-PLATED
⊖	39.37	+1.969/-1.969		NON-PLATED
M	47.0	+2.0/-2.0		NON-PLATED
△	80.0	+3.0/-3.0		NON-PLATED
o	93.0	+2.0/-2.0		NON-PLATED
P	94.488	+3.0/-3.0		NON-PLATED
⊖	118.0	+3.0/-3.0		NON-PLATED
o	125.0	+2.0/-2.0		NON-PLATED
R	134.0	+2.0/-2.0		NON-PLATED
S	148.0	+3.0/-3.0		NON-PLATED
•	149.0	+3.0/-3.0		NON-PLATED
B	55.118x21.654	+3.0/-3.0		PLATED
⊖	62.205x36.614	+3.0/-3.0		PLATED
B	62.992x21.654	+3.0/-3.0		PLATED

TOTAL HOLES: 2264



NOTES:

- PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW THE REQUIREMENTS OF IPC-2221;-2222 AND IPC-6011;-6012, TYPE 3 ,CLASS 2.
- MATERIAL: MEGTRON6 R-5775G OR EQUIVALENT IAW IPC-4101. MUST HAVE UL FLAMMABILTY RATING OF 94V-0 OR BETTER. BOARD MUST BE UL RECOGNIZED AND VENDOR SHALL AFFIX UL LISTING DESIGNATION ON PRINTED CIRCUIT BOARD. THIS BOARD IS INTENDED FOR LEAD-FREE ASSEMBLY.(RoHS COMPLIANT)
- FINISH:IMMERSION GOLD (ENIG) 2-6 MICROINCHES OVER NICKEL 100 MICROINCHES MIN. FULL BODY IMMERSION GOLD ACCEPTED.
- APPLY SOLDER MASK, IAW IPC-SM-840, CLASS H, TYPE B1 (LPI) COLOR GREEN, MATTE FINISH. ALL SOLDER PADS MUST BE 100% FREE OF SOLDER MASK.
- APPLY SILKSCREEN TO BOTH SIDES IAW MARKING TEMPLATES. USING WHITE NON-CONDUCTIVE EPOXY INK. VENDOR MAY VIA CAP OR PLUG VIAS BEFORE FINAL SOLDER MASK TO AVOID TRIMMING OF SILKSCREEN. SILKSCREEN INK NOT ALLOWED ON ANY EXPOSED ETCH. VENDOR TO DETERMINE BEST PROCESS TO ENSURE THIS.
- PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. VENDOR MUST USE IPC D 356 NETLIST PROVIDED PRIOR TO BOARD FABRICATION AND COMPARE AGAINST GERBER DATA AND REPORT ANY DISCREPANCIES.
- UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING. TOLERANCE OF +/- .003. FINISH HOLE SIZES LESS THAN .012 ,TOLERANCE SHALL BE +.003 -.012
- MARK DATE CODE AND MANUFACTURERS IDENTIFICATION ON SOLDER SIDE PER IPC-6011;-6012.
- DIMENSIONING AND TOLERANCING IAW ANSI Y14.5.
- ALL PATTERNS ARE VIEWED FROM THE COMPONENT SIDE LOOKING THROUGH THE BOARD.
- VENDOR TO REMOVE ALL BURRS AND SHARP EDGES.
- BOW AND TWIST NOT TO EXCEED 1 % OVER ENTIRE AREA.
- ALL INSIDE RADII SHALL BE .062 UNLESS OTHERWISE SPECIFIED.
- VENDOR IS ALLOWED TO FOLLOW BOARD OUTLINE FOR SIZE OF BOARD. VENDOR IS ALSO ALLOWED TO DO THE FOLLOWING: REMOVE INTERNAL NON FUNCTIONAL PADS. ADD TEAR DROPPING AND REMOVE SOLDER MASK SLIVERS LESS THAN .003 TO ENHANCE PRODUCIBILITY.
- BOARD TO BE PANELIZE IN AN ARRAY WITH RAILS, TOOLING HOLES AND FIDUCIALS. VENDOR TO SEND PROPOSED ARRAY FOR APROVAL BEFORE PROCEEDING WITH BUILD.
- GOLD PLATE CARD EDGE FINGERS .00003 MIN. OVER NICKEL .0001 MIN.

TABLE 1 CONTROL IMPEDANCE						
VENDOR TO DETERMINE DIELECTRIC THICKNESS. TRACES MAY BE ADJUSTED TO MEET IMPEDANCE TARGETS. A TEST COUPON IS TO BE GENERATED AND A TDR (TIME DOMAIN REFLECTOMETER) TEST PERFORMED BY BOARD VENDOR TO VERIFY IMPEDANCES ARE WITHIN SPECIFIED TOLERANCES.						
LAYER #	SINGLE ENDED IMPDANCE	DIFFERENTIAL PAIR IMPDANCE	TOLERANCE	LINE WIDTH	LINE SPACING	REFERENCE LAYER
1	50 OHM	-	10%	.0063	-	2
3	50 OHM	-	10%	.0080	-	2 & 4
6	50 OHM	-	10%	.0080	-	5 & 7
8	50 OHM	-	10%	.0063	-	7 & 9
1	-	85 OHM	10%	.0059	.0060	2
3	-	85 OHM	10%	.0049	.0050	2 & 4
6	-	85 OHM	10%	.0049	.0050	5 & 7
8	-	85 OHM	10%	.0059	.0050	7 & 9
1	-	100 OHM	10%	.0079	.0080	2
3	-	100 OHM	10%	.0059	.0060	2 & 4

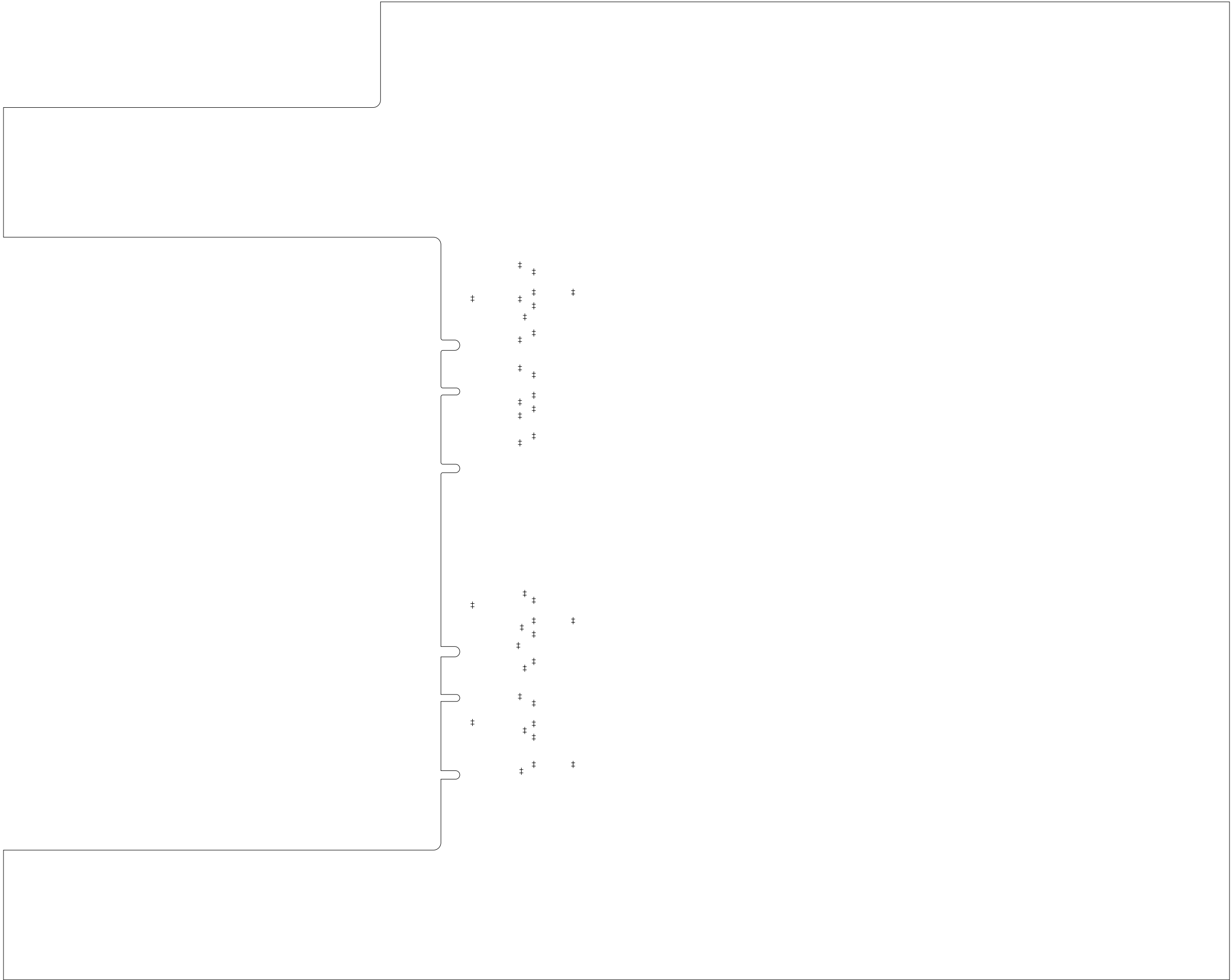
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SH 1 OF 5			
LAYER: FAB DRAWING 1	BRO. NAME: SHI PIN 155/CP3, TIF	AW No: 00000	REV: 000

BACKDRILL: TOP to GND					
ALL UNITS ARE IN MILS					
FIGURE	BD_SIZE	MNC_LAYER	MAX_DEPTH	MFG_STUB	QTY
+	22.059	INNER1	11.7	10.0	74

TOTAL BACKDRILLS: 74

- NOTES:
- MNC_LAYER: MUST-NOT-CUT-LAYER
 - MAX_DEPTH: DEPTH FROM START LAYER TO THE SURFACE OF MUST-NOT-CUT-LAYER
 - MFG_STUB : MANUFACTURING STUB LENGTH



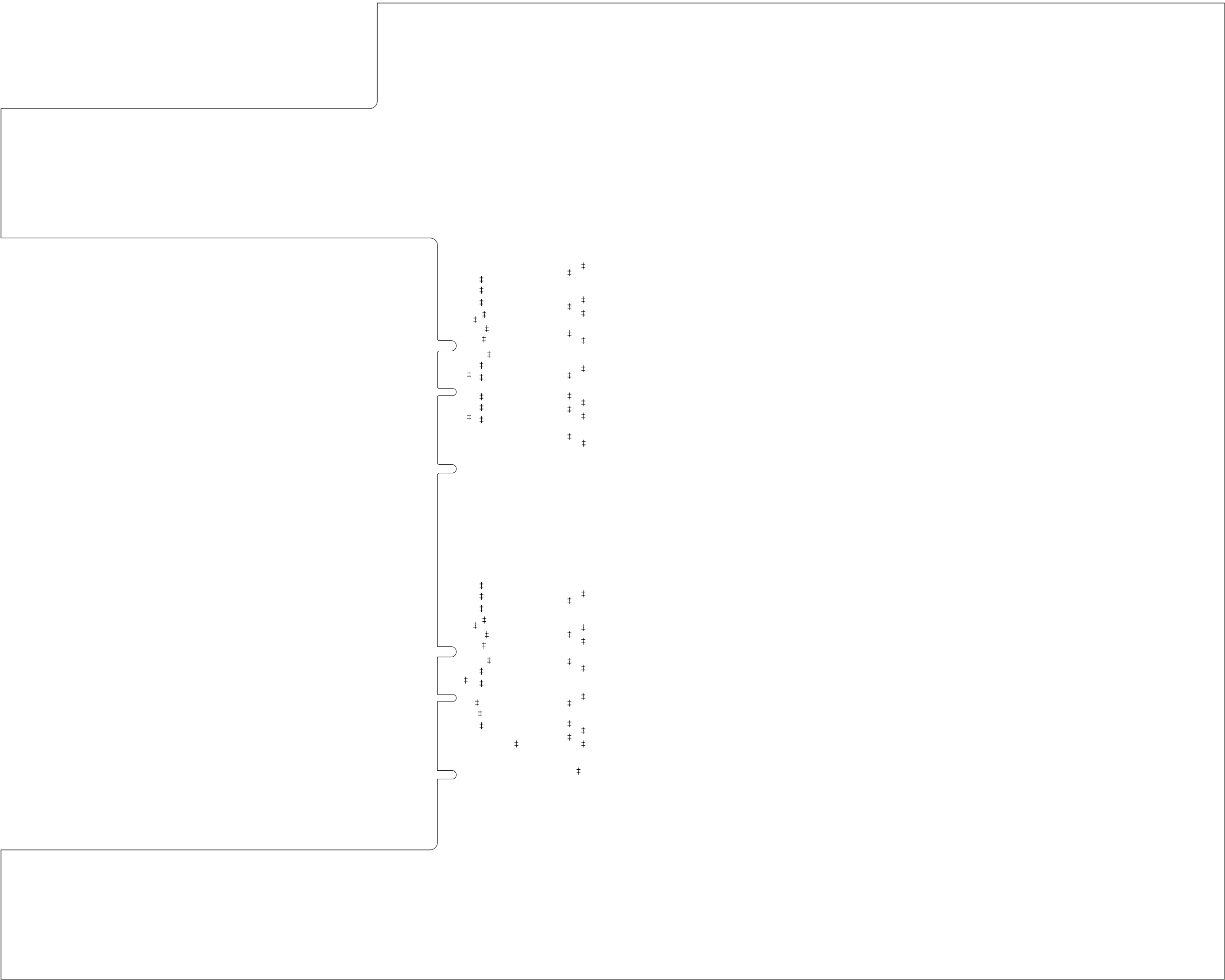
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	LAYER: FAB DRAWING 2	BRD. NAME: PWB_PLN.156_OCP3.TTF DSFF	AW No. QPPP8	REV. X02

BACKDRILL: TOP to GD3					
ALL UNITS ARE IN MILS					
FIGURE	BD_SIZE	MNC_LAYER	MAX_DEPTH	MFG_STUB	QTY
+	22.059	INNER2	61.6	10.0	118

TOTAL BACKDRILLS: 118

NOTES:

- MNC_LAYER: MUST-NOT-CUT-LAYER
- MAX_DEPTH: DEPTH FROM START LAYER TO THE SURFACE OF MUST-NOT-CUT-LAYER
- MFG_STUB : MANUFACTURING STUB LENGTH



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	LAYER: FAB DRAWING 3	BRD. NAME: FWB_PLN.156_UCF3.TTF DSF2
AW No. OPPP8		REV. X02

BACKDRILL: BOTTOM to GD2					
ALL UNITS ARE IN MILS					
FIGURE	BD_SIZE	MNC_LAYER	MAX_DEPTH	MFG_STUB	QTY
+	22.059	INNER1	61.6	10.0	62

TOTAL BACKDRILLS: 62

- NOTES:
- MNC_LAYER: MUST-NOT-CUT-LAYER
 - MAX_DEPTH: DEPTH FROM START LAYER TO THE SURFACE OF MUST-NOT-CUT-LAYER
 - MFG_STUB : MANUFACTURING STUB LENGTH



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	LAYER: FAB DRAWING 4	BRD. NAME: P8B_PLN.156.DCP3.1TF D377	AW No. 0PPP8	REV. X02

BACKDRILL: BOTTOM to GD4					
ALL UNITS ARE IN MILS					
FIGURE	BD_SIZE	MNC_LAYER	MAX_DEPTH	MFG_STUB	QTY
+	22.059	INNER2	11.7	10.0	2

TOTAL BACKDRILLS: 2

NOTES :

- MNC_LAYER: MUST-NOT-CUT-LAYER
- MAX_DEPTH: DEPTH FROM START LAYER TO THE SURFACE OF MUST-NOT-CUT-LAYER
- MFG_STUB : MANUFACTURING STUB LENGTH

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	LAYER: FAB DRAWING 5	BRD. NAME: PWB_PLN.15G.DCP3.TTF 15G