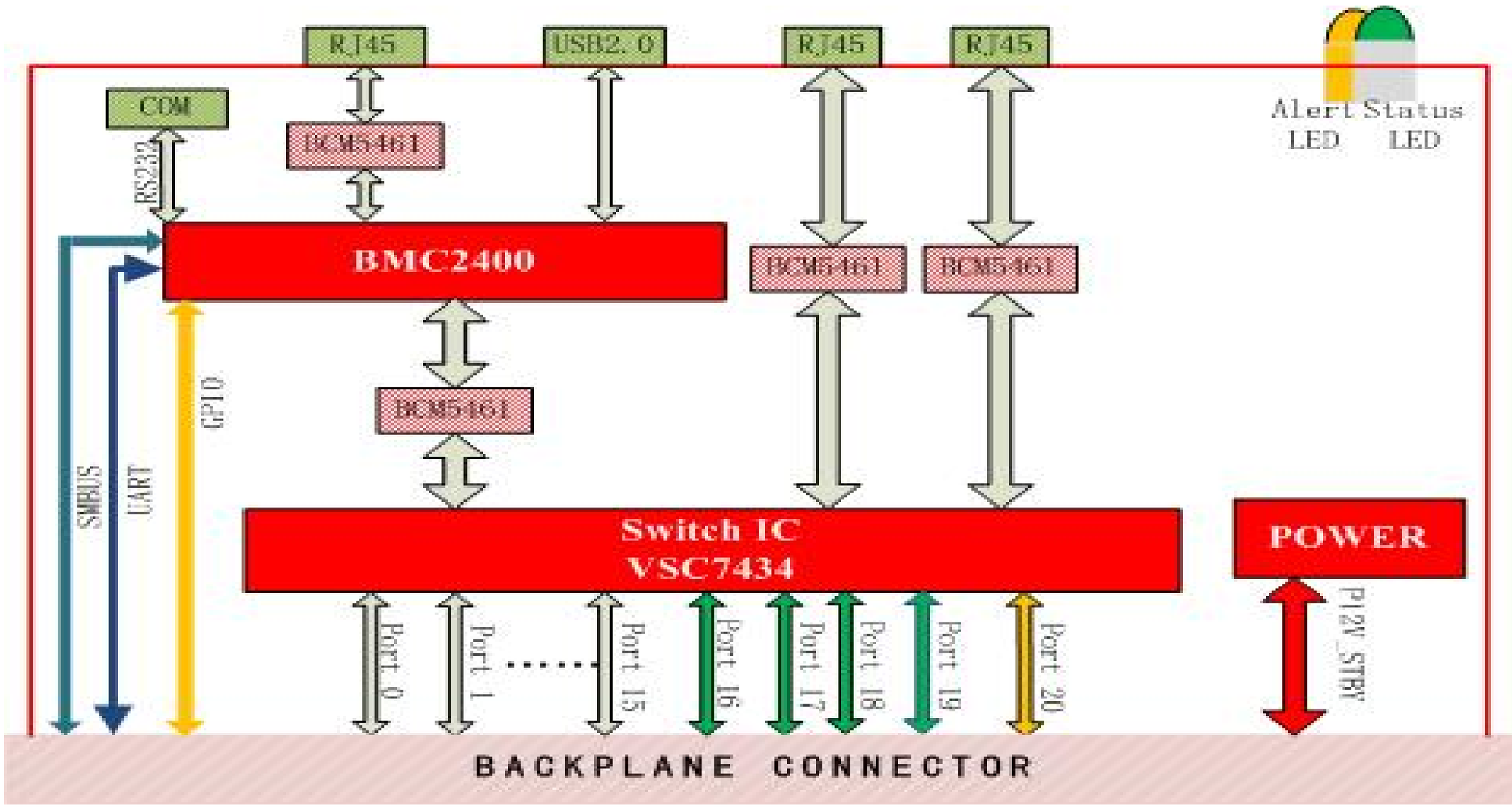
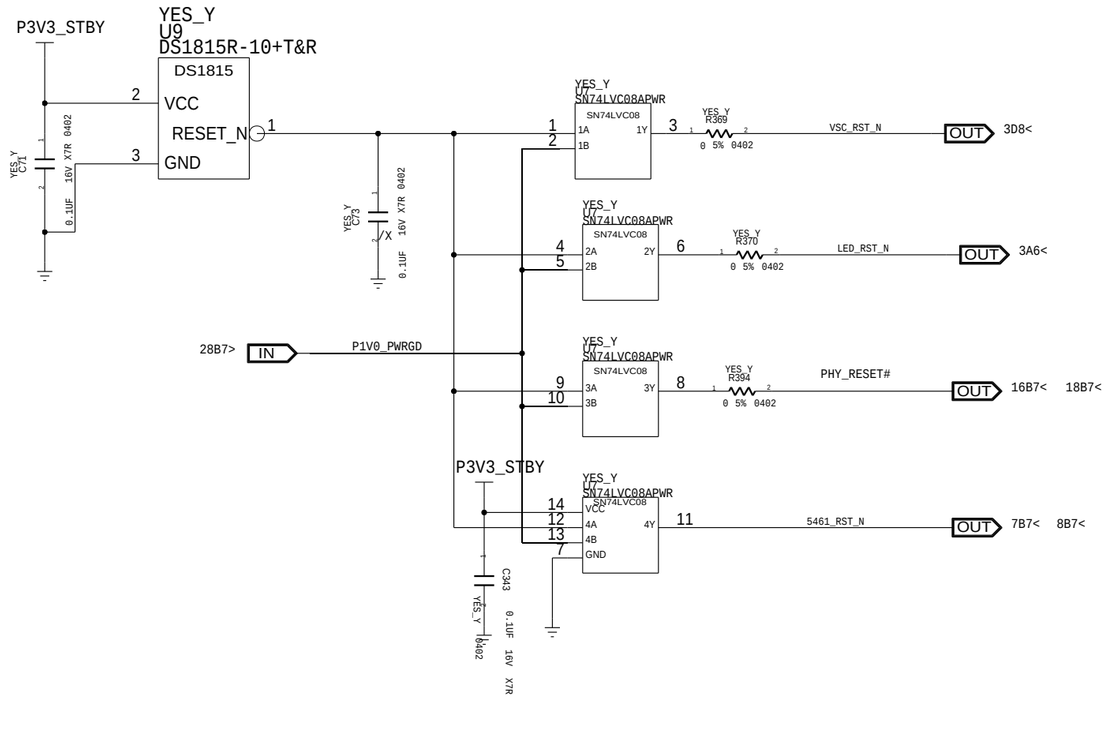
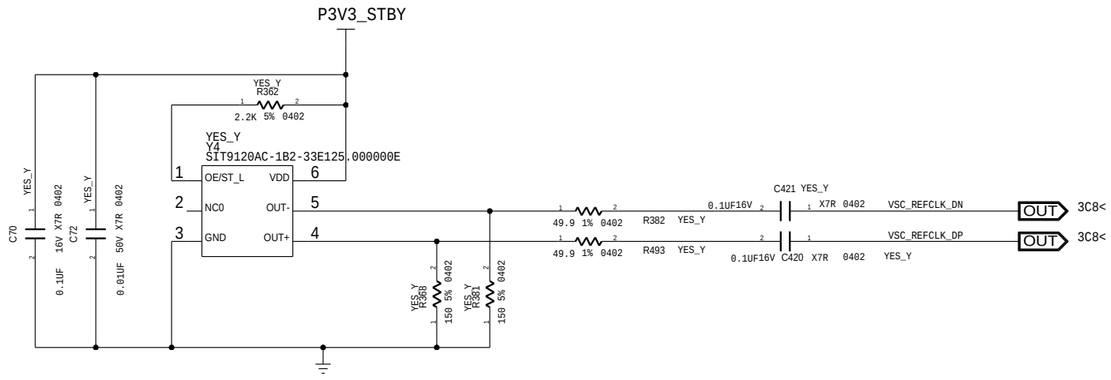


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ENGINEER: <ENGINEER>	TEL: <TEL>		PAGE: 20F29

YES_Y
U5
VSC7434YIH

JTAG_TDO C25
JTAG_TDI C24
JTAG_TMS D22
JTAG_TCK C26
JTAG_NTRST D23
SERDES_REXT_0 AB4
SERDES_REXT_1 AB3
MDC1 B20
MDIO1 A20
MDC0 AC1
MDIO0 AB1
NRESET A19
THERMDA A18
THERMDC B18
HS_TST_P N22
HS_TST* N23
REFCLK_P M26
REFCLK* M25
REFCLK_SEL0 D17
REFCLK_SEL1 C16
REFCLK_SEL2 C17
TEST_MODE1 B17
TESTCFG0 D19
TESTCFG1 C19
TESTCFG2 B19
TESTCFG3 D18

GPIO23_PI_NBE1 D9
GPIO22_PI_NBE0 C9
GPIO21_PI_NCS3 B9
GPIO20_PI_NCS2 A9
GPIO19_PI_NCS1 D8
GPIO18_SPI_NCS3 D15
GPIO17_SPI_NCS2 C15
GPIO16_SPI_NCS1 D16
GPIO15_TWI_SDA B14
GPIO14_TWI_SCL A14
GPIO13_UART_RTS D12
GPIO12_UART_CTS C12
GPIO11_UART_TX B13
GPIO10_UART_RX A13
GPIO9_TIME_TICK_1 B12
GPIO8_TIME_TICK_0 A12
GPIO7_INT1 D11
GPIO6_INT0 C11
GPIO5_LED_DO B11
GPIO4_LED_CLK A11
GPIO3_SIO_LATCH D10
GPIO2_SIO_DI C10
GPIO1_SIO_DO B10
GPIO0_SIO_CLK A10
VCORE_CFG0 C20
VCORE_CFG1 D20
VCORE_BE_EN C18
VCORE_ICE_NEN A17
RESERVED_0 AF2
RCVRD_CLK0 C22
RCVRD_CLK1 C23

1/19

R261 0 5% 0402 SMC_ID
VSC_UART_TX
VSC_UART_RX
LED_DATA
LED_CLK
R356 1K 5% 0402
R357 /X
R355 /X
TP27
RESERVED_[5:4] LEAVE FLOATING

YES_Y
U5
VSC7434YIH

PI_NCS0 B8
PI_NWR A8
PI_NOE A7
PI_NDONE C8
PI_ADDR0 H1
PI_ADDR1 H2
PI_ADDR2 H3
PI_ADDR3 H4
PI_ADDR4 G1
PI_ADDR5 G2
PI_ADDR6 G3
PI_ADDR7 G4
PI_ADDR8 F1
PI_ADDR9 F2
PI_ADDR10 F3
PI_ADDR11 F4
PI_ADDR12 E1
PI_ADDR13 E2
PI_ADDR14 E3
PI_ADDR15 E4
PI_ADDR16 D1
PI_ADDR17 D2
PI_ADDR18 D3
PI_ADDR19 C1
PI_ADDR20 C2
PI_ADDR21 B1
PI_ADDR22 A2
PI_ADDR23 B2
PI_ADDR24 B3
PI_ADDR25 C3

SGMII_NPI_TXP AD2
SGMII_NPI_TXN AD1
SGMII_NPI_RXP AE2
SGMII_NPI_RXN AE1
PI_DATA0 B4
PI_DATA1 A3
PI_DATA2 B5
PI_DATA3 A4
PI_DATA4 B6
PI_DATA5 A5
PI_DATA6 B7
PI_DATA7 A6
PI_DATA8 D4
PI_DATA9 C4
PI_DATA10 D5
PI_DATA11 C5
PI_DATA12 D6
PI_DATA13 C6
PI_DATA14 D7
PI_DATA15 C7
SI_DI A16
SI_DO B15
SI_NCS0 B16
SI_CLK A15

2/19

YES_Y
U5
VSC7434YIH

DDR_DQ0 M1
DDR_DQ1 T3
DDR_DQ2 M3
DDR_DQ3 T1
DDR_DQ4 N2
DDR_DQ5 R4
DDR_DQ6 N4
DDR_DQ7 R2
DDR_DQ8 N1
DDR_DQ9 R3
DDR_DQ10 T2
DDR_DQ11 P3
DDR_DQ12 R1
DDR_DQ13 P4
DDR_DQ14 U1
DDR_DQ15 T4
DDR_ODT M2
DDR_VREF AB2
DDR_REXT Y1
RESERVED_1 AA1
RESERVED_2 AA2
RESERVED_3 AA3
DDR_A0 U2
DDR_A1 P2
DDR_A2 P1
DDR_A3 U3
DDR_A4 N3
DDR_A5 L3
DDR_A6 M4
DDR_A7 J1
DDR_A8 J2
DDR_A9 Y3
DDR_A10 Y4
DDR_A11 J3
DDR_A12 Y2
DDR_A13
DDR_BA0 T4
DDR_BA1 U2
DDR_BA2
DDR_CK P2
DDR_CKN P1
DDR_CKE U3
DDR_NWE
DDR_NRAS
DDR_NCAS
DDR_LDQS J1
DDR_LDQSN J2
DDR_UDQS Y3
DDR_UDQSN Y4
DDR_LDM J3
DDR_UDM Y2

3/19

R378 240 1% 0402

YES_Y
U18
H5PS1G63KFR_S5C

DDR_CK_DP J8
DDR_CK_DN K8
DDR_CKE K2
DDR_CS_N L8
DDR_NRAS K7
DDR_NCAS L7
DDR_NWE K3
CK
CK#
CKE
CS#
RAS#
CAS#
WE#
A0 M8
A1 M3
A2 M7
A3 N2
A4 N8
A5 N3
A6 N7
A7 P2
A8 P8
A9 P3
A10 M2
A11 P7
A12 R2
BA0 L2
BA1 L3
ODT K9
VREF J2
NC/BA2 L1
NC/A14 R3
NC/A15 R7
VDD A1 E1 J9 M9 R1 A9
VDDQ C1 C3 C7 C9 E9 G1 G3 G7 G9
VDDL J1
VSS A3 E3 J3 N1 P9 A7 B2 B8 D2 D8 E7 F2 F8 H2 H8 J7
VSSQ
VSSDL
DQ0 G8
DQ1 G2
DQ2 C8
DQ3 H3
DQ4 H1
DQ5 H9
DQ6 F1
DQ7 F9
DQ8
DQ9 C2
DQ10 D7
DQ11 D3
DQ12 D1
DQ13 D9
DQ14 B1
DQ15 B9
UDM B3
LDM F3
UDQS_L A8
UDQS B7
LDQS_L E8
LDQS F7
NC_0
NC_1
NC/A13 R8
VREF

R379 1K 5% 0402
R380 1K 5% 0402

P1V8_STBY

P3V3_STBY

VSC7434_MDC1 R358 1K 5% 0402
VSC7434_MDIO1 R359 1K 5% 0402
VSC7434_MDC0 R360 1K 5% 0402
VSC7434_MDIO0 R361 1K 5% 0402
SI_CLK R365 330 5% 0402
VSC_REFCLK_DP R366 4.99K 1% 0402
VSC_REFCLK_DN R367 4.99K 1% 0402

P3V3_STBY

YES_Y
U17
M25P128-VMF6TPB

HOLD_L
W_L
S_L
C
D
Q
VCC
VSS
DU0
DU1
DU2
DU3
DU4
DU5
DU6
DU7
SI_CS
SI_CLK
SI_DO
SI_DI

C7 0.1UF
R371 10K 5% 0402
R372 10K 5% 0402
R373 0 5% 0402

P1V8_STBY

RC0402JR-071KL
0402
YR10-00087-100

R364 1K 1% 0603
R377 1K 1% 0603
FB17 BLM18BD102SN1D
DDR_VREF

C135 10U 6.3V X5R 0603
C137 C139 C141 C142 C145 C147 C149 C150 0.1UF 16V X7R 0402
C136 C138 C140 1UF 10V X5R 0402
C154 0.1UF 16V X7R 0402

P3V3_STBY

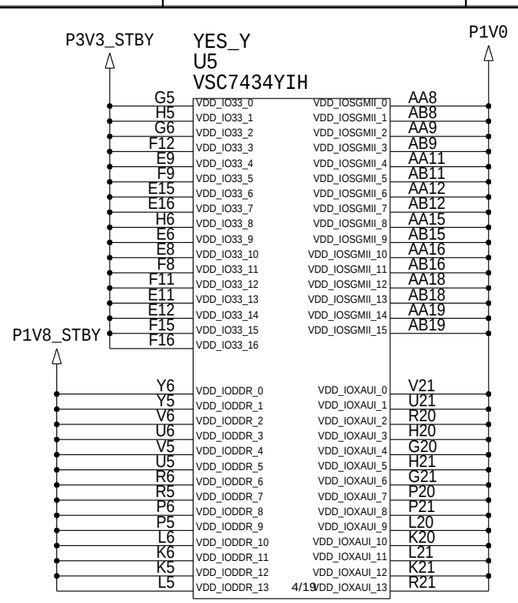
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74HC164PW,118
DSA DSB Q0 Q1 Q2 Q3 GND VCC Q7 Q6 Q5 Q4 MR_L CP

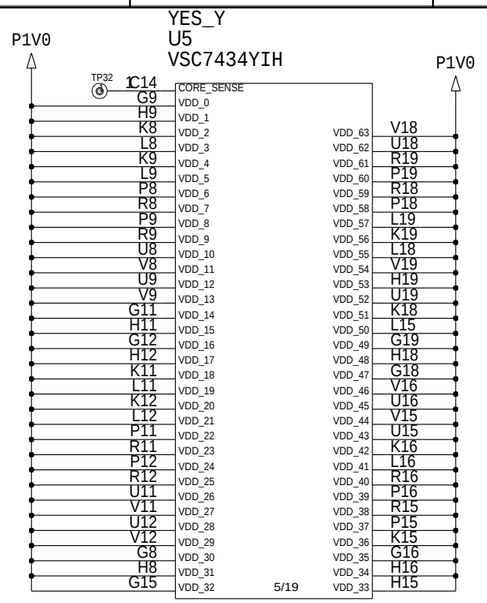
R268 1K 1% 0402
C399 0.1UF 16V X7R 0402
LED_DATA
LED_CLK
LED_RST_N
2D4>

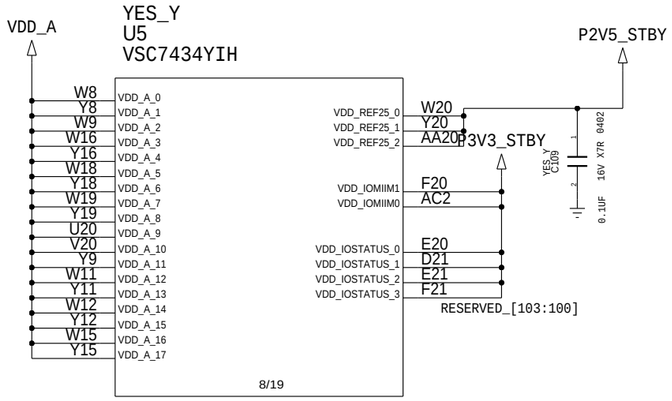
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LED3 LED4 LED7 LED8 LED9 LED10 LED11 LED12
R209 R259 R260 R265 R269 R270 R271 R321 200 1% 0402

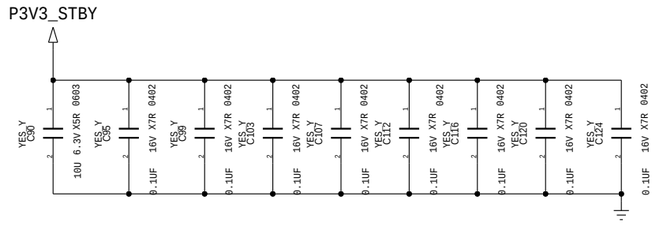
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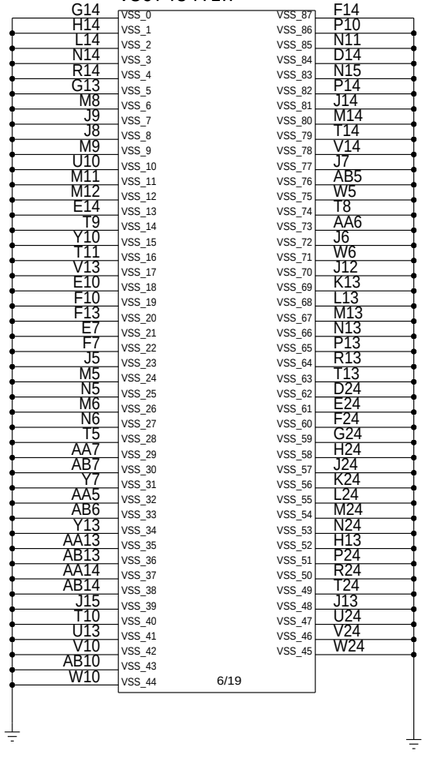
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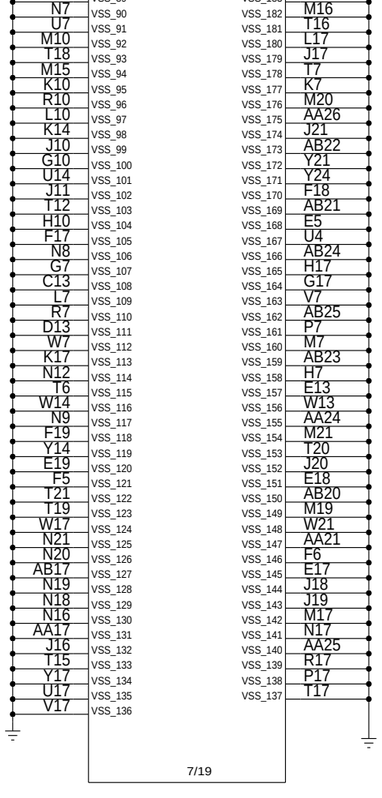


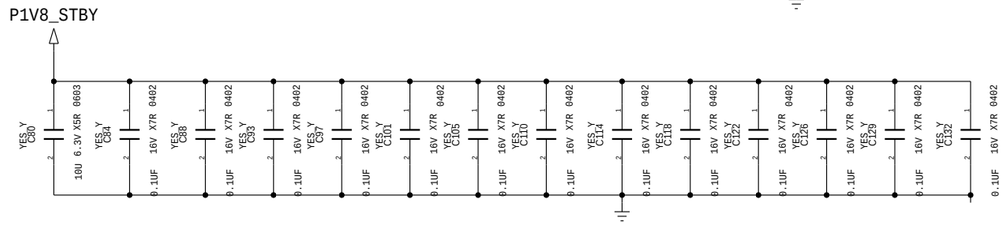


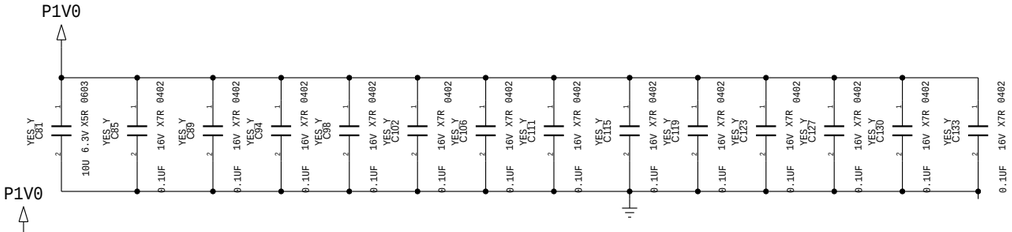


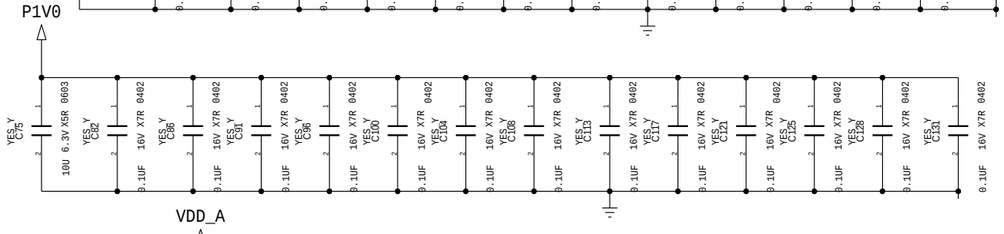


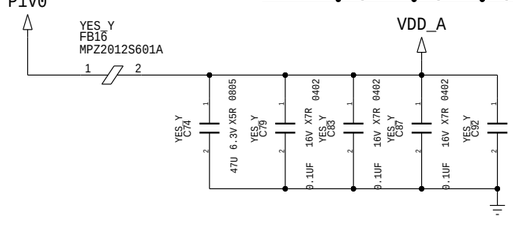












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YES_Y
U5
VSC7434YIH

SGMII0_TXP AC3 SGMII0_TXP OUT 19C7<
SGMII0_TXN AD3 SGMII0_TXN OUT 19C7<
C161 4700PF
SGMII0_RXP AE3 S0_RXP 50V 0402X7R SGMII0_RXP IN 19C7<
SGMII0_RXN AF3 S0_RXN 50V X7R 0402 4700PF SGMII0_RXN IN 19C7<

SGMII1_TXP AC4 SGMII1_TXP OUT 19C8<
SGMII1_TXN AD4 SGMII1_TXN OUT 19C8<
C163 4700PF
SGMII1_RXP AE4 S1_RXP 50V 0402X7R SGMII1_RXP IN 19C8<
SGMII1_RXN AF4 S1_RXN 50V X7R 0402 4700PF SGMII1_RXN IN 19C8<

SGMII2_TXP AC5 SGMII2_TXP OUT 19C7<
SGMII2_TXN AD5 SGMII2_TXN OUT 19C7<
C165 4700PF
SGMII2_RXP AE5 S2_RXP 50V 0402X7R SGMII2_RXP IN 19C7<
SGMII2_RXN AF5 S2_RXN 50V X7R 0402 4700PF SGMII2_RXN IN 19C7<

SGMII3_TXP AC6 SGMII3_TXP OUT 19C8<
SGMII3_TXN AD6 SGMII3_TXN OUT 19C8<
C167 4700PF
SGMII3_RXP AE6 S3_RXP 50V 0402X7R SGMII3_RXP IN 19C8<
9/19 SGMII3_RXN AF6 S3_RXN 50V X7R 0402 4700PF SGMII3_RXN IN 19C8<

YES_Y
U5
VSC7434YIH

SGMII4_TXP AC7 SGMII4_TXP OUT 19D7<
SGMII4_TXN AD7 SGMII4_TXN OUT 19D7<
C178 4700PF
SGMII4_RXP AE7 S4_RXP 50V 0402X7R SGMII4_RXP IN 19D7<
SGMII4_RXN AF7 S4_RXN 50V X7R 0402 4700PF SGMII4_RXN IN 19C7<

SGMII5_TXP AC8 SGMII5_TXP OUT 19D8<
SGMII5_TXN AD8 SGMII5_TXN OUT 19D8<
C180 4700PF
SGMII5_RXP AE8 S5_RXP 50V 0402X7R SGMII5_RXP IN 19D8<
SGMII5_RXN AF8 S5_RXN 50V X7R 0402 4700PF SGMII5_RXN IN 19C8<

SGMII6_TXP AC9 SGMII6_TXP OUT 19D7<
SGMII6_TXN AD9 SGMII6_TXN OUT 19D7<
C182 4700PF
SGMII6_RXP AE9 S6_RXP 50V 0402X7R SGMII6_RXP IN 19D7<
SGMII6_RXN AF9 S6_RXN 50V X7R 0402 4700PF SGMII6_RXN IN 19D7<

SGMII7_TXP AC10 SGMII7_TXP OUT 19D8<
SGMII7_TXN AD10 SGMII7_TXN OUT 19D8<
C184 4700PF
SGMII7_RXP AE10 S7_RXP 50V 0402X7R SGMII7_RXP IN 19D8<
10/19 SGMII7_RXN AF10 S7_RXN 50V X7R 0402 4700PF SGMII7_RXN IN 19D8<

YES_Y
U5
VSC7434YIH

SGMII8_TXP AC11 SGMII8_TXP OUT 19C4<
SGMII8_TXN AD11 SGMII8_TXN OUT 19C4<
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SGMII8_RXP AE11 S8_RXP 50V 0402X7R SGMII8_RXP IN 19C4<
SGMII8_RXN AF11 S8_RXN 50V X7R 0402 4700PF SGMII8_RXN IN 19C4<

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SGMII9_TXN AD12 SGMII9_TXN OUT 19C6<
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SGMII9_RXP AE12 S9_RXP 50V 0402X7R SGMII9_RXP IN 19C6<
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SGMII10_TXN AD13 SGMII10_TXN OUT 19C4<
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SGMII10_RXP AE13 S10_RXP 50V 0402X7R SGMII10_RXP IN 19C4<
SGMII10_RXN AF13 S10_RXN 50V X7R 0402 4700PF SGMII10_RXN IN 19C4<

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SGMII11_TXN AD14 SGMII11_TXN OUT 19C6<
C194 4700PF
SGMII11_RXP AE14 S11_RXP 50V 0402X7R SGMII11_RXP IN 19C6<
11/19 SGMII11_RXN AF14 S11_RXN 50V X7R 0402 4700PF SGMII11_RXN IN 19C6<

YES_Y
U5
VSC7434YIH

SGMII12_TXP AC15 SGMII12_TXP OUT 19D4<
SGMII12_TXN AD15 SGMII12_TXN OUT 19D4<
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SGMII12_RXN AF15 S12_RXN 50V X7R 0402 4700PF SGMII12_RXN IN 19C4<

SGMII13_TXP AC16 SGMII13_TXP OUT 19D6<
SGMII13_TXN AD16 SGMII13_TXN OUT 19D6<
C198 4700PF
SGMII13_RXP AE16 S13_RXP 50V 0402X7R SGMII13_RXP IN 19D6<
SGMII13_RXN AF16 S13_RXN 50V X7R 0402 4700PF SGMII13_RXN IN 19D6<

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SGMII14_TXN AD17 SGMII14_TXN OUT 19D4<
C200 4700PF
SGMII14_RXP AE17 S14_RXP 50V 0402X7R SGMII14_RXP IN 19D4<
SGMII14_RXN AF17 S14_RXN 50V X7R 0402 4700PF SGMII14_RXN IN 19D4<

SGMII15_TXP AC18 SGMII15_TXP OUT 19D6<
SGMII15_TXN AD18 SGMII15_TXN OUT 19D6<
C202 4700PF
SGMII15_RXP AE18 S15_RXP 50V 0402X7R SGMII15_RXP IN 19D6<
12/19 SGMII15_RXN AF18 S15_RXN 50V X7R 0402 4700PF SGMII15_RXN IN 19D6<

YES_Y
U5
VSC7434YIH

SGMII16_TXP AC19 SGMII16_TXP OUT 19C1<
SGMII16_TXN AD19 SGMII16_TXN OUT 19C1<
C170 4700PF
SGMII16_RXP AE19 S16_RXP 50V 0402X7R SGMII16_RXP IN 19C1<
SGMII16_RXN AF19 S16_RXN 50V X7R 0402 4700PF SGMII16_RXN IN 19C1<

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SGMII17_TXN AD20 SGMII17_TXN OUT 19C3<
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SGMII17_RXN AF20 S17_RXN 50V X7R 0402 4700PF SGMII17_RXN IN 19C3<

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SGMII18_TXN AD21 SGMII18_TXN OUT 19C1<
C174 4700PF
SGMII18_RXP AE21 S18_RXP 50V 0402X7R SGMII18_RXP IN 19C1<
SGMII18_RXN AF21 S18_RXN 50V X7R 0402 4700PF SGMII18_RXN IN 19C1<

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SGMII19_TXN AD22 SGMII19_TXN OUT 19C3<
C176 4700PF
SGMII19_RXP AE22 S19_RXP 50V 0402X7R SGMII19_RXP IN 19C3<
13/19 SGMII19_RXN AF22 S19_RXN 50V X7R 0402 4700PF SGMII19_RXN IN 19C3<

YES_Y
U5
VSC7434YIH

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SGMII20_RXP AE23
SGMII20_RXN AF23

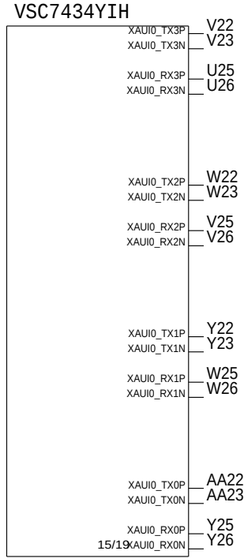
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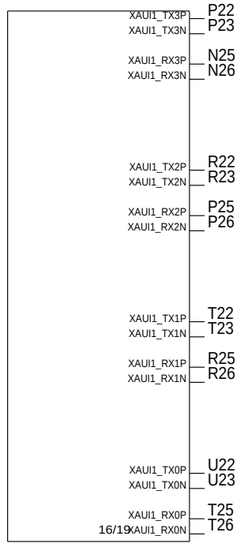
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SGMII23_RXP AD26 S23_RXP SGMII23_RXP IN 18D8>
14/19 SGMII23_RXN AE26 S23_RXN SGMII23_RXN IN 18D8>
YES_Y C419 0.01UF 50V X7R 0402

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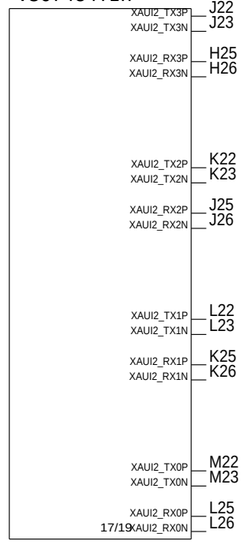
YES_Y
U5
VSC7434YIH



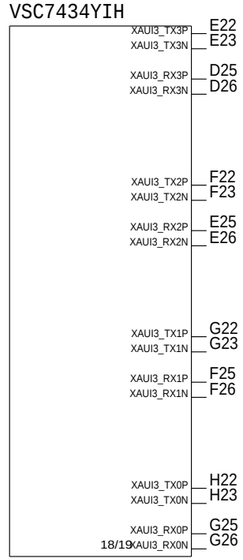
YES_Y
U5
VSC7434YIH



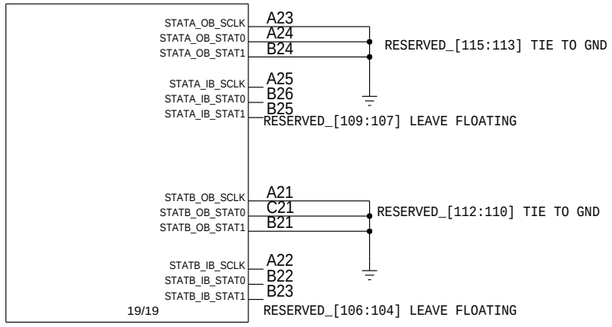
YES_Y
U5
VSC7434YIH



YES_Y
U5
VSC7434YIH



YES_Y
U5
VSC7434YIH



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P3V3_STBY

PHYA:11101

YES_Y
U24
BCM5461SA3KFBG

PHYA4_3 PHYA3_3 PHYA2_3 PHYA1_3 PHYA0_3

SGMII22_RXP SGMII22_RXN SGMII22_TXP SGMII22_TXN

GBE22_TXP GBE22_TXN

0.01UF 50V X7R 0402

SGOUT+ SGOUT- SGIN+ SGIN-

GTXCLK

TXD<3> TXD<2> TXD<1> TXD<0>

TX_EN

RXC RXD<3> RXD<2> RXD<1> RXD<0> RX_DV

LOWPWR_3 RGMIIEN_3

LOWPWR RGMIIEN

FDX HUB ER SLAVE_L|ANEN EN_10B|SD

5461S_ANEN_3

MANMS SPD0 F1000

5461S_SPD_3 5461S_F1000_3

MDIO MDC

XTALI XTALO

CLK125

RESET_L

5461S_RST_N

GND_0 GND_1 GND_2 GND_3

AGND_0 AGND_1 AGND_2

NC_0 NC_1 NC_2 NC_3 NC_4 NC_5

5461 PHY ADDRESS:0X1D

VSC7434_MDIO0 VSC7434_MDC0

YES_Y
Y6
FL2500097

0VDD_3 AVDD_3 XTALVDD BIASVDD PLLVDD1 PLLVDD2 DVDD_3 AVDDL_3 REG_SUP2 REG_SEN2 REG_SUP1 REG_SEN1

BIAS_VDD_3 PLL_VDD1_3 PLL_VDD2_3 DVDD_3 AVDDL_3 REG_SUP_3 REG_SEN2_3 REG_SEN1_3

TRD3+ TRD3- TRD2+ TRD2- TRD1+ TRD1- TRD0+ TRD0-

P2_TRD_3P P2_TRD_3N P2_TRD_2P P2_TRD_2N P2_TRD_1P P2_TRD_1N P2_TRD_0P P2_TRD_0N

LINKSPD2_L_I2C_DISABLE LINKSPD1_L_INTF_SEL0 FDXLED_L|INTF_SEL1 XMTLED_L|FBRUSE_L|AUTODET ACTIVITYLED_L INTR_L|ENERGYDET QUALITY_L|GTXCLK_RXC_DELAY

P2_LINK1_L INTF_SEL0_3 INTF_SEL1_3 AUTODETECT_3 P2_LINK1_ACT P2_LINK1

TRST_L TDI TCK TMS TDO

TEST<0> TEST<1>

2.5V_EN RDAC

PLLGND1 PLLGND2

NC_6 NC_7 NC_8 NC_9 NC_10 NC_11

FB28 BLM18BD102SN1D

SGMII MODE:10

18

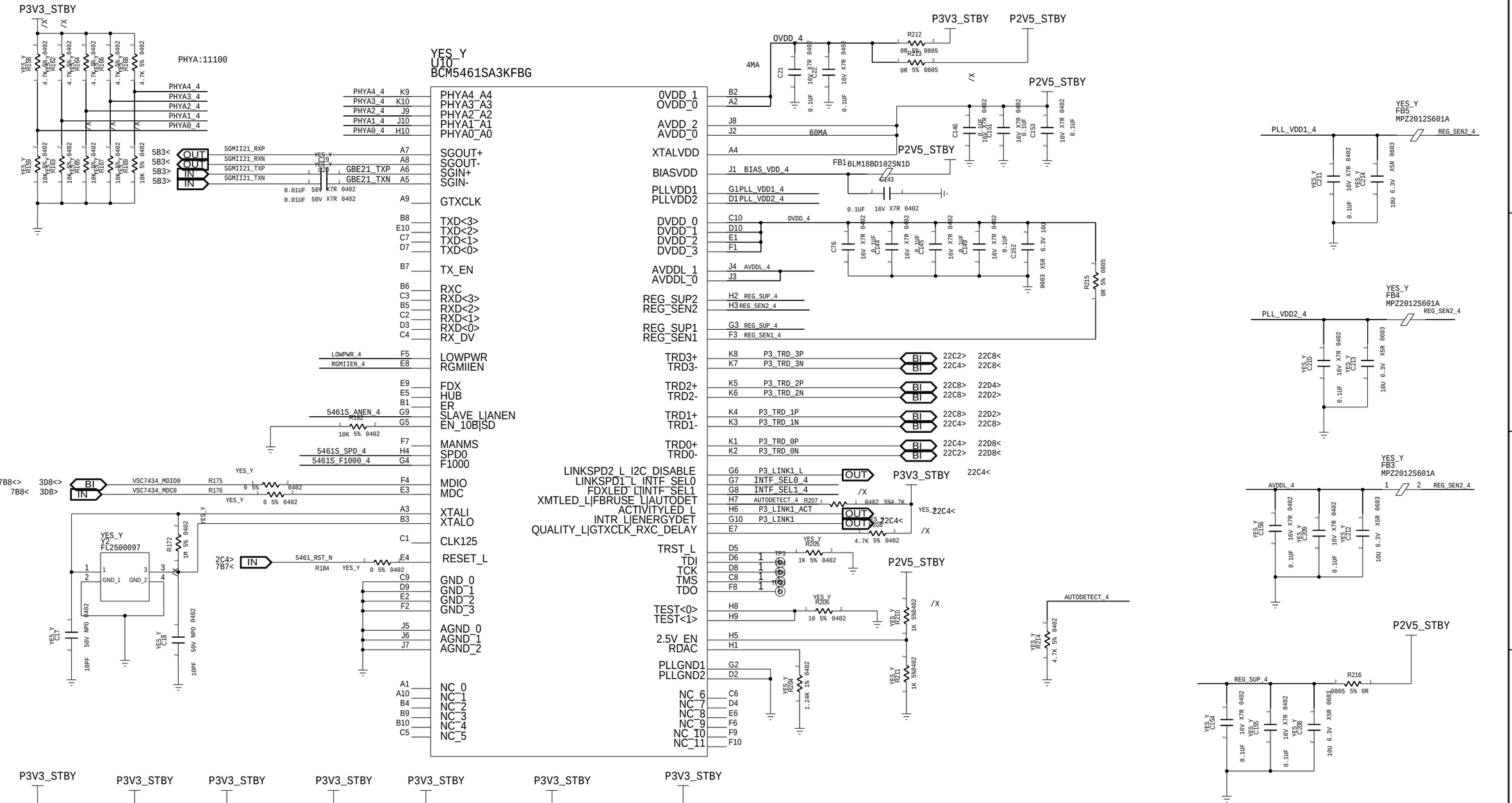
P3V3_STBY P2V5_STBY

YES_Y
FB31
MPZ2012S601A

YES_Y
FB30
MPZ2012S601A

YES_Y
FB29
MPZ2012S601A

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YES_Y
U10
BCM5461SA3KFBG
PHYA:11100
P3V3_STBY
P2V5_STBY
PHYA4_A4
PHYA3_A3
PHYA2_A2
PHYA1_A1
PHYA0_A0
SGOUT+
SGOUT-
SGIN+
SGIN-
GTXCLK
TXD<3>
TXD<2>
TXD<1>
TXD<0>
TX_EN
RXC
RXD<3>
RXD<2>
RXD<1>
RXD<0>
RX_DV
LOWPWR
RGMIIEN
FDX
HUB
ER
SLAVE_L|ANEN
EN_10B|SD
MANMS
SPD0
F1000
MDIO
MDC
XTALI
XTALO
CLK125
RESET_L
GND_0
GND_1
GND_2
GND_3
AGND_0
AGND_1
AGND_2
NC_0
NC_1
NC_2
NC_3
NC_4
NC_5
0VDD_1
0VDD_0
AVDD_2
AVDD_0
XTALVDD
BIASVDD
PLLVDD1
PLLVDD2
DVDD_0
DVDD_1
DVDD_2
DVDD_3
AVDDL_1
AVDDL_0
REG_SUP2
REG_SEN2
REG_SUP1
REG_SEN1
TRD3+
TRD3-
TRD2+
TRD2-
TRD1+
TRD1-
TRD0+
TRD0-
LINKSPD2_L_I2C_DISABLE
LINKSPD1_L_INTF_SEL0
FDXLED_L|INTF_SEL1
XMTLED_L|FBRUSE_L|AUTODET
ACTIVITYLED_L
INTR_L|ENERGYDET
QUALITY_L|GTXCLK_RXC_DELAY
TRST_L
TDI
TCK
TMS
TDO
TEST<0>
TEST<1>
2.5V_EN
RDAC
PLLGND1
PLLGND2
NC_6
NC_7
NC_8
NC_9
NC_10
NC_11
SGMII MODE:10
FB1 BLM18BD102SN1D
FB5 MPZ2012S601A
FB4 MPZ2012S601A
FB3 MPZ2012S601A
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YES_Y
U32
AST2400A1-GP

PCI_Express
1/4

DDR2_DDR3

LPC

I2C

SD_SDIO

GPIO

NOT USED FOR 2400

SD-SDIO USED FOR GPIO FUNCTION

GPIO A4~B3 CAN USED FOR GPIO

TRACE WIDTH >=15MILS

NEW

C268 PLACE NEAR PIN

YES_Y
U27
H5TQ2G63GFR-RDC

128M

BMC

inspur 浪潮	INSPUR (BEIJING) ELECTRONIC INFORMATION INDUSTRY CO.,LTD.	
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0416 CHANGE RPMD[0:2] FROM 101 TO 010

GPIO R0~3 CAN USED FOR GPIO
GPIO S4~5 CAN USED FOR GPIO

YES_Y
U32
AST2400A1-GP

ROMA 0~23
ROMD 0~7

ROMD 8~15 CAN USED FOR GPIO

2/4

BMC NOR_NAND_SPI

SYstem UART

BMC UART

MAC1

MAC2

SYstem SPI

GPIO R4~5 CAN USED FOR GPIO

GPIO E0~7 CAN USED GPIO

GPIO F0~7 CAN USED GPIO

P3V3_STBY

I744

MX25L25735EMI-12G

BMC

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P3V3_STBY

YES_Y Y8 TC48000002

OE/ST_L, GND, OUT, VDD

CLK_48M_USB_IBMC_R

CLK_48M_USB_IBMC

11D6> IN CLK_48M_USB_IBMC AB22

13C4<> IN BMC_RESET1_N W20

OUT 11D6<

YES_Y U32 AST2400A1-GP

CLKIN, SRST#, ENTEST

MISC

VGA: DACB R4, DACG R3, DACR R2; VGAHS_GPIOJ4 T4, VGAVS_GPIOJ5 U2, DDCCLK_GPIOJ6 T2, DDCDAT_GPIOJ7 T1

FAN

14C5<> BI N1_SMC1_GPIO1 V6 GPIOO0_TACH0_VPIG8
14C5<> BI N2_SMC1_GPIO1 Y5 GPIOO1_TACH1_VPIG9
14C5<> BI N3_SMC1_GPIO1 AA4 GPIOO2_TACH2_VPIR0
14C5<> BI N4_SMC1_GPIO1 AB3 GPIOO3_TACH3_VPIR1
14C5<> BI N5_SMC1_GPIO1 W6 GPIOO4_TACH4_VPIR2
14B5<> BI N6_SMC1_GPIO1 AA5 GPIOO5_TACH5_VPIR3
14B5<> BI N7_SMC1_GPIO1 AB4 GPIOO6_TACH6_VPIR4
14B5<> BI N8_SMC1_GPIO1 V7 GPIOO7_TACH7_VPIR5
14B5<> BI N9_SMC1_GPIO1 Y6 GPIOP0_TACH8_VPIR6
14B5<> BI N10_SMC1_GPIO1 AB5 GPIOP1_TACH9_VPIR7
14B5<> BI N11_SMC1_GPIO1 W7 GPIOP2_TACH10_VPIR8
14A5<> BI N12_SMC1_GPIO1 AA6 GPIOP3_TACH11_VPIR9
14A5<> BI N13_SMC1_GPIO1 AB6 GPIOP4_TACH12
14A5<> BI N14_SMC1_GPIO1 Y7 GPIOP5_TACH13
14A5<> BI N15_SMC1_GPIO1 AA7 GPIOP6_TACH14_BMCINT
14A5<> BI N16_SMC1_GPIO1 AB7 GPIOP7_TACH15_FLACK

P3V3_STBY

MASTER SMC: PULL-DOWN ON BACKPLANE

GPION0_PWM0_VPIG0 W4 SMC_ID IN 19D6>
GPION1_PWM1_VPIG1 Y3 BMC_ALERT_LED OUT 13B7<
GPION2_PWM2_VPIG2 AA2 BMC_INIT_OK OUT 13A6<
GPION3_PWM3_VPIG3 AB1
GPION4_PWM4_VPIG4 W5
GPION5_PWM5_VPIG5 Y4
GPION6_PWM6_VPIG6 AA3 23B7< 29C3> IN P12V_PWRGD
GPION7_PWM7_VPIG7 AB2 SW_BMC_SEL_N OUT 19C4<

P5V, P5V_USB1, P5V_USB2

YES_Y U14 TPS2042BDGNR: GND, IN, EN1#, EN2#, OC1#, OUT1, OUT2, OC2#, POWERPAD

P3V3_STBY

/X

BMC_INIT

3C8> IN AD0 L5

ADC

ADC0_GPIW0 L5, ADC1_GPIW1 L4, ADC2_GPIW2 L3, ADC3_GPIW3 L2, ADC4_GPIW4 L1, ADC5_GPIW5 M5, ADC6_GPIW6 M4, ADC7_GPIW7 M3, ADC8_GPIX0 M2, ADC9_GPIX1 M1, ADC10_GPIX2 N5, ADC11_GPIX3 N4, ADC12_GPIX4 N3, ADC13_GPIX5 N2, ADC14_GPIX6 N1, ADC15_GPIX7 P5

HWM_5VSEN

YES_Y R317 1K 1% 0402
YES_Y R318 1K 1% 0402
YES_Y R319 1K 1% 0402
YES_Y R320 1K 1% 0402

USB

USB2AV33 W18, USB2VRES AA20, USB2AVSS Y19
USB2_DP/USB2_HDP AB21 USB2_P0_DP BI 22A5<>
USB2_DN/USB2_HDN AB20 USB2_P0_DN BI 22A2<>

YES_Y R4022 8.2K 1% 0402

USB11_HDP J1 USB11_HDP BI 22C4<>
USB11_HDN J2 USB11_HDN BI 22C6<> 22C1<> 22C6<>

USB11_DP/USB11_HDP2 K4
USB11_DN/USB11_HDN2 K3

P1V2_STBY

YES_Y R306 100 5% 0603

PECI

PECIVDD V19
PECI AA21

YES_Y R305 10K 1% 0402 VCC_PECI

JTAG

11A3<> BI ARM_NTRST E1
11A2< IN ARM_TDI F1
11A2< IN ARM_TMS G2
11A2> IN ARM_TCK F2
11A2> OUT ARM_RTCK G4
11A2< OUT ARM_TDO G3
IN

NTRST, TDI_BDM, TMS, TCK, RTCK, TDO

SGPIO

GPIOJ0_SGPMCK J5 PSU1_PWRON_N OUT 19C7<
GPIOJ1_SGPMLD J4 PSU2_PWRON_N OUT 19C7<
GPIOJ2_SGPMO K5 PSU3_PWRON_N OUT 19C7<
GPIOJ3_SGPMI J3 PSU4_PWRON_N OUT 19C7<
GPIOG0_SGPSCK A14 PSU5_PWRON_N OUT 19C7<
GPIOG1_SGPSLD E13 PSU6_PWRON_N OUT 19C7<
GPIOG2_SGPSI0 D13 NODE3_PRESENT_N IN 14D7<> 19D6>
GPIOG3_SGPSI1 C13 NODE4_PRESENT_N IN 14D7<> 19D4>

GPIO

14D3<> BI N1_SMC1_GPIO2 D6 GPIOA0_MAC1LINK
14D3<> BI N2_SMC1_GPIO2 B5 GPIOA1_MAC2LINK
14D3<> BI N3_SMC1_GPIO2 A4 GPIOA2_TIMER3
14D3<> BI N4_SMC1_GPIO2 E6 GPIOA3_TIMER4
14C3<> BI N5_SMC1_GPIO2 B13 GPIOG4_WDTRST1_OSCCLK
14C3<> BI N6_SMC1_GPIO2 Y21 GPIOG5_WDTRST2_USBCKI
14C3<> BI N7_SMC1_GPIO2 AA22 GPIOG6_FLBUSY#
14C3<> BI N8_SMC1_GPIO2 U18 GPIOG7_FLWP#

3/4

GPIOB4_LPCRST# E19 N9_SMC1_GPIO2 BI 14C3<>
GPIOB5_LPCPD#_LPCSMI# H19 N10_SMC1_GPIO2 BI 14B3<>
GPIOB6_LPCPME# H20 N11_SMC1_GPIO2 BI 14B3<>
GPIOB7_EXTRST#_SPICS1# E18 N12_SMC1_GPIO2 BI 14B3<>
GPIOY0_SIOS3# C21 N13_SMC1_GPIO2 BI 14B3<>
GPIOY1_SIOS5# F20 N14_SMC1_GPIO2 BI 14B3<>
GPIOY2_SIOPWREQ# G20 N15_SMC1_GPIO2 BI 14A3<>
GPIOY3_SIOONCTRL# K20 N16_SMC1_GPIO2 BI 14A3<>

P5V

YES_Y R1 82K 1% 0402
HWM_5VSEN
YES_Y R2 1K 1% 0402
YES_Y C1 0.1UF 16V X7R 0402

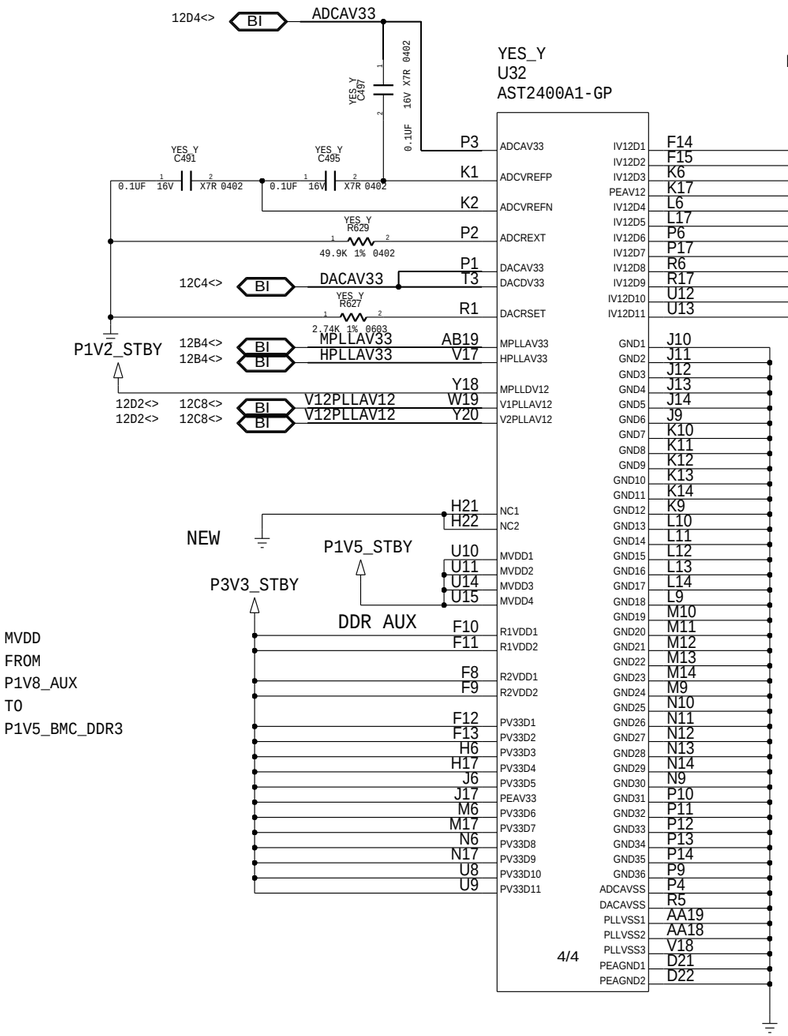
P3V3_STBY

11B6<> BI
ARM_NTRST, ARM_TDI, ARM_TMS, ARM_TCK, ARM_RTCK, ARM_TDO
11B6< IN, 11B6< IN, 11B6> OUT, 11B6> OUT, 11B6< IN

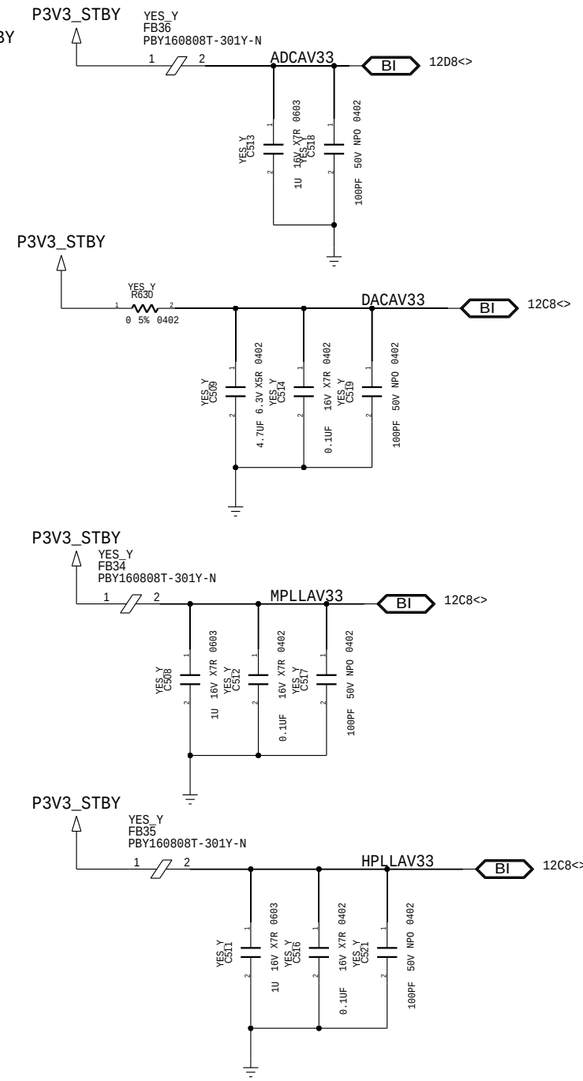
YES_Y R614 47K 5% 0402
YES_Y C405 1U 16V X7R 0603
YES_Y R615 10K 1% 0402

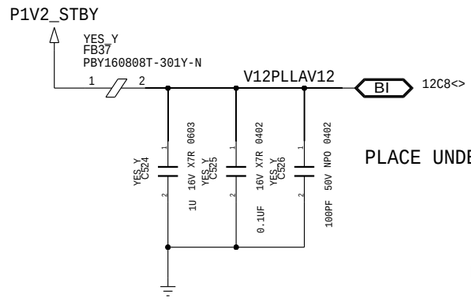
BMC

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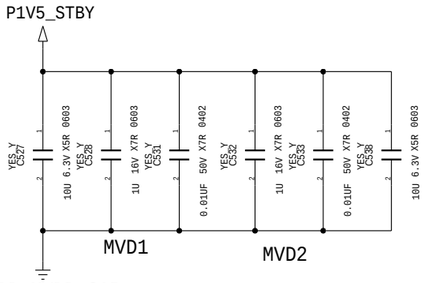


MVDD
FROM
P1V8_AUX
TO
P1V5_BMC_DDR3





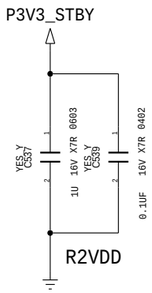
PLACE UNDER BGA

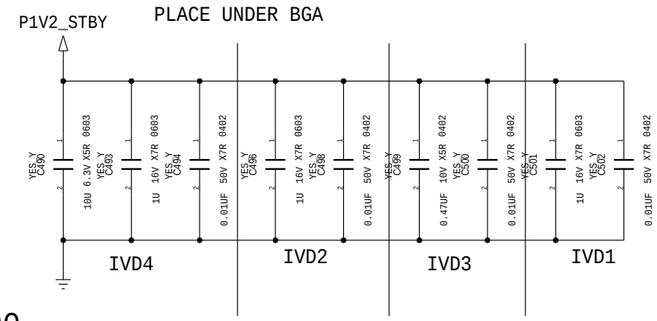


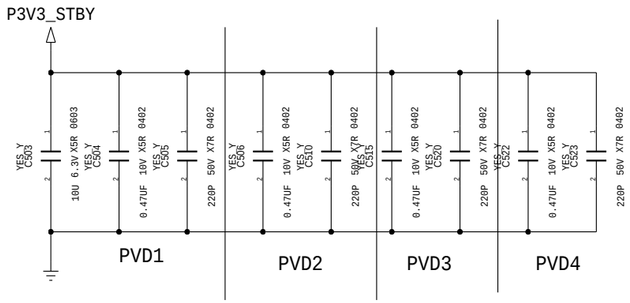
0416 ADD MVDD CAP
PLACE UNDER BGA,
AT LEAST 5 POWER VIAS(18 MIL)

DDR AUX PLACE UNDER BGA

DELETE P1V8 PCIEA

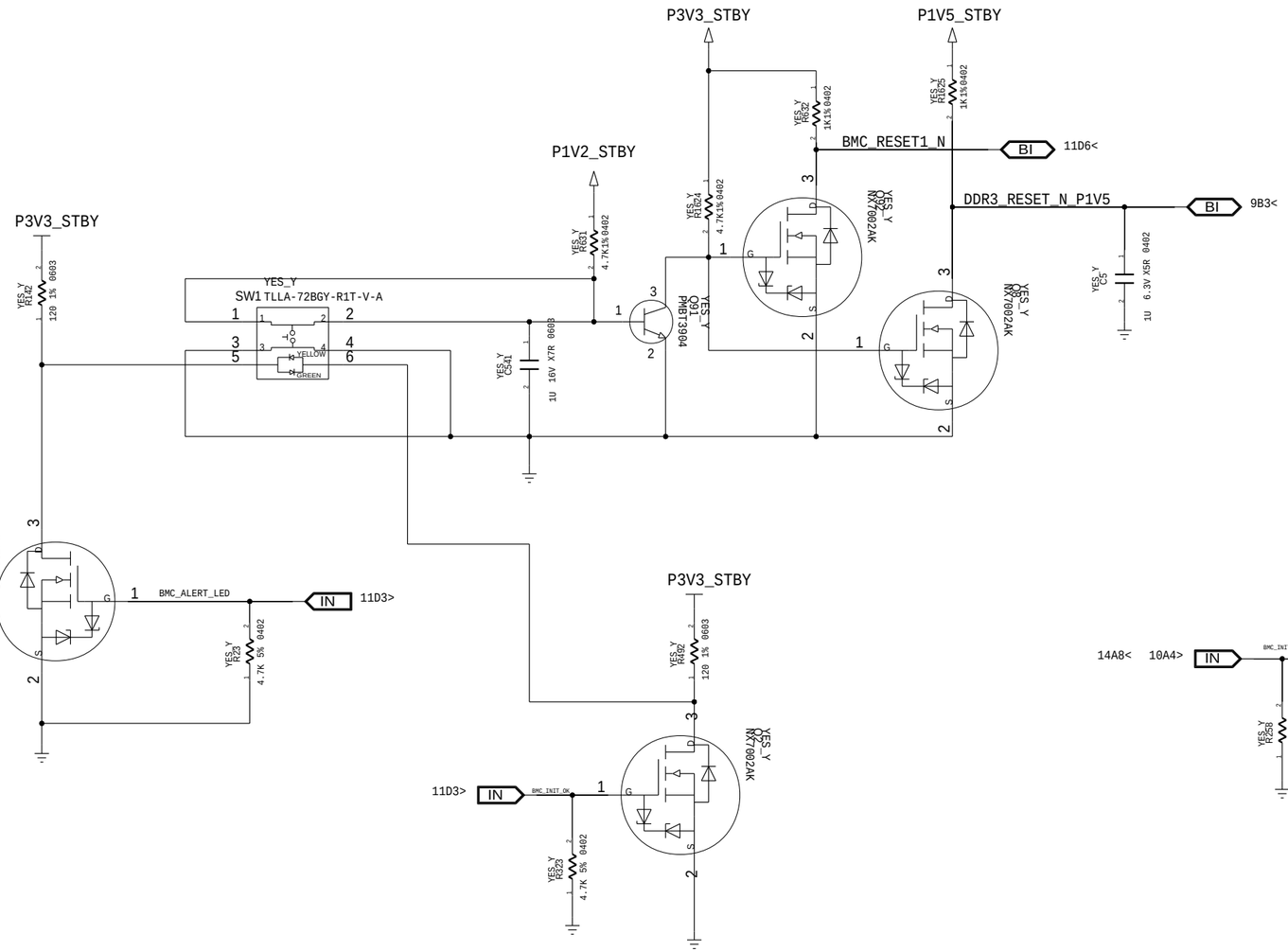


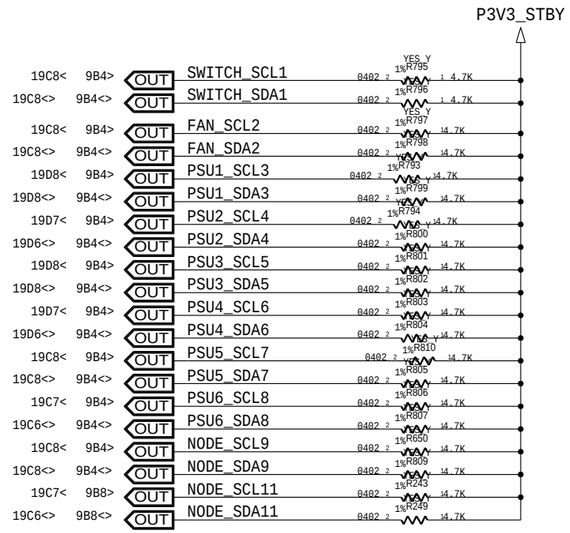




2400

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ADD DDR3 SDRAM RESET SIGNAL FOR DDR3 SDRAM

BMC

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P3V3_STBY

YES_Y	R26	NODE1_PRESENT_N	BI	19D6>	9A4<
YES_Y	R27	NODE2_PRESENT_N	BI	19D4>	9A4<
YES_Y	R28	NODE3_PRESENT_N	BI	19D6>	11B3<
YES_Y	R31	NODE4_PRESENT_N	BI	19D4>	11B3<
YES_Y	R36	NODE5_PRESENT_N	BI	19D6>	9B8<
YES_Y	R37	NODE6_PRESENT_N	BI	19D4>	9B8<
YES_Y	R38	NODE7_PRESENT_N	BI	19D6>	9B8<
YES_Y	R39	NODE8_PRESENT_N	BI	19D4>	9B8<
YES_Y	R40	NODE9_PRESENT_N	BI	19C6>	9B8<
YES_Y	R42	NODE10_PRESENT_N	BI	19C4>	9B8<
YES_Y	R43	NODE11_PRESENT_N	BI	19C6>	9B8<
YES_Y	R75	NODE12_PRESENT_N	BI	19C4>	9B8<
YES_Y	R97	NODE13_PRESENT_N	BI	19C6>	9B8<
YES_Y	R134	NODE14_PRESENT_N	BI	19C4>	9B8<
YES_Y	R149	NODE15_PRESENT_N	BI	19C6>	9B8<
YES_Y	R102	NODE16_PRESENT_N	BI	19C4>	9B8<
YES_Y	R154	SW1_PRESENT_N	BI	19C8>	9A8<
YES_Y	R155	SW2_PRESENT_N	BI	19C8>	9A8<
YES_Y	R156	SW3_PRESENT_N	BI	9A8<	19C6<
YES_Y	R157	SW4_PRESENT_N	BI	9A8<	19C6<
YES_Y	R234	SW1_INT1_N	BI	19C3>	10C7<
YES_Y	R238	PSU1_ALERT_N	BI	19C3>	10B7<
YES_Y	R242	PSU2_ALERT_N	BI	19C3>	10B7<
YES_Y	R235	SW2_INT1_N	BI	10C4<	19C1<
YES_Y	R239	PSU3_ALERT_N	BI	19C3>	10B7<
YES_Y	R244	PSU4_ALERT_N	BI	19C3>	10B7<
YES_Y	R236	SW3_INT1_N	BI	19C3>	10B7<
YES_Y	R240	PSU5_ALERT_N	BI	19C1>	10B4<
YES_Y	R246	PSU6_ALERT_N	BI	19C1>	10B4<
YES_Y	R237	SW4_INT1_N	BI	19C1>	10B4<
YES_Y	R241	FAN1_ALERT_N	BI	19C1>	10B4<
YES_Y	R248	FAN2_ALERT_N	BI	19C1>	10B4<
	R647	BMC_SALT1	BI	9A4<>	
	R670	BMC_SALT2	BI	9A4<>	

All resistors: 4.7K 5% 0402

YES_Y R257 1K 1% 0402 BMC_INIT_OK_N OUT 13B2>

13B4< 10A4> IN BMC_INIT 1 G Q1 RX7002AK YES_Y 3 D S 2

GPIO

P3V3_STBY

YES_Y	R250	PSU1_PRST_N	OUT	19C6>	10A7<
YES_Y	R251	PSU2_PRST_N	OUT	19C6>	10A7<
YES_Y	R252	PSU3_PRST_N	OUT	19C6>	10A7<
YES_Y	R253	PSU4_PRST_N	OUT	19C6>	10A7<
YES_Y	R254	PSU5_PRST_N	OUT	19C6>	10A4<
YES_Y	R255	PSU6_PRST_N	OUT	19C6>	10A4<
YES_Y	R256	NODE_SMC1_INT_N	OUT	10A4<	19C6<

All resistors: 4.7K 5% 0402

11D6<>	BI	N1_SMC1_GPIO1	Q7 PMBT3906 YES_Y	N1_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N2_SMC1_GPIO1	Q9 PMBT3906 YES_Y	N2_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N3_SMC1_GPIO1	Q10 PMBT3906 YES_Y	N3_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N4_SMC1_GPIO1	Q11 PMBT3906 YES_Y	N4_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N5_SMC1_GPIO1	Q12 PMBT3906 YES_Y	N5_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N6_SMC1_GPIO1	Q13 PMBT3906 YES_Y	N6_SMC1_GPIO1_Q	OUT 19D3<>
11D6<>	BI	N7_SMC1_GPIO1	Q14 PMBT3906 YES_Y	N7_SMC1_GPIO1_Q	OUT 19D3<>
11C6<>	BI	N8_SMC1_GPIO1	Q15 PMBT3906 YES_Y	N8_SMC1_GPIO1_Q	OUT 19D3<>
11C6<>	BI	N9_SMC1_GPIO1	Q16 PMBT3906 YES_Y	N9_SMC1_GPIO1_Q	OUT 19D3<>
11C6<>	BI	N10_SMC1_GPIO1	Q17 PMBT3906 YES_Y	N10_SMC1_GPIO1_Q	OUT 19D3<>
11C6<>	BI	N11_SMC1_GPIO1	Q18 PMBT3906 YES_Y	N11_SMC1_GPIO1_Q	OUT 19C3<>
11C6<>	BI	N12_SMC1_GPIO1	Q19 PMBT3906 YES_Y	N12_SMC1_GPIO1_Q	OUT 19C3<>
11C6<>	BI	N13_SMC1_GPIO1	Q20 PMBT3906 YES_Y	N13_SMC1_GPIO1_Q	OUT 19C3<>
11C6<>	BI	N14_SMC1_GPIO1	Q21 PMBT3906 YES_Y	N14_SMC1_GPIO1_Q	OUT 19C3<>
11C6<>	BI	N15_SMC1_GPIO1	Q22 PMBT3906 YES_Y	N15_SMC1_GPIO1_Q	OUT 19C3<>
11C6<>	BI	N16_SMC1_GPIO1	Q23 PMBT3906 YES_Y	N16_SMC1_GPIO1_Q	OUT 19C3<>

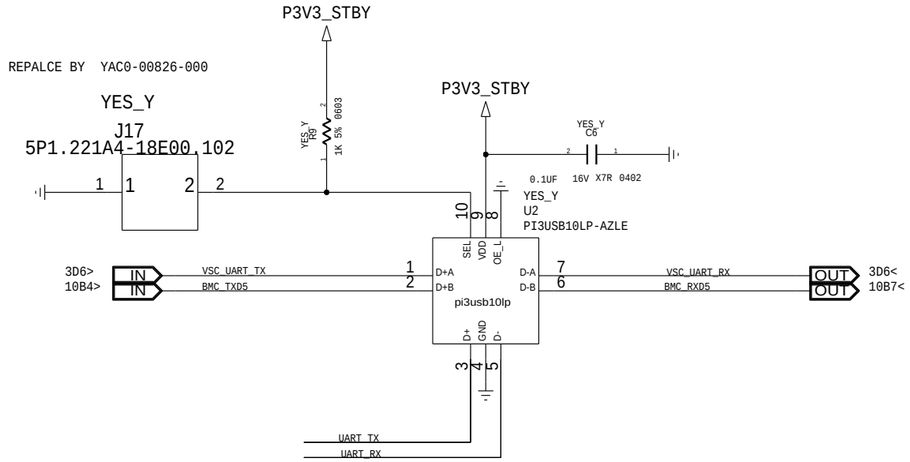
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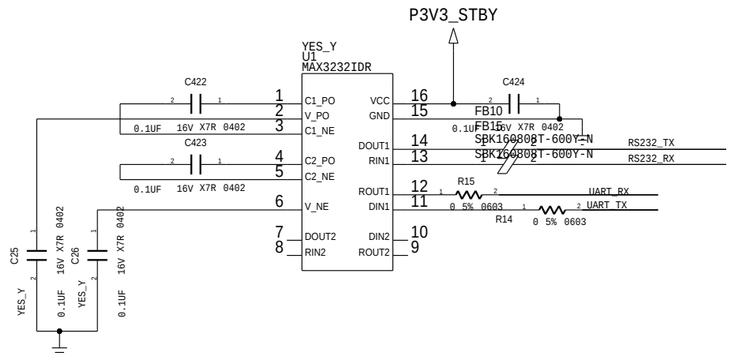
11B6<>	BI	N1_SMC1_GPIO2	Q24 PMBT3906 YES_Y	N1_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N2_SMC1_GPIO2	Q25 PMBT3906 YES_Y	N2_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N3_SMC1_GPIO2	Q26 PMBT3906 YES_Y	N3_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N4_SMC1_GPIO2	Q27 PMBT3906 YES_Y	N4_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N5_SMC1_GPIO2	Q28 PMBT3906 YES_Y	N5_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N6_SMC1_GPIO2	Q29 PMBT3906 YES_Y	N6_SMC1_GPIO2_Q	OUT 19D1<>
11B6<>	BI	N7_SMC1_GPIO2	Q30 PMBT3906 YES_Y	N7_SMC1_GPIO2_Q	OUT 19D1<>
11A6<>	BI	N8_SMC1_GPIO2	Q31 PMBT3906 YES_Y	N8_SMC1_GPIO2_Q	OUT 19D1<>
11B3<>	BI	N9_SMC1_GPIO2	Q32 PMBT3906 YES_Y	N9_SMC1_GPIO2_Q	OUT 19D1<>
11B3<>	BI	N10_SMC1_GPIO2	Q33 PMBT3906 YES_Y	N10_SMC1_GPIO2_Q	OUT 19D1<>
11B3<>	BI	N11_SMC1_GPIO2	Q34 PMBT3906 YES_Y	N11_SMC1_GPIO2_Q	OUT 19C1<>
11B3<>	BI	N12_SMC1_GPIO2	Q35 PMBT3906 YES_Y	N12_SMC1_GPIO2_Q	OUT 19C1<>
11B3<>	BI	N13_SMC1_GPIO2	Q36 PMBT3906 YES_Y	N13_SMC1_GPIO2_Q	OUT 19C1<>
11B3<>	BI	N14_SMC1_GPIO2	Q37 PMBT3906 YES_Y	N14_SMC1_GPIO2_Q	OUT 19C1<>
11A3<>	BI	N15_SMC1_GPIO2	Q38 PMBT3906 YES_Y	N15_SMC1_GPIO2_Q	OUT 19C1<>
11A3<>	BI	N16_SMC1_GPIO2	Q39 PMBT3906 YES_Y	N16_SMC1_GPIO2_Q	OUT 19C1<>

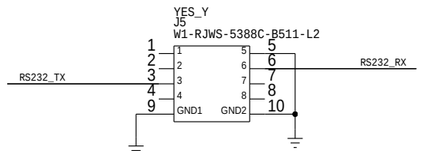
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BMC_COM

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P3V3_STBY

PHYA:11001

PHYA4
PHYA3
PHYA2
PHYA1
PHYA0

5461 PHY ADDRESS:0X1D

YES_Y
U6
BCM5461SA3KFBG

PHYA4	K9	PHYA4_A4	
PHYA3	K10	PHYA3_A3	
PHYA2	J9	PHYA2_A2	
PHYA1	J10	PHYA1_A1	
PHYA0	H10	PHYA0_A0	
	A7	SGOUT+	
	A8	SGOUT-	
	A6	SGIN+	
	A5	SGIN-	
10B3> IN MAC2_GTXCLK	A9	GTXCLK	
10A3> IN MAC2_TXD3	B8	TXD<3>	
10A3> IN MAC2_TXD2	E10	TXD<2>	
10A3> IN MAC2_TXD1	C7	TXD<1>	
10B3> IN MAC2_TXD0	D7	TXD<0>	
10B3> IN MAC2_TXD_EN	B7	TX_EN	
10B7< OUT MAC2_RXC (R82 33 5% 0402) MAC2_RXC_R	B6	RXC	
10A7< OUT MAC2_RXD3 (33 5% 0402) MAC2_RXD3_R	C3	RXD<3>	
10A7< OUT MAC2_RXD2 (33 5% 0402) MAC2_RXD2_R	B5	RXD<2>	
10A7< OUT MAC2_RXD1 (33 5% 0402) MAC2_RXD1_R	C2	RXD<1>	
10B7< OUT MAC2_RXD0 (33 5% 0402) MAC2_RXD0_R	D3	RXD<0>	
10B7< OUT MAC2_RX_DV (33 5% 0402) MAC2_RX_DV_R	C4	RX_DV	
LOWPWR	F5	LOWPWR	
RGMIIEN	E8	RGMIIEN	
	E9	FDX	
	E5	HUB	
	B1	ER	
5461S_ANEN (R88 10K 5% 0402)	G9	SLAVE_L|ANEN	
	G5	EN_10B|SD	
	F7	MANMS	
5461S_SPD	H4	SPD0	
5461S_F1000	G4	F1000	
10A3<> BI MDIO_5461_2	F4	MDIO	
10A3> IN MDC_5461_2	E3	MDC	
	A3	XTALI	
	B3	XTALO	
	C1	CLK125	
IN PHY_RESET# (R56 YES_Y 0 5% 0402)	E4	RESET_L	
	C9	GND_0	
	D9	GND_1	
	E2	GND_2	
	F2	GND_3	
	J5	AGND_0	
	J6	AGND_1	
	J7	AGND_2	
	A1	NC_0	
	A10	NC_1	
	B4	NC_2	
	B9	NC_3	
	B10	NC_4	
	C5	NC_5	

	B2	0VDD_1	
	A2	OVDD_0	
	J8	AVDD_2	
	J2	AVDD_0	
	A4	XTALVDD	
	J1	BIAS_VDD	
	G1	PLLVDD1	PLL_VDD1
	D1	PLLVDD2	PLL_VDD2
	C10	DVDD_0	
	D10	DVDD_1	
	E1	DVDD_2	
	F1	DVDD_3	
	J4	AVDDL_1	AVDDL
	J3	AVDDL_0	
	H2	REG_SUP2	REG_SUP
	H3	REG_SEN2	REG_SEN2
	G3	REG_SUP1	REG_SUP
	F3	REG_SEN1	REG_SEN1
	K8	TRD3+	P1_TRD_3P (BI 17C4> 17C7<)
	K7	TRD3-	P1_TRD_3N (BI 17C4> 17C7<)
	K5	TRD2+	P1_TRD_2P (BI 17C4> 17C7>)
	K6	TRD2-	P1_TRD_2N (BI 17C4> 17C7>)
	K4	TRD1+	P1_TRD_1P (BI 17D4> 17D7>)
	K3	TRD1-	P1_TRD_1N (BI 17D4> 17D7>)
	K1	TRD0+	P1_TRD_0P (BI 17D4> 17D7<)
	K2	TRD0-	P1_TRD_0N (BI 17D4> 17D7<)
	G6	LINKSPD2_L_I2C_DISABLE	P1_LINK1_L (OUT 17C4<)
	G7	LINKSPD1_L_INTF_SEL0	INTF_SEL0
	G8	FDXLED_L|INTF_SEL1	INTF_SEL1
	H7	XMTLED_L|FBRUSE_L|AUTODET	AUTODETECT (R193 0402 5%4.7K)
	H6	ACTIVITYLED_L	P1_LINK1_ACT (OUT 17C4<)
	G10	INTR_L|ENERGYDET	(YES_Y R194 4.7K 5% 0402)
	E7	QUALITY_L|GTXCLK_RXC_DELAY	
	D5	TRST_L	(YES_Y R59 1K 5% 0402)
	D6	TDI	TP6
	D8	TCK	
	C8	TMS	
	F8	TDO	
	H8	TEST<0>	(YES_Y R60 10 5% 0402)
	H9	TEST<1>	
	H5	2.5V_EN	
	H1	RDAC	
	G2	PLLGND1	
	D2	PLLGND2	
	C6	NC_6	
	D4	NC_7	
	E6	NC_8	
	F6	NC_9	
	F9	NC_10	
	F10	NC_11	

P3V3_STBY P2V5_STBY 0VDD 4MA 60MA

R197 0R 5% 0805
R198 0R 5% 0805

C168 10U 6.3V X5R 0603
C169 0.1UF 16V X7R 0402

FB38 BLM18BD102SN1D
C29 0.1UF 16V X7R 0402

C32 10U 6.3V X5R 0603
C34 0.1UF 16V X7R 0402
C36 0.1UF 16V X7R 0402

C28 0.1UF 16V X7R 0402
C30 0.1UF 16V X7R 0402
C31 0.1UF 16V X7R 0402
C33 0.1UF 16V X7R 0402
C35 10U 6.3V X5R 0603

R84 0R 5% 0805

YES_Y FB7 MPZ2012S601A
PLL_VDD1 REG_SEN2
C147 0.1UF 16V X7R 0402
C148 10U 6.3V X5R 0603

YES_Y FB2 MPZ2012S601A
PLL_VDD2 REG_SEN2
C150 0.1UF 16V X7R 0402
C151 10U 6.3V X5R 0603

YES_Y FB8 MPZ2012S601A
AVDDL REG_SEN2
C43 0.1UF 16V X7R 0402
C45 0.1UF 16V X7R 0402
C46 10U 6.3V X5R 0603

AUTODETECT
R491 4.7K 5% 0402

REG_SUP
R95 0805 5% 0R
C41 0.1UF 16V X7R 0402
C42 0.1UF 16V X7R 0402
C44 10U 6.3V X5R 0603

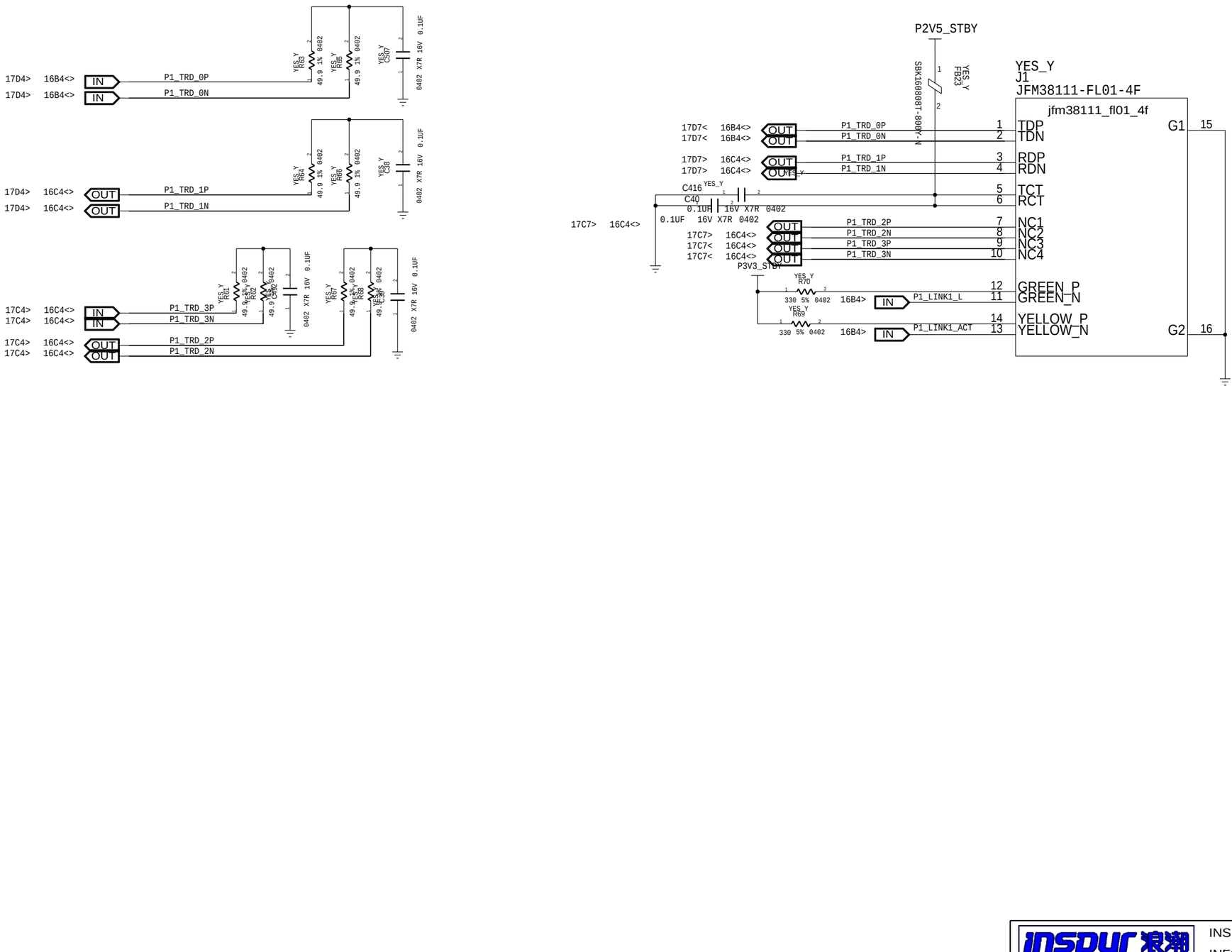
YES_Y
Y1
FC2500097
GND_1 GND_2
R191 1M 5% 0402
C23 10PF 50V NPO 0402
C24 10PF 50V NPO 0402

P3V3_STBY
R190 10K 5% 0402 5461S_F1000
R187 4.7K 5% 0402 5461S_SPD
R189 10K 5% 0402 5461S_ANEN
R54 4.7K 5% 0402 4.INTF_SEL1
R57 4.7K 5% 0402 INTF_SEL0
R89 4.7K 5% 0402 LOWPWR
R91 4.7K 5% 0402 RGMIIEN
R188 4.7K 5% 0402
R55 10K 5% 0402
R58 10K 5% 0402
R90 0 5% 0402

RGMII MODE:00

P3V3_STBY
R458 0402 5% 4.7K MDC_5461_2
R457 0402 5% 4.7K MDIO_5461_2

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P3V3_STBY

PHYA:11110

YES_Y
U4
BCM5461SA3KFBG

P3V3_STBY P2V5_STBY

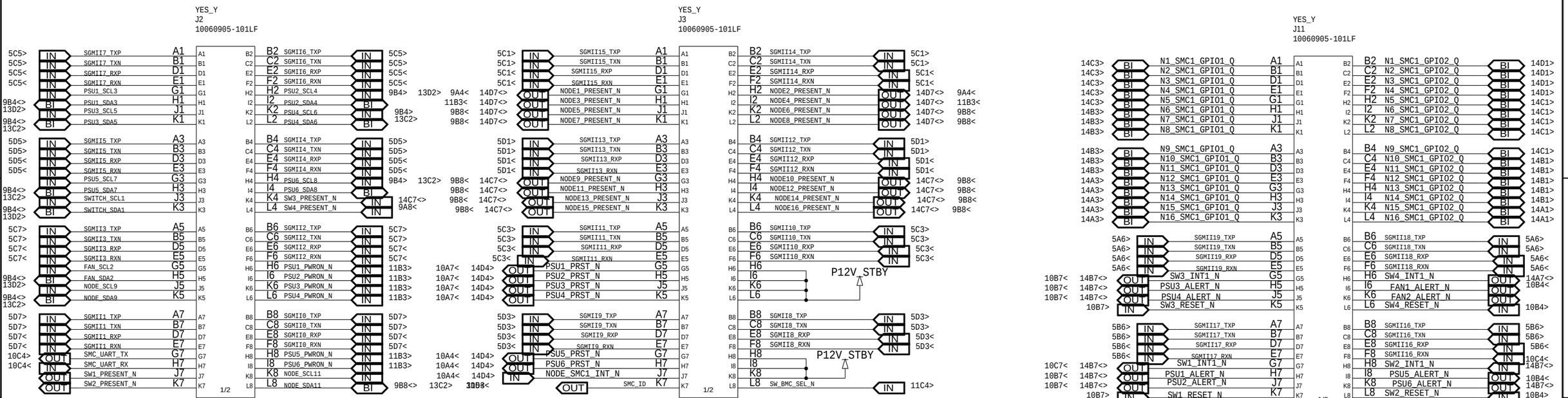
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P3V3_STBY

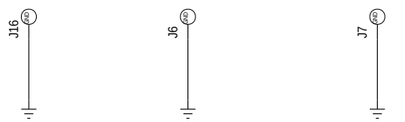
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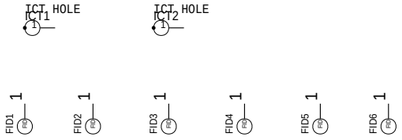
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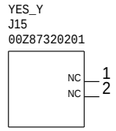
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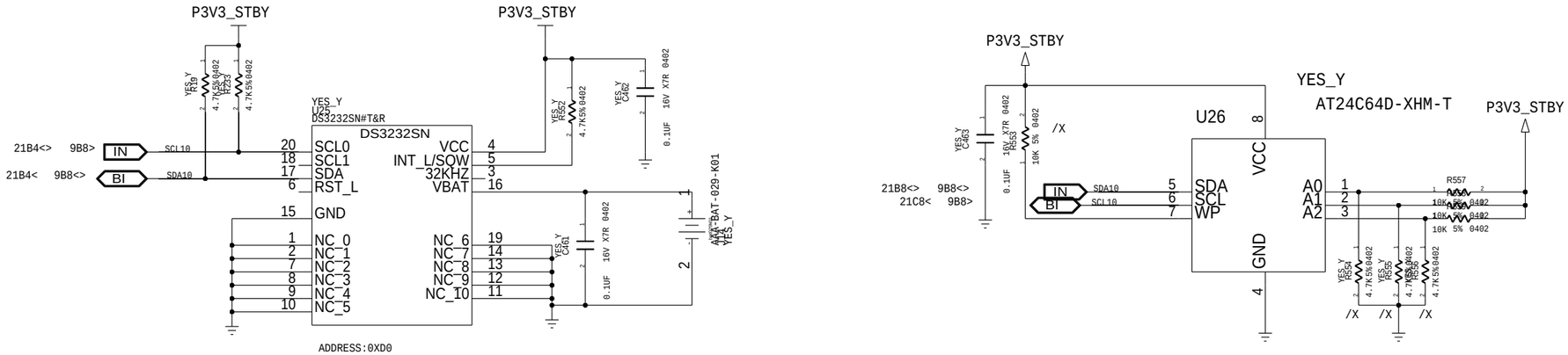
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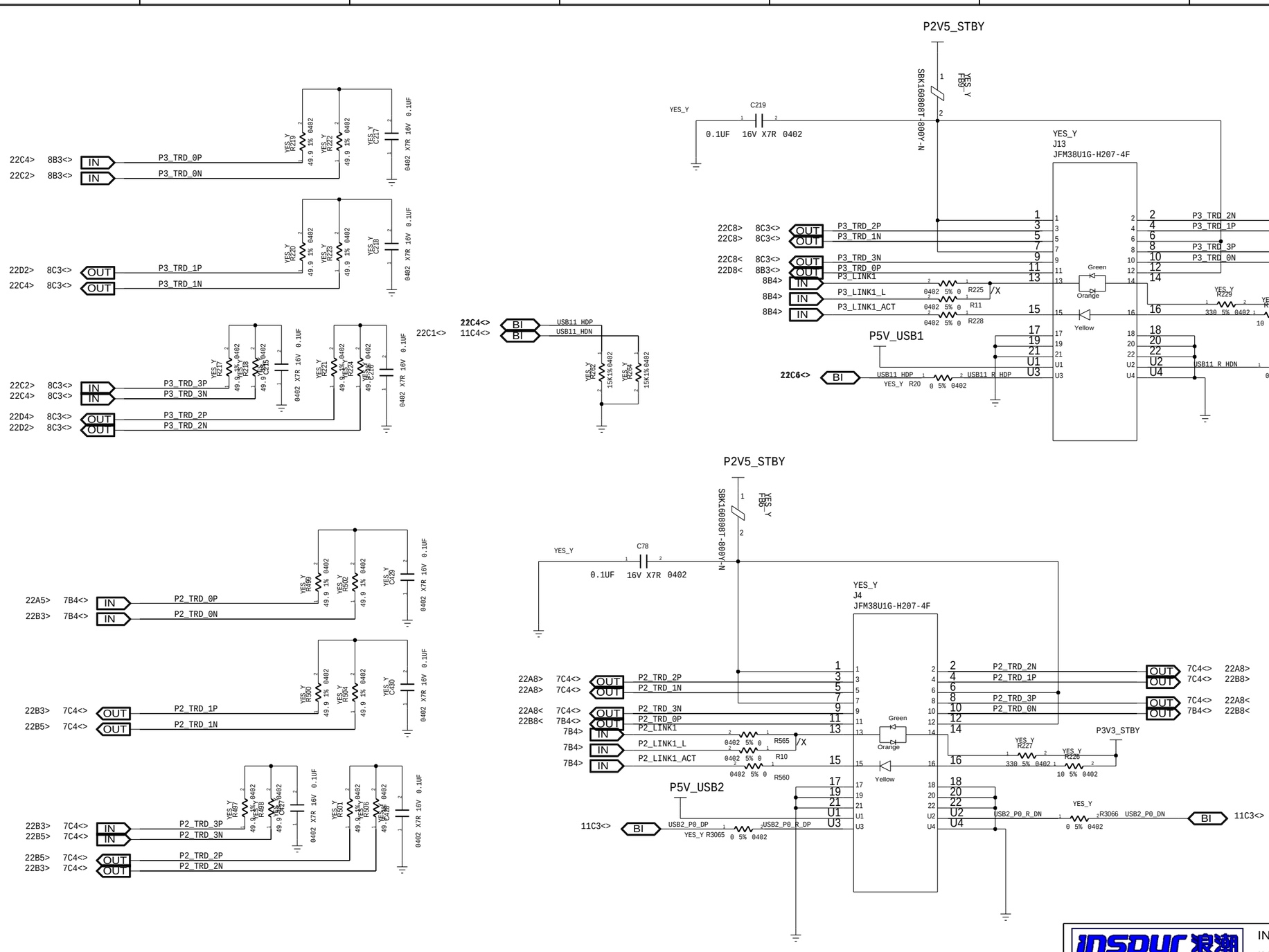




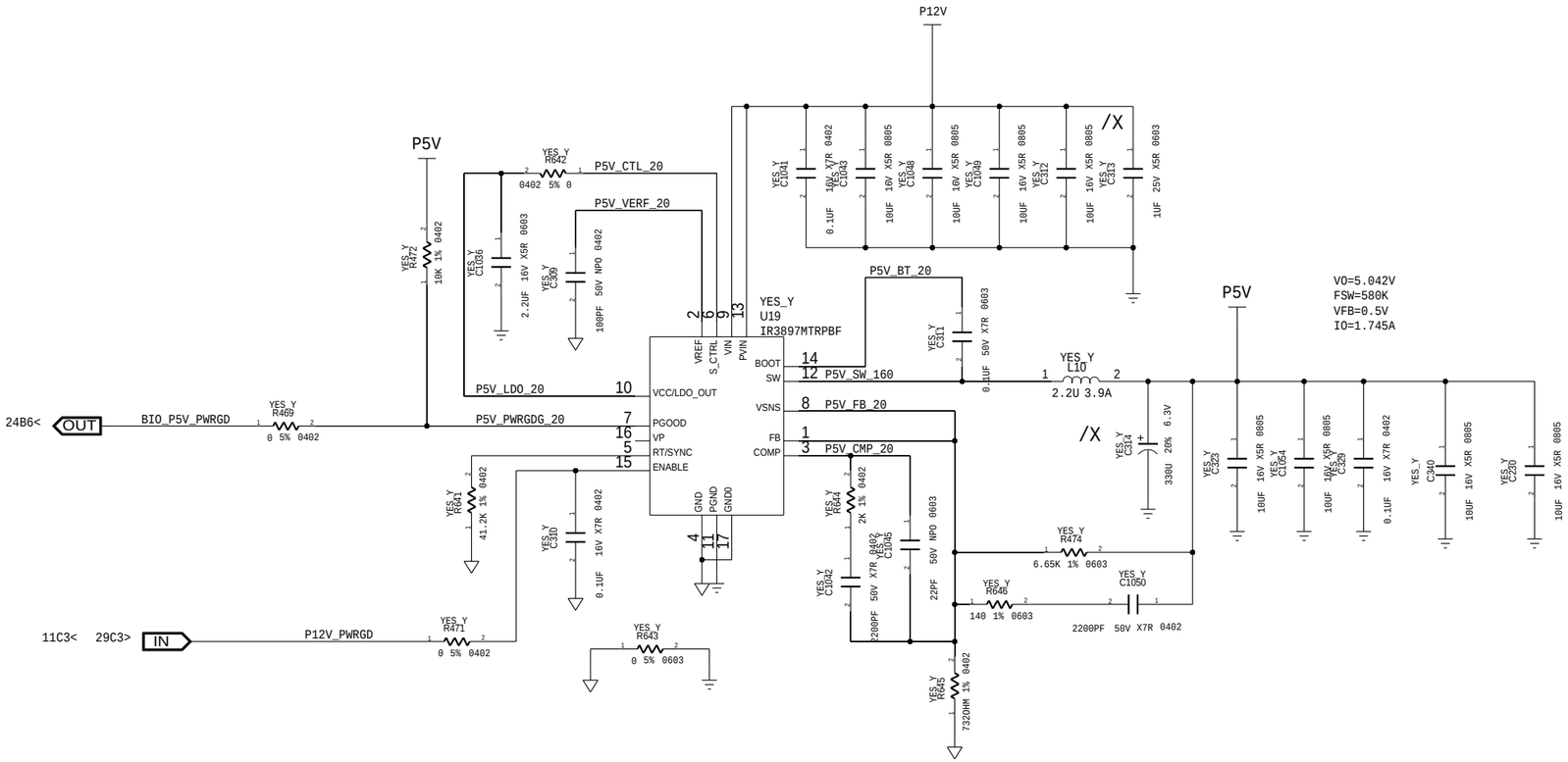
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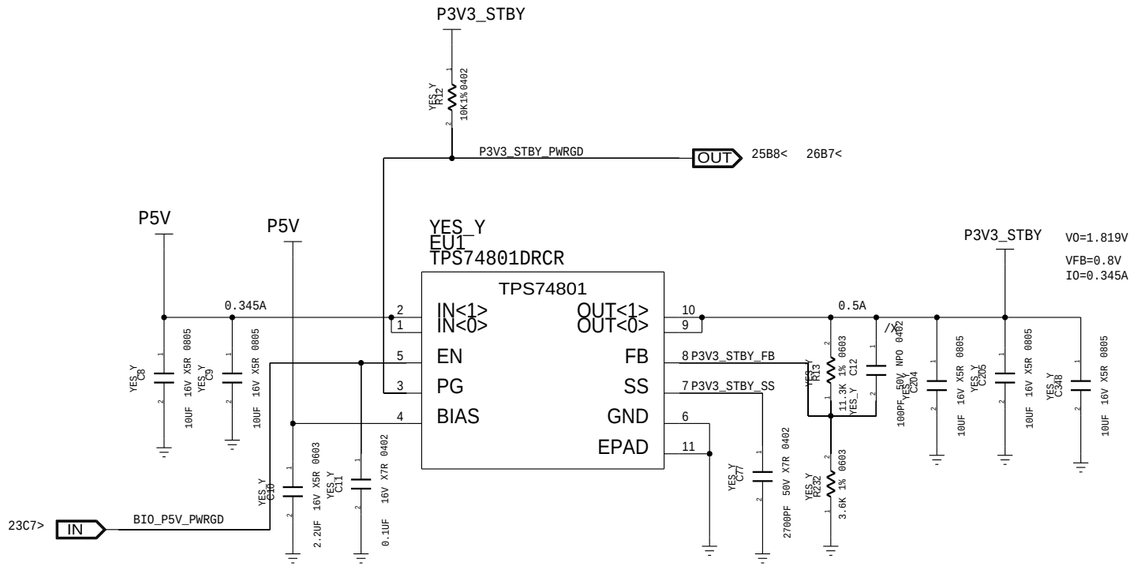
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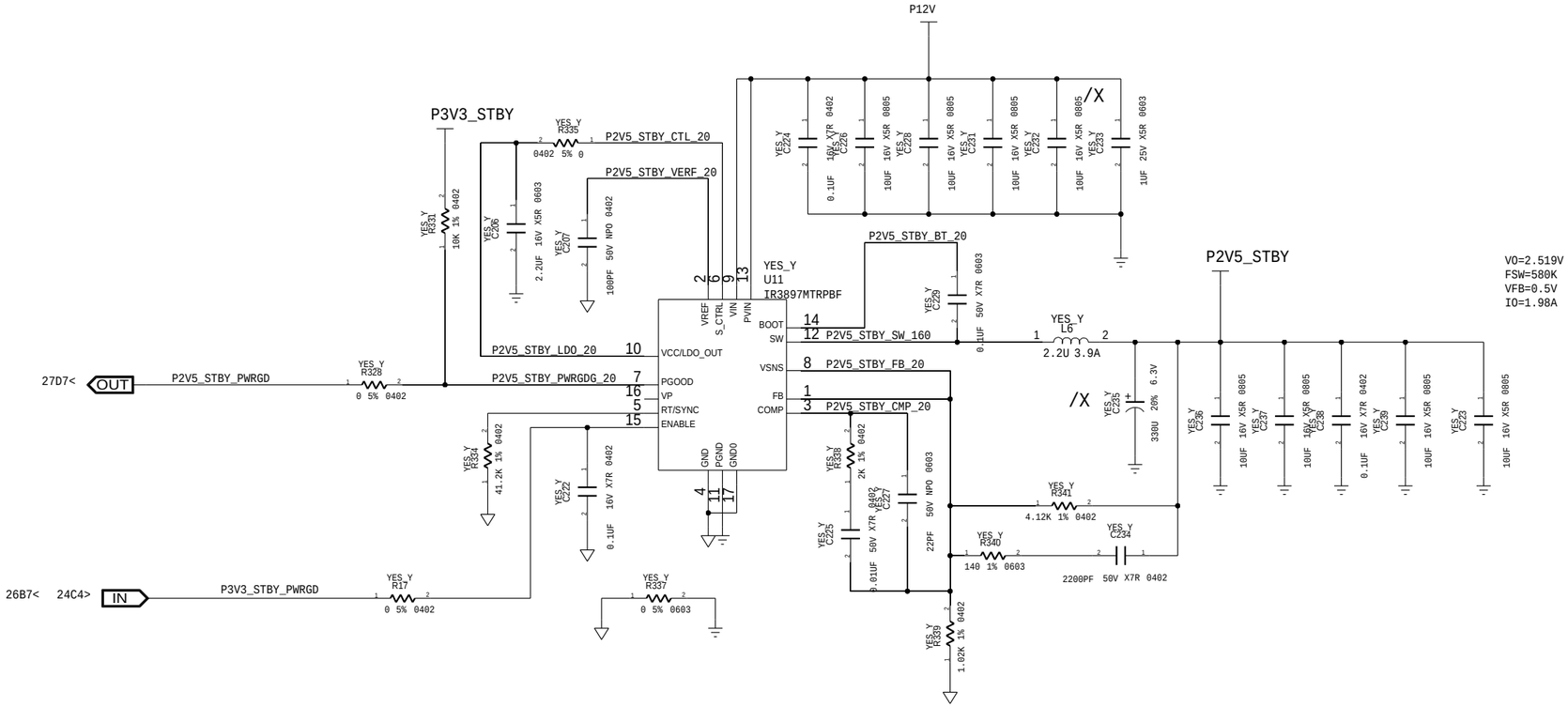
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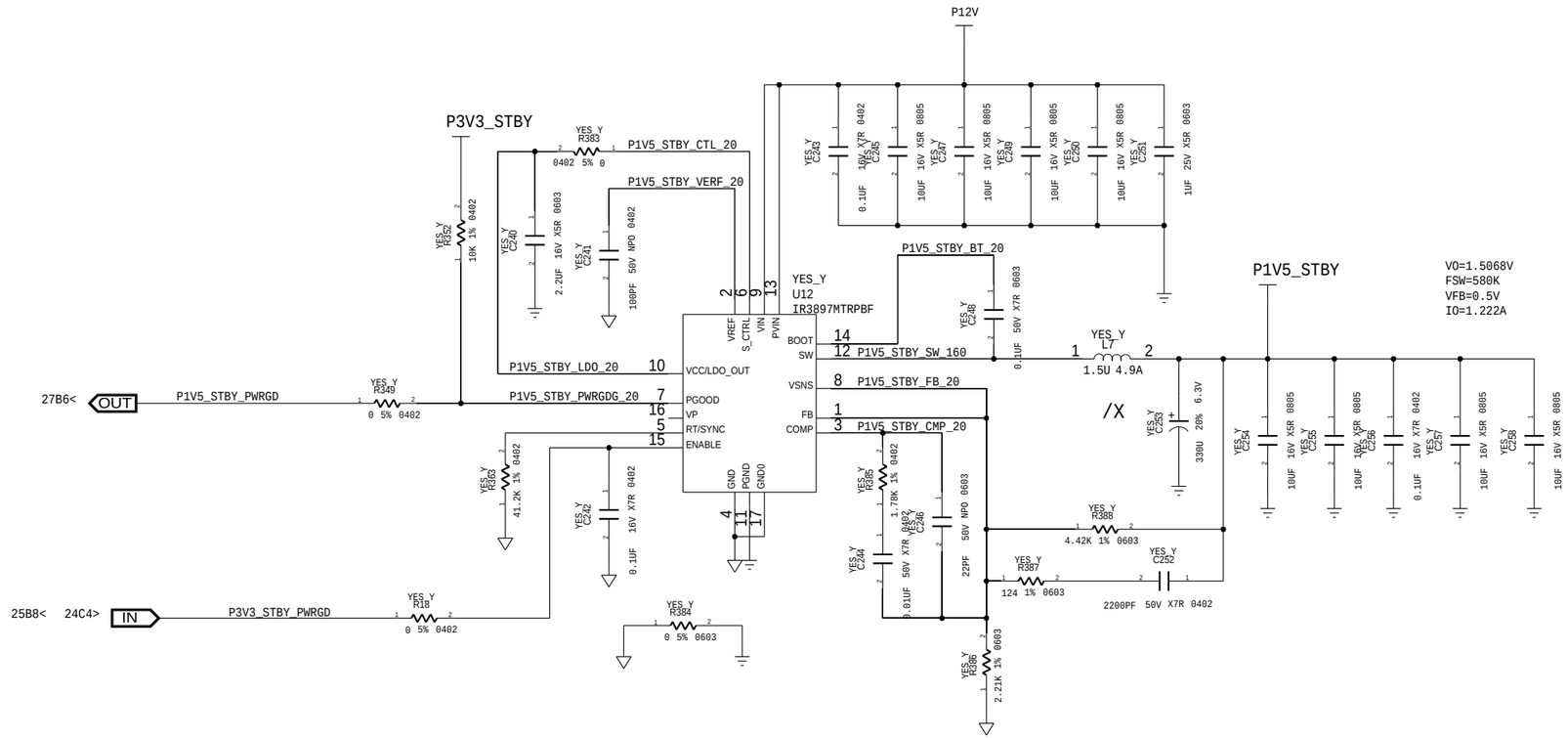
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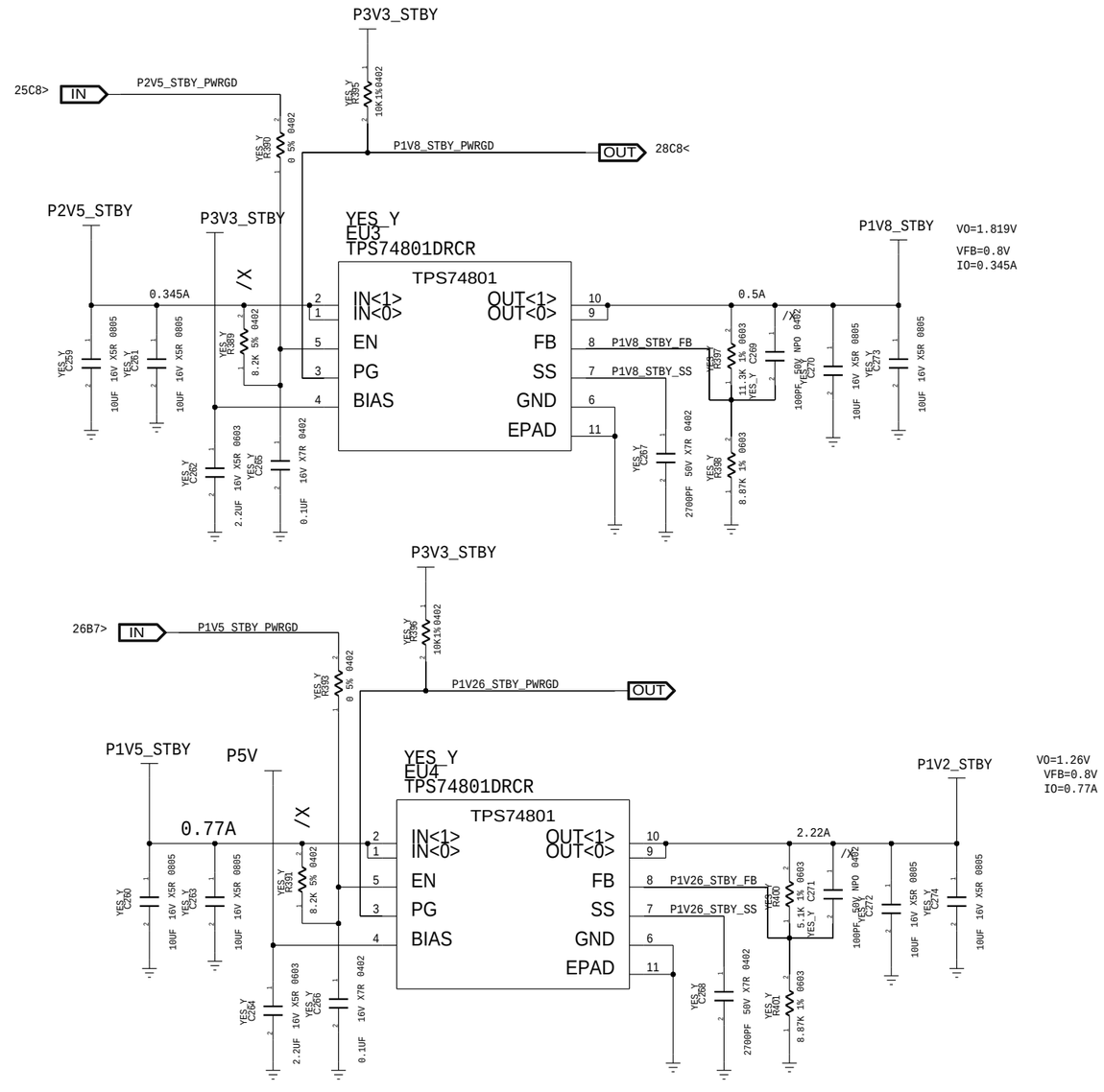
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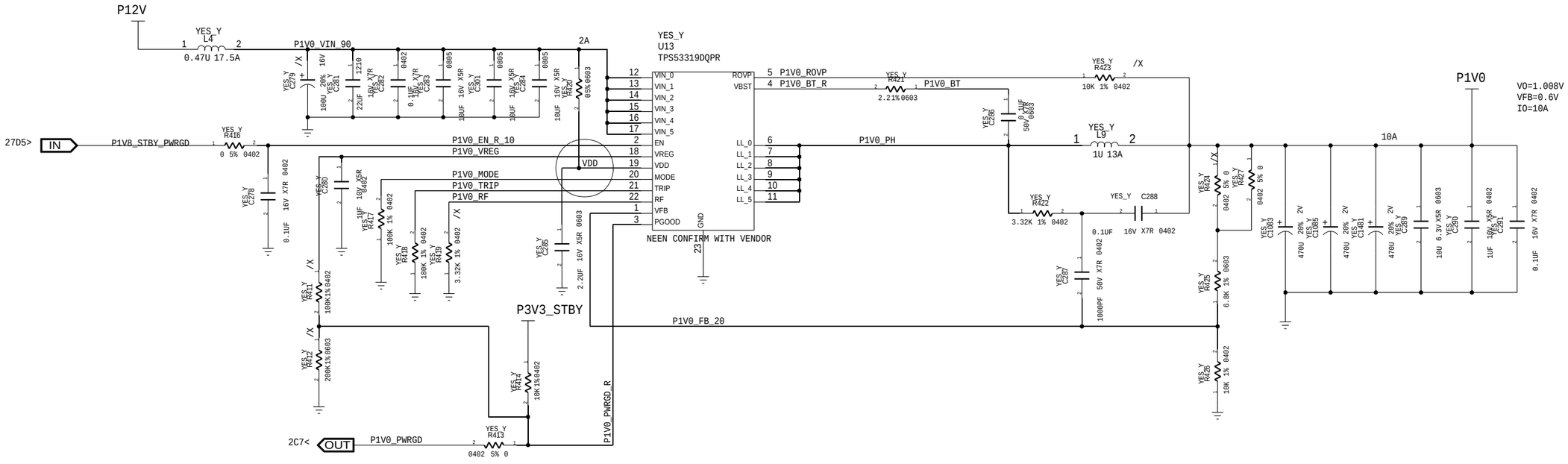
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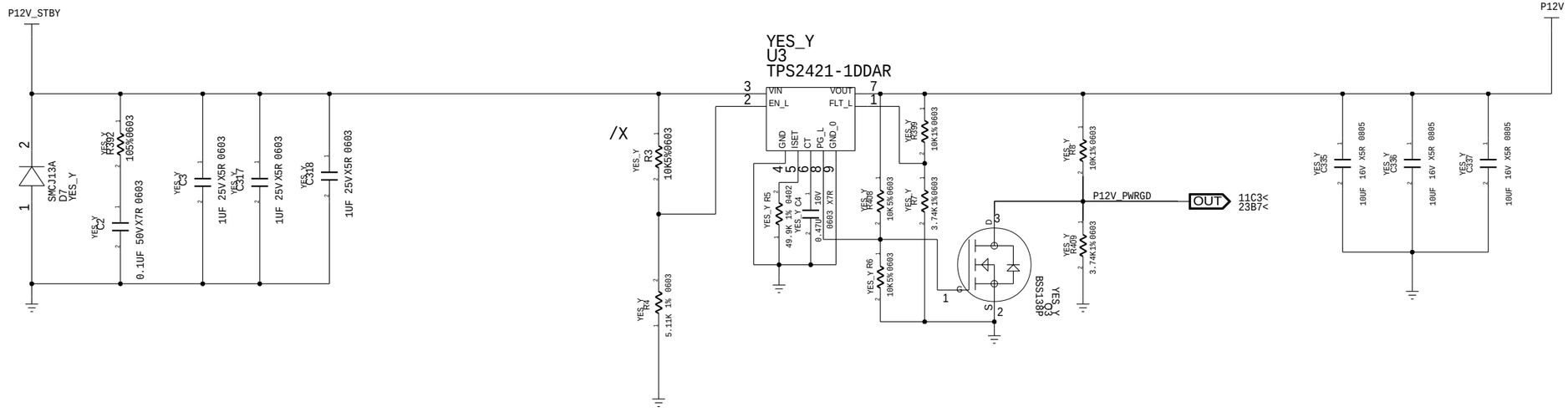
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