

ES7632BT-0614-168ZZ Cover Page

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50	- Extension Board
51	- I2C0_PCA9548

@ For AO project
* Un-mount
Extension Board only for P2041

ES7632BT-0614-168ZZ Revision History

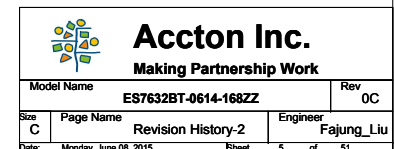
Rev.	Date	Author	Revision Description
R0A	07 - 19 - 2014	Fajung_Liu	First draft
R0B	09 - 23 - 2014	Fajung_Liu	(1)Add Heatsink BRACKET(HS2) Component(P20) (2)Modify matched Front Port's net name (P21~P28)
R0B	10 - 03 - 2014	Fajung_Liu	(1)Modify Interrupt block diagram(P06) (2)Add Clock Gen(U24) phase jitter SPEC(P33) (3)Modify Page Name SCREW change to mounting holes(P41) (4)Modify clock buffer net name, add frequency in the net name (P32、P33、P8、P9) (5)Add external connector ANT2 for clock buffer(P33)
R0B	10 - 14 - 2014	Fajung_Liu	(1)Modify OCXO Cover symbol CA1(P33) (2)Modify Cage Footprint(P21~P28) (3)Modify U39 select pull low resister R1270 Populate R1269 Depopulate(P30)
R0B	10 - 17 - 2014	Fajung_Liu	(1)Modify USB HUB I2C net name(P47) (2)change VDD1P25 L15 component and add C2100 470uF cap(P40) (3)change R161 , R163 ,R185 , R186 to 49.9 ohm (P38,P40) (4)Add R1800 10Kohm pull low resister(P31) (5)PHY Address reserve pull low resister R1801~R1805(P42) (6)R803 Pull low Resister 4.7K ohm change to 100K ohm(P47)
R0B	10 - 31 - 2014	Fajung_Liu	(1)Populatar R803,Depulatar R214,R43 and R50 for USB HUB I2C(P43、P47)
R0B	10 - 31 - 2014	Fajung_Liu	(1)Add the reset button(SW2) on front side(P41)
R0B	11 - 07 - 2014	Fajung_Liu	(1)Add the I2C LM75 Pull-Up resister 4.7Kohm R1806,R1807(P29)
R0B	11 - 14 - 2014	Fajung_Liu	(1)Modify CPU to CPLD JTAG TDI/TDO(P29) (2)Link database Cage Footprint
R0B	12 - 18 - 2014	Fajung_Liu	(1)Change Power chock(102000000449A to 102000000627A)L1,L2,L20,L21,L22,L23,L24,L25,L26 (P38,P40) (2)Change Power chock(102000000449A to 102000000610A)L15,L16,L17(P40)
R0B	12 - 22 - 2014	Fajung_Liu	(1)Add 1.25V 470uF Cap C2101,C2102(P40) (2)Add 3.3V 100uF Cap C2103,C2104,C2015,C2016,C2017,C2108(P40) (3)Add 1V 470uF Cap C2109,C2110 and mount C164,C855(P38) (4)Mount ROV_1V 470uF Cap C858,C859,C860,C861,C862(P38) (5)Change power chock(102000000289A to 102000000462A)L6(P39) (6)Add Vstb_3V3 22uF Cap C2111(P39) (7)Add 5V 22uF Cap for U4 C2112(P39) (8)Add 5V 22uF Cap for U40 C2113(P39) (9)Add 1.2V 22uF Cap for U21 C2114,C2115(P48)

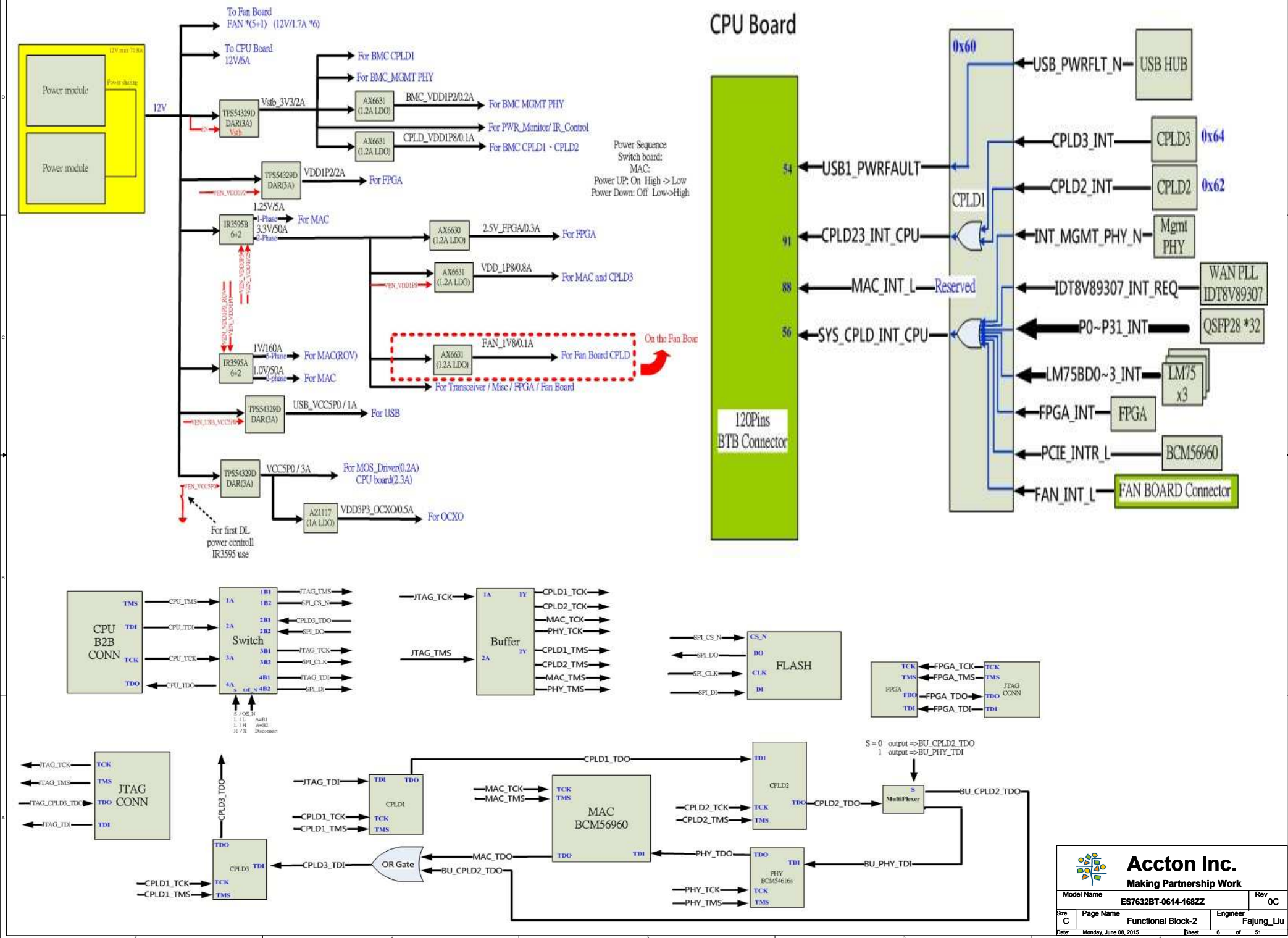
ES7632BT-0614-168ZZ Revision History

Rev.	Date	Author	Revision Description
R0B	12 - 22 - 2014	Fajung_Liu	(1)Modify OSC3 C load 22pF change to 18pF C220,C233 (P31) (2)Modify X2 C load 10pF change to 6.8pF C282,C274 (P30)
R0B	12 - 23 - 2014	Fajung_Liu	(1)Modify I2C topology (P05) (2)Add I2C PCA9548*4 for transceiver EEPROM (P49)
R0B	12 - 24 - 2014	Fajung_Liu	(1)Modify System LED color ,Remove R715(P44 and P27) (2)Modify CPLD pin assignment (P27 and P28) (3)Change Net CPLD3 to CPLD1 RESET_SYS_CPLD (P27 and P29)
R0B	12 - 25 - 2014	Fajung_Liu	(1)Change Resister size 0402 to 0603 R185,R186,R163,R161 (P36,P38) (2)Add 22pF Cap for U21 incresed phase margin (P46)
R0B	12 - 26 - 2014	Fajung_Liu	(1)Add 0.1uF Cap for Power monitor Net Name:HVO1_R (P38) (2)Depopulate thermal sensor I2C AC termination R782,R1142,R873,C845,C1887,C949 (P44)
R0B	12 - 29 - 2014	Fajung_Liu	(1)Modify USB default value Populate R213 Depopulate R802, R803 change 100K to 4.7kohm (P45) (2)Change LED current limit Resister 10 ohm to 49.9ohm R724,R683~R688,R689~R694,R695~R698 (P44,P32~P34) (3)Add Reset signal pull low resister 4.7k ohm , Net Name :RESET_SYS_CPLD,CPU_JTAG_RST,RESET_MAC (P43) (4)Add 33 ohm serial termination for 25M_DFF_CLK_P/N (P30) (5)Modify extension Board reset signal CONN120_P52==>CONN100_P28 , CONN120_P68 for BMC folating , CONN100_P70 ? (P48)
R0B	01 - 08 - 2015	Fajung_Liu	(1)Modify Reset button circuit SW2(P39)
R0B	01 - 12 - 2015	Fajung_Liu	(1)Modify CPLD3 and CPLD1 Pin assignment(P27.P29)
R0B	01 - 14 - 2015	Fajung_Liu	(1)Add BMC Function (P40,P27,p28)
R0B	01 - 23 - 2015	Fajung_Liu	(1)CPLD[1:3] dual layout(5M1270F256 to 5M2210F256) pin(F10,F7,G11,G5,G6,K11,K6,L10,L7,T6) (P27,P28,P29) (2)Add 49.9ohm resister for Regulator Bode plot test(P37,P46) (3)Modify USB config CFG_SEL setup to "00"(P45) (4)Modify R1970,R1971 to 10kohm(P41)
R0B	01 - 28 - 2015	Fajung_Liu	(1)Add I2C Switch for thermal sensor and Fan board(P41)
R0B	01 - 29 - 2015	Fajung_Liu	(1)Change TVS Part NO. 102600005100E to 102600011100E (P43,P45) (2)Change 150ohm part NO. 5% to 1%(P32~P34) (3)Add test module in Estension Board (P48)
R0B	01 - 30 - 2015	Fajung_Liu	(1)Cage 1~4 change part NO.119A00000173A to 119A00000226A (P19,P21,P23,P25)
R0B	02 - 05 - 2015	Fajung_Liu	(1)Change L15,L16,L17 footprint for PIP
R0C	03 - 04 - 2015	Fajung_Liu	(1)Change BTB CONN part NO. 119B00000127A to 119B00000198A
R0C	03 - 04 - 2015	Fajung_Liu	(1)Modify Title blocks version to R0C
R0C	03 - 24 - 2015	Fajung_Liu	(1)Modify Front Ports number P1 to P32
R0C	03 - 25 - 2015	Fajung_Liu	(1)Modify LED number P1 to P32

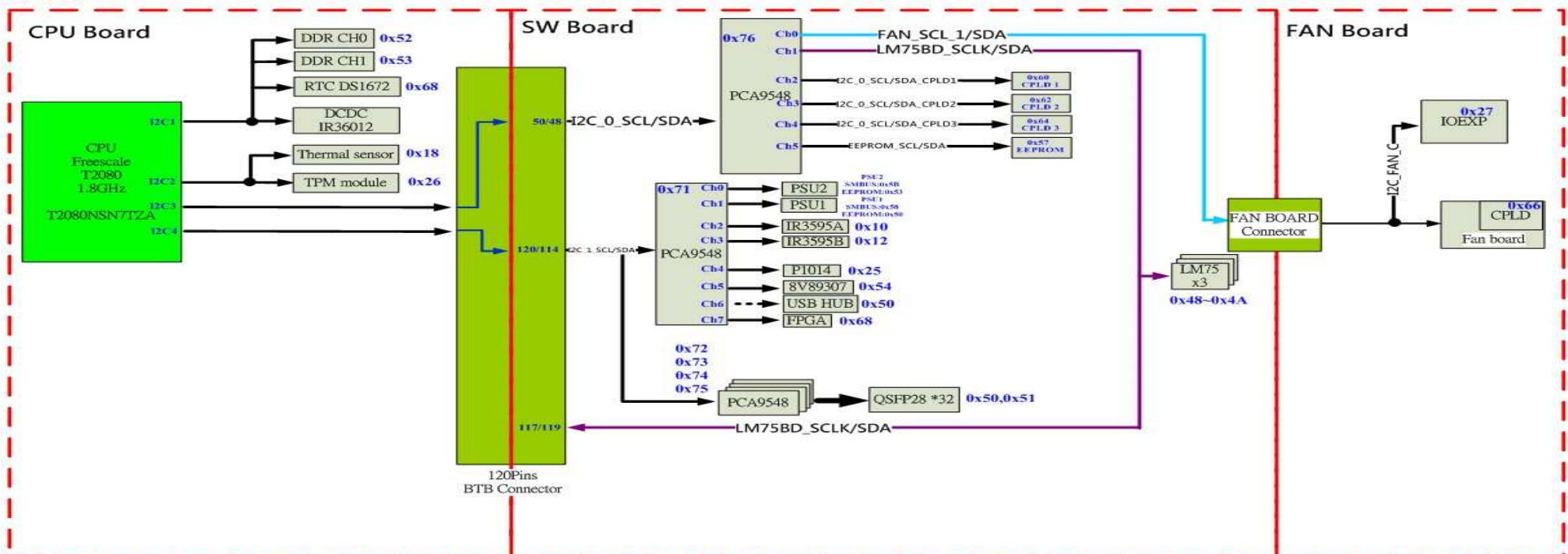
ES7632BT-0614-168ZZ Revision History

Rev.	Date	Author	Revision Description
R0C	03 - 25 - 2015	Fajung_Liu	(1)Modify LED Resister 510 ohm to 220ohm,remove SCR1 and SCR2 ,populate R1993 and R1994(P44)
R0C	04 - 13 - 2015	Fajung_Liu	(1)TCK Resister R868 1k ohm change to 10k ohm (P29)
R0C	05 - 06 - 2015	Fajung_Liu	(1)Depopulate C750 and C751 47pF for USB(P47) (2)Modify I2C block diagram,PCA9548 connect to CPLD1/2/3 (P43,P29,P30,P31) (3)Add MDIO termination resisrer 33 ohm R2078(P42) (4)Reserve Net name RESET_MAC pull up and pull low resister R1972 and R2079(P43) (5)Modify Location C1655 and C1523 change to R2080 and R2081 (P08) (6)Add IP_GPIO[0,1,3] external pull low resister R2082~R2084 (P08)
R0C	05 - 21 - 2015	Fajung_Liu	(1)Modify Extension Board Reset CONN9000.28 connect to CONN9001.52 and CONN9000.70 connect to CONN9001.68 (P50)
R0C	05 - 25 - 2015	Fajung_Liu	(1)Add CPU can download code of FPGA SPI function (P43、P49) (2)Modify Extension Board 120 pin connector pin assigment(P50) (3)Modify B2B CONN pin assigment for XFI issue(P43)
R0C	05 - 27 - 2015	Fajung_Liu	(1)Add EMI Solution for OOB interface (P8、P49) (2)Add 3*0.1uF cap for EMI (P45)
R0C	05 - 28 - 2015	Fajung_Liu	(1)Modify SPI CPU free download function,add config pin to CPLD1(P43、P49) (2)Change R731 to 33 ohm for FPGA DATA0 (P49) (3)Modify SW1_CPU_TDI and SW1_CPU_TDO to correct direction (P29) (4)Modify JTAG Block Diagram(P06) (5)Add 22pf cap for EMI soulation(P33、P40)
R0C	05 - 31 - 2015	Fajung_Liu	(1)Add 22uf/16V cap*2 for VCC5P0(P33) (2)Unmount C208 and C550 for EMI soulation(P20) (3)C551 and C206 remove change to 120100000058A for EMI soulation(P20) (4)H4,H5,H9 unmount CAP , C191 and C192 change to Bead 102100000058A for EMI(P41)
R0C	06 - 03 - 2015	Fajung_Liu	(1)Modify JTAG circuit define TDI/TDO direction(P29、P43)
R0C	06 - 04 - 2015	Fajung_Liu	(1)Add Conn9 SCREW SCR1 and SCR2(P41)

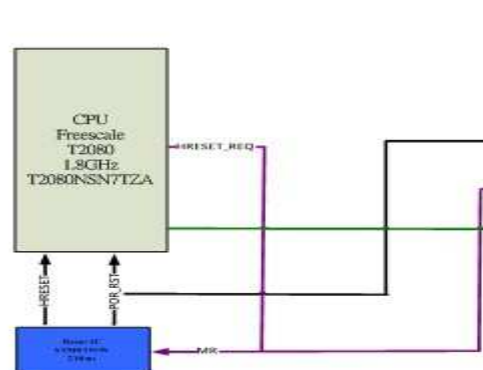




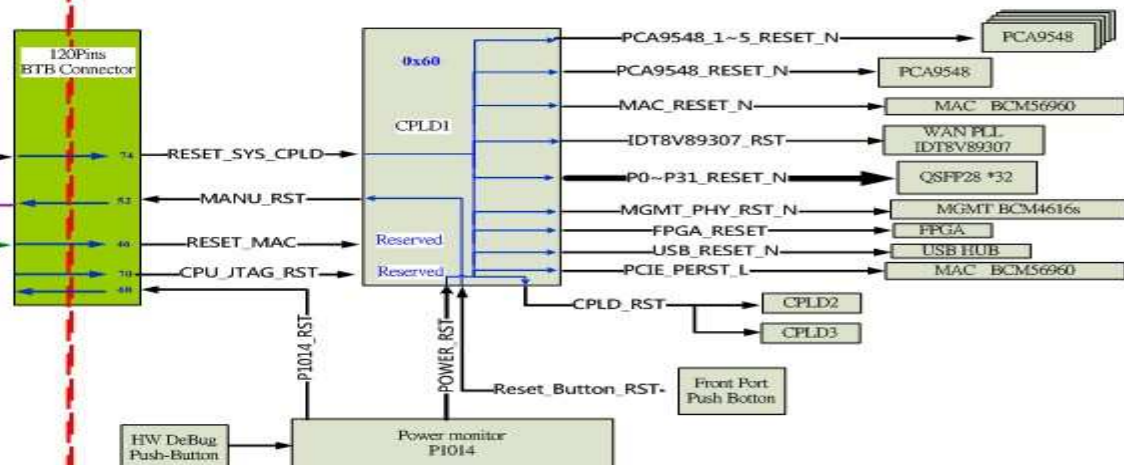
I2C



CPU Board



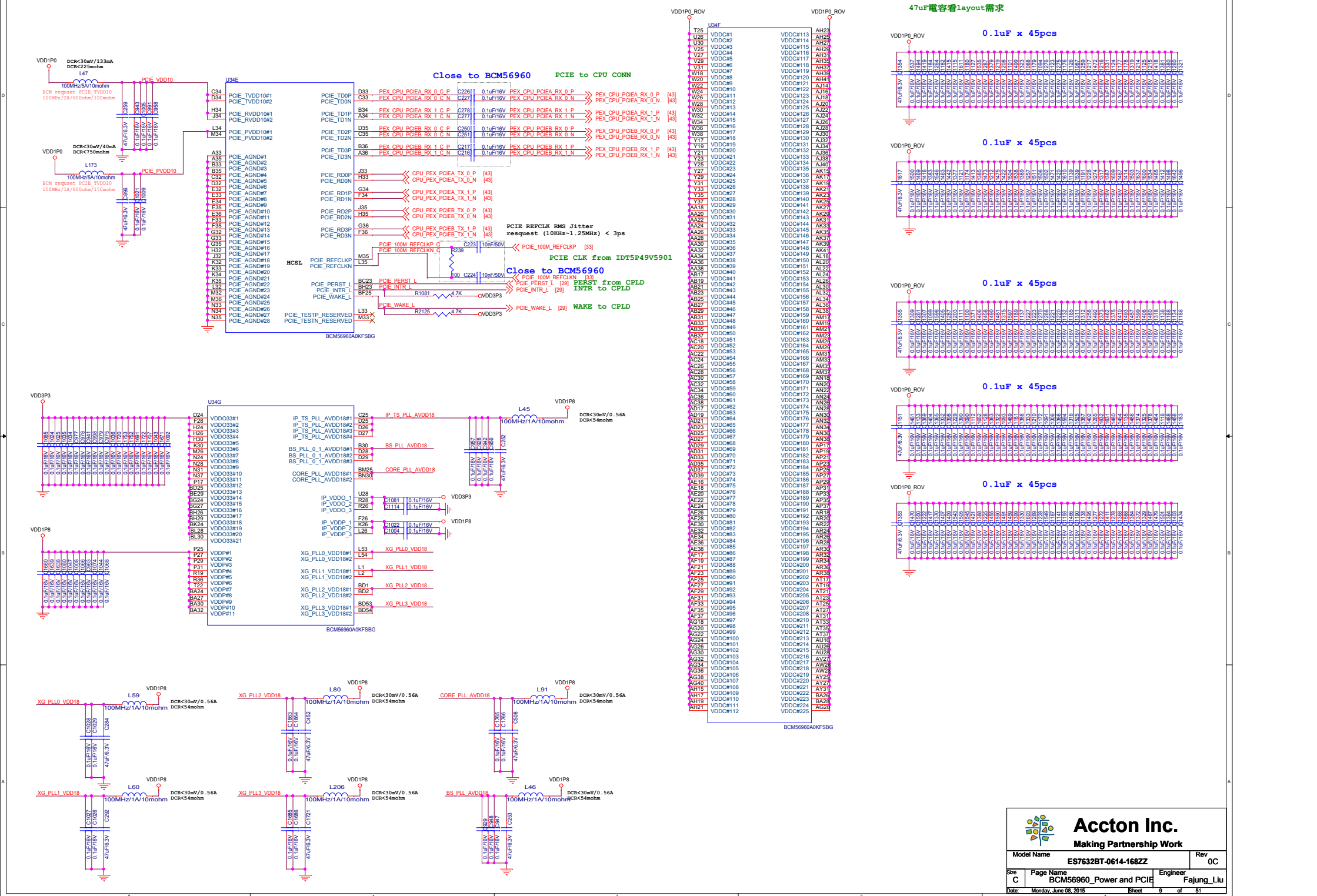
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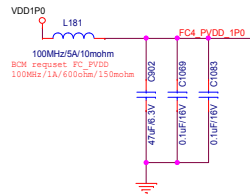
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Size	Page Name	Engineer	Fajung_Liu
C	Functional Block-3		
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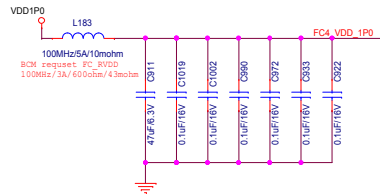




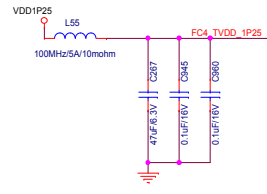
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DCR<200mohm



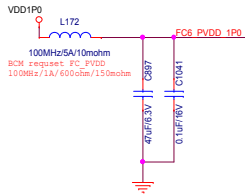
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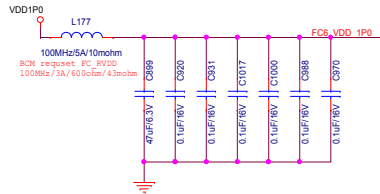
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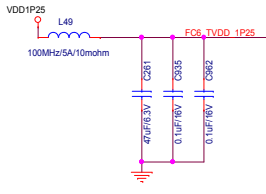
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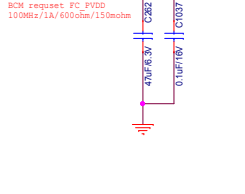
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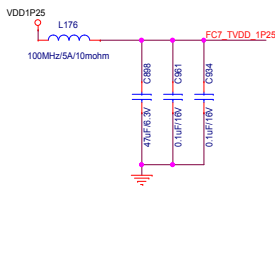
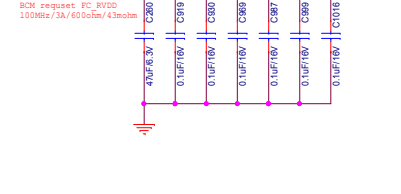
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DCR<30mV/0.15A
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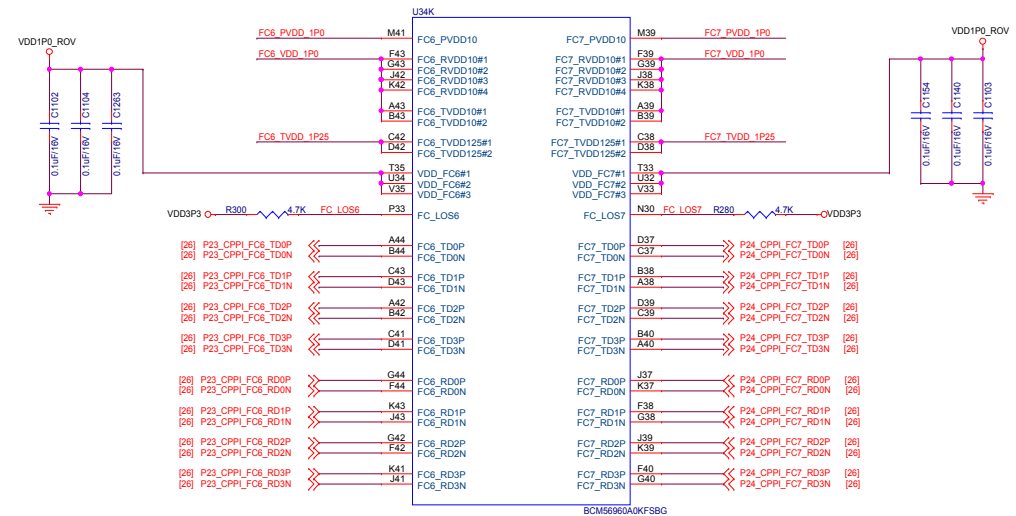


For FC *VDD
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DCR<25mohm



Port 21

Port 22



Port 23

Port 24

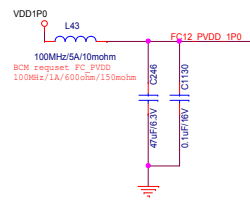


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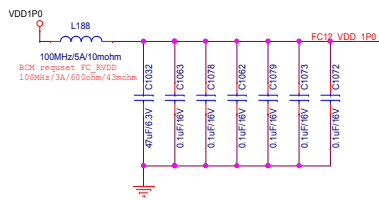
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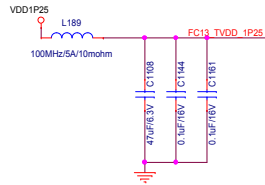
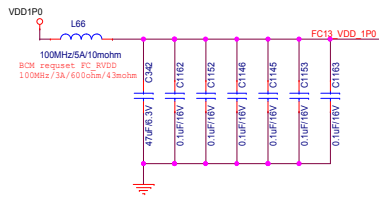
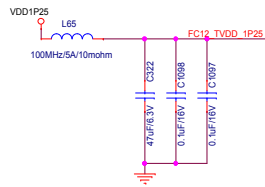
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For FC *VDD
DCR<30mV/1.2A
DCR<25mohm

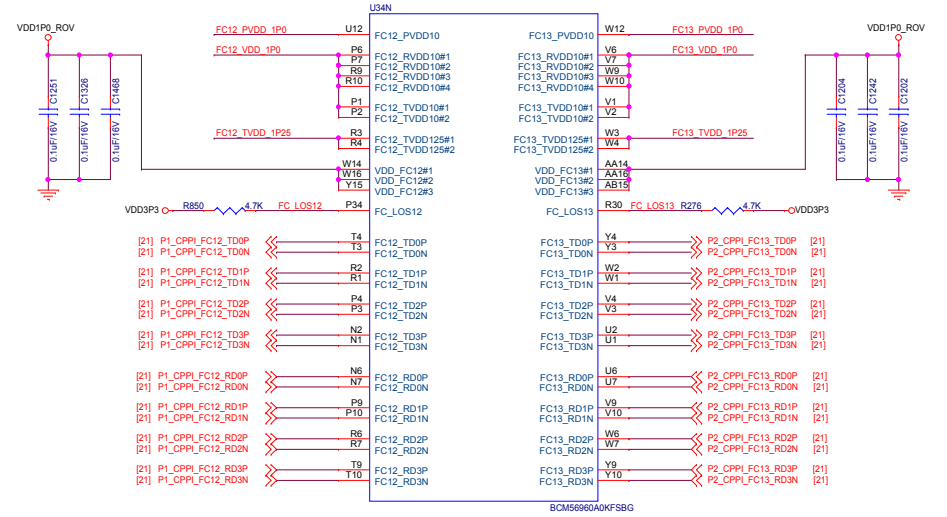


For FC *TVDD1P25
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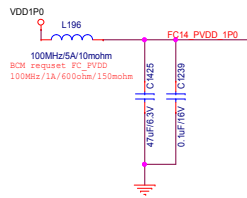
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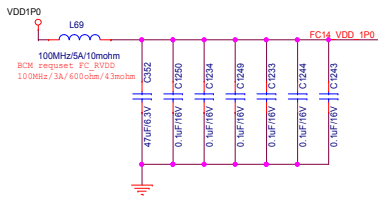
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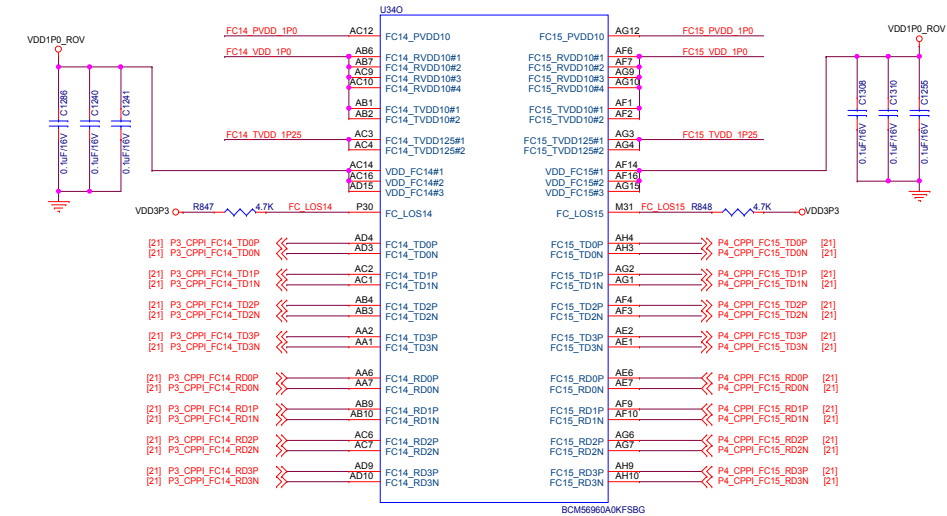
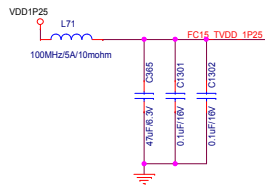
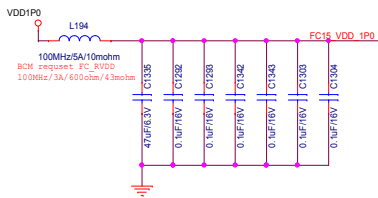
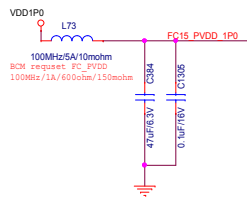
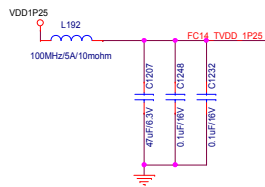
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For FC *VDD
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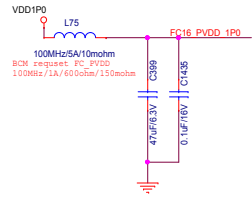
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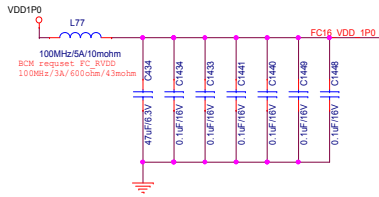
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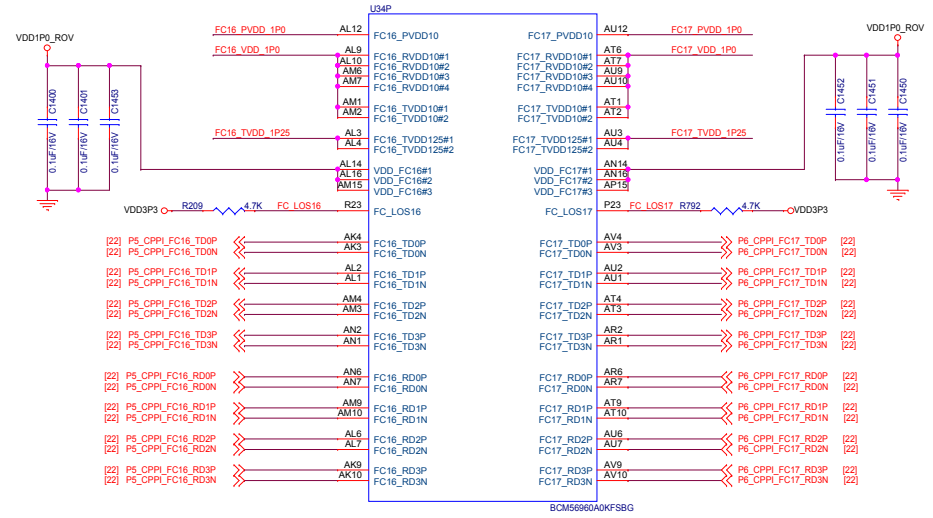
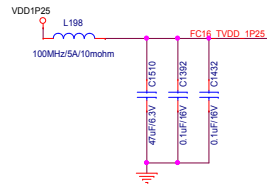
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For FC *VDD
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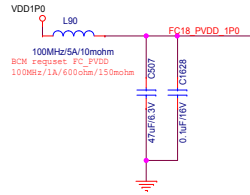
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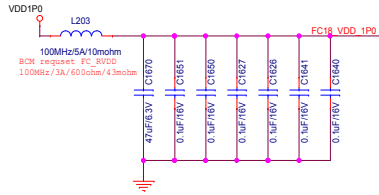
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Port 6

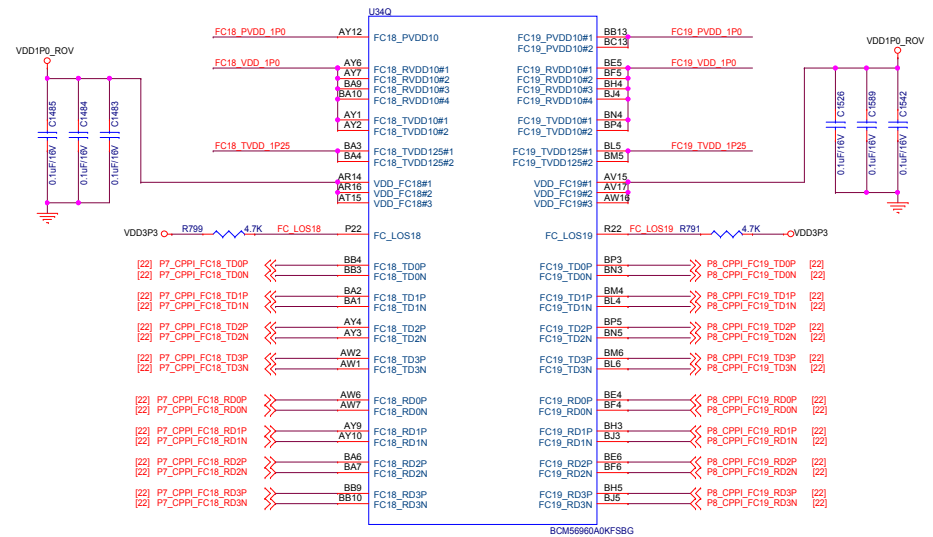
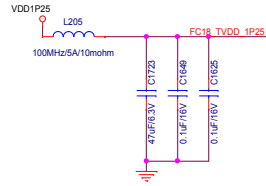
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DCR<200mohm



For FC *VDD
DCR<30mV/1.2A
DCR<25mohm



For FC *TVDD1P25
DCR<37.5mV/0.15A
DCR<250mohm



Port 7

Port 8

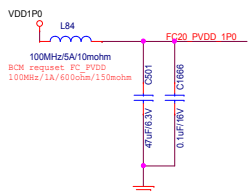


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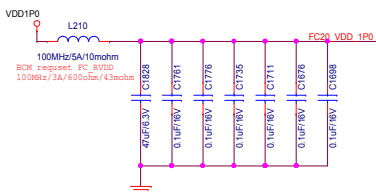
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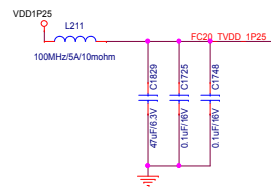
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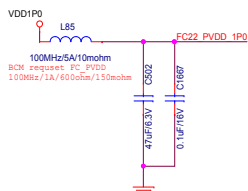
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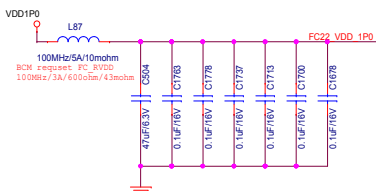
For FC *TVDD1P25
DCR<37.5mV/0.15A
DCR<250mohm



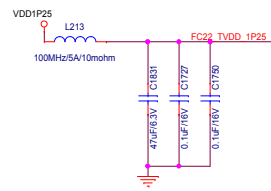
For FC *PVDD
DCR<30mV/0.15A
DCR<200mohm



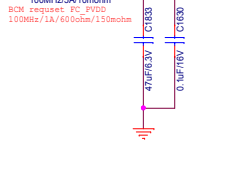
For FC *VDD
DCR<30mV/1.2A
DCR<25mohm



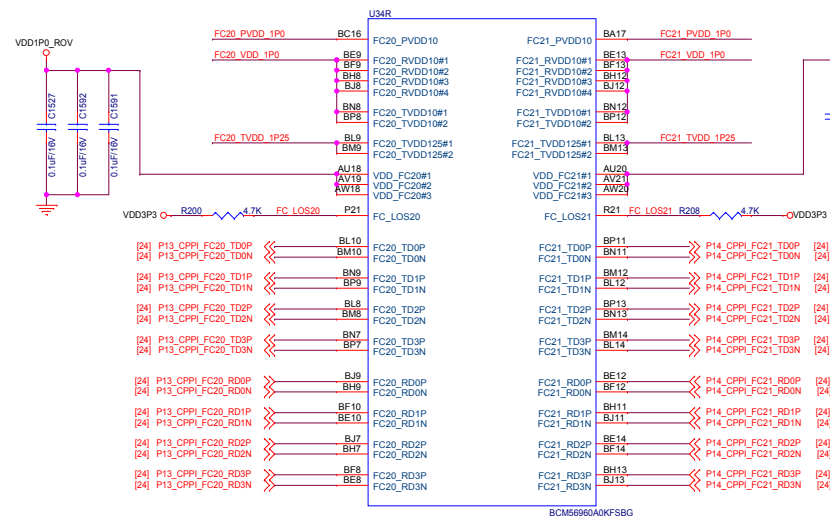
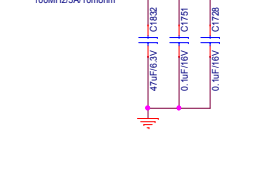
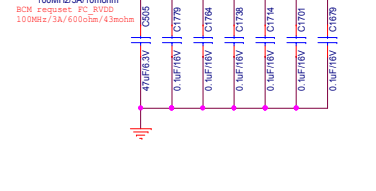
For FC *TVDD1P25
DCR<37.5mV/0.15A
DCR<250mohm



For FC *PVDD
DCR<30mV/0.15A
DCR<200mohm

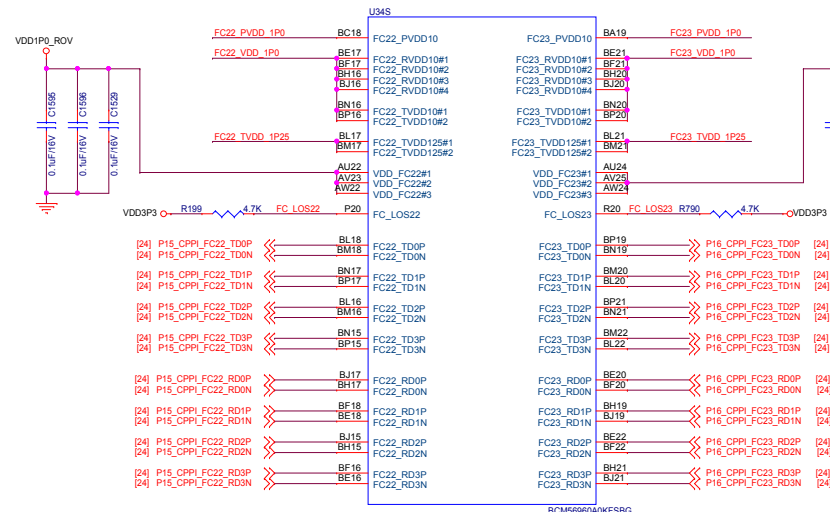


For FC *VDD
DCR<30mV/1.2A
DCR<25mohm



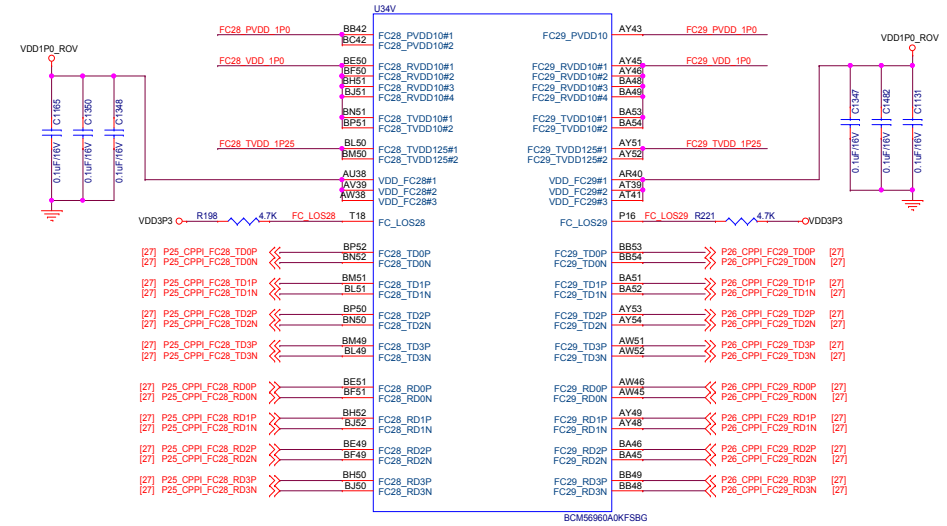
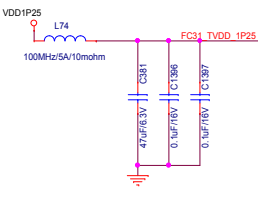
Port 13

Port 14



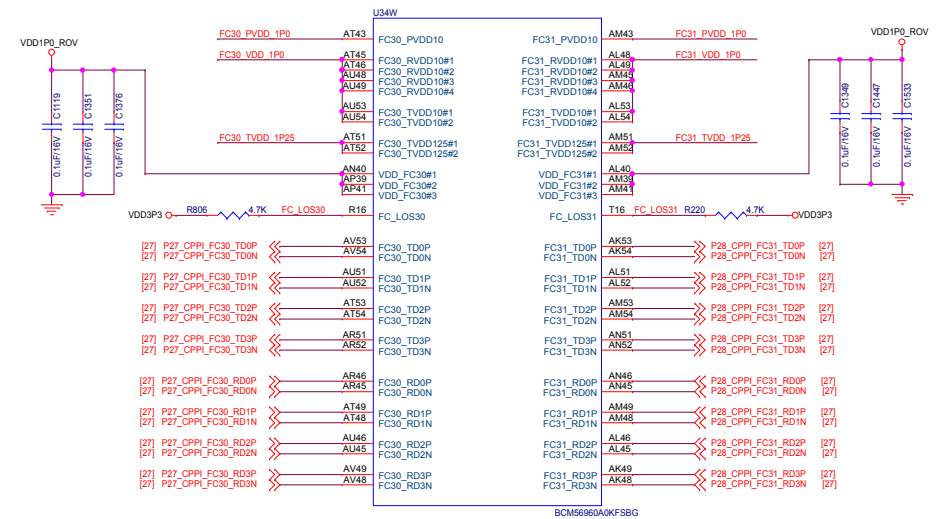
Port 15

Port 16



Port 25

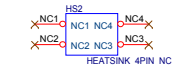
Port 26

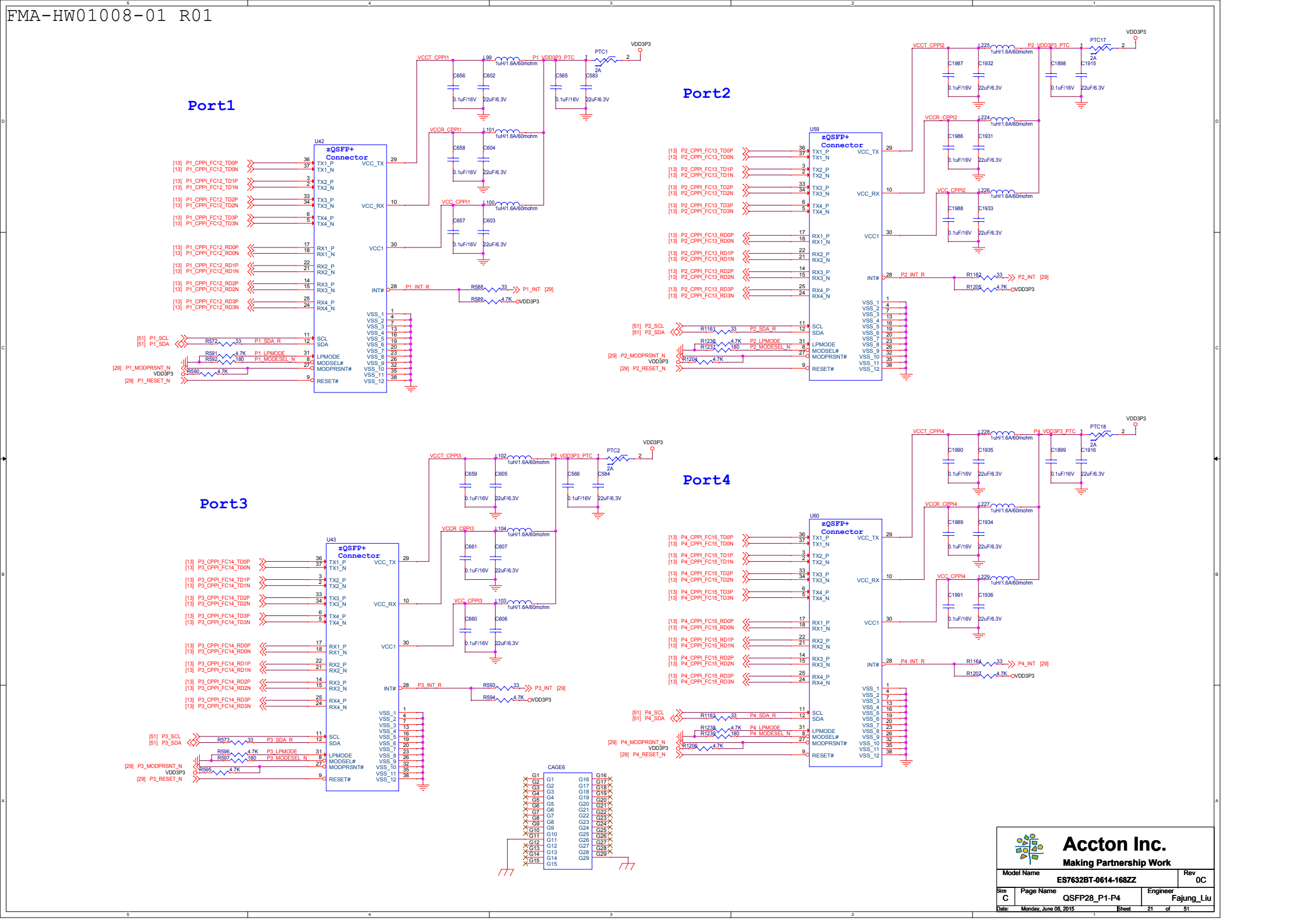


Port 27

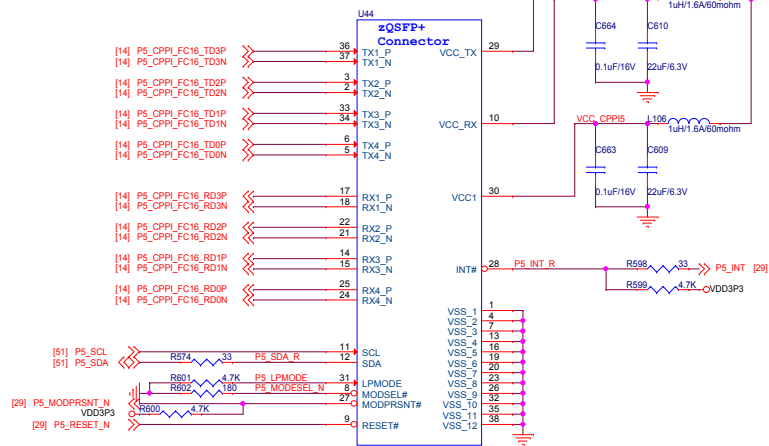
Port 28

	B0D2	B0D3	B0D4	B0D5	B0D6	B0D7	B0D8	B0D9	B0DA	B0DB	B0DC	B0DD	B0DE	B0DF	B0E0	B0E1	B0E2	B0E3	B0E4	B0E5	B0E6	B0E7	B0E8	B0E9	B0EA	B0EB	B0EC	B0ED	B0EE	B0EF	B0F0	B0F1	B0F2	B0F3	B0F4	B0F5	B0F6	B0F7	B0F8	B0F9	B0FA	B0FB	B0FC	B0FD	B0FE	B0FF	B100	B101	B102	B103	B104	B105	B106	B107	B108	B109	B10A	B10B	B10C	B10D	B10E	B10F	B10G	B10H	B10I	B10J	B10K	B10L	B10M	B10N	B10O	B10P	B10Q	B10R	B10S	B10T	B10U	B10V	B10W	B10X	B10Y	B10Z	B110	B111	B112	B113	B114	B115	B116	B117	B118	B119	B11A	B11B	B11C	B11D	B11E	B11F	B11G	B11H	B11I	B11J	B11K	B11L	B11M	B11N	B11O	B11P	B11Q	B11R	B11S	B11T	B11U	B11V	B11W	B11X	B11Y	B11Z	B120	B121	B122	B123	B124	B125	B126	B127	B128	B129	B12A	B12B	B12C	B12D	B12E	B12F	B12G	B12H	B12I	B12J	B12K	B12L	B12M	B12N	B12O	B12P	B12Q	B12R	B12S	B12T	B12U	B12V	B12W	B12X	B12Y	B12Z	B130	B131	B132	B133	B134	B135	B136	B137	B138	B139	B13A	B13B	B13C	B13D	B13E	B13F	B13G	B13H	B13I	B13J	B13K	B13L	B13M	B13N	B13O	B13P	B13Q	B13R	B13S	B13T	B13U	B13V	B13W	B13X	B13Y	B13Z	B140	B141	B142	B143	B144	B145	B146	B147	B148	B149	B14A	B14B	B14C	B14D	B14E	B14F	B14G	B14H	B14I	B14J	B14K	B14L	B14M	B14N	B14O	B14P	B14Q	B14R	B14S	B14T	B14U	B14V	B14W	B14X	B14Y	B14Z	B150	B151	B152	B153	B154	B155	B156	B157	B158	B159	B15A	B15B	B15C	B15D	B15E	B15F	B15G	B15H	B15I	B15J	B15K	B15L	B15M	B15N	B15O	B15P	B15Q	B15R	B15S	B15T	B15U	B15V	B15W	B15X	B15Y	B15Z	B160	B161	B162	B163	B164	B165	B166	B167	B168	B169	B16A	B16B	B16C	B16D	B16E	B16F	B16G	B16H	B16I	B16J	B16K	B16L	B16M	B16N	B16O	B16P	B16Q	B16R	B16S	B16T	B16U	B16V	B16W	B16X	B16Y	B16Z	B170	B171	B172	B173	B174	B175	B176	B177	B178	B179	B17A	B17B	B17C	B17D	B17E	B17F	B17G	B17H	B17I	B17J	B17K	B17L	B17M	B17N	B17O	B17P	B17Q	B17R	B17S	B17T	B17U	B17V	B17W	B17X	B17Y	B17Z	B180	B181	B182	B183	B184	B185	B186	B187	B188	B189	B18A	B18B	B18C	B18D	B18E	B18F	B18G	B18H	B18I	B18J	B18K	B18L	B18M	B18N	B18O	B18P	B18Q	B18R	B18S	B18T	B18U	B18V	B18W	B18X	B18Y	B18Z	B190	B191	B192	B193	B194	B195	B196	B197	B198	B199	B19A	B19B	B19C	B19D	B19E	B19F	B19G	B19H	B19I	B19J	B19K	B19L	B19M	B19N	B19O	B19P	B19Q	B19R	B19S	B19T	B19U	B19V	B19W	B19X	B19Y	B19Z	B200	B201	B202	B203	B204	B205	B206	B207	B208	B209	B20A	B20B	B20C	B20D	B20E	B20F	B20G	B20H	B20I	B20J	B20K	B20L	B20M	B20N	B20O	B20P	B20Q	B20R	B20S	B20T	B20U	B20V	B20W	B20X	B20Y	B20Z	B210	B211	B212	B213	B214	B215	B216	B217	B218	B219	B21A	B21B	B21C	B21D	B21E	B21F	B21G	B21H	B21I	B21J	B21K	B21L	B21M	B21N	B21O	B21P	B21Q	B21R	B21S	B21T	B21U	B21V	B21W	B21X	B21Y	B21Z	B220	B221	B222	B223	B224	B225	B226	B227	B228	B229	B22A	B22B	B22C	B22D	B22E	B22F	B22G	B22H	B22I	B22J	B22K	B22L	B22M	B22N	B22O	B22P	B22Q	B22R	B22S	B22T	B22U	B22V	B22W	B22X	B22Y	B22Z	B230	B231	B232	B233	B234	B235	B236	B237	B238	B239	B23A	B23B	B23C	B23D	B23E	B23F	B23G	B23H	B23I	B23J	B23K	B23L	B23M	B23N	B23O	B23P	B23Q	B23R	B23S	B23T	B23U	B23V	B23W	B23X	B23Y	B23Z	B240	B241	B242	B243	B244	B245	B246	B247	B248	B249	B24A	B24B	B24C	B24D	B24E	B24F	B24G	B24H	B24I	B24J	B24K	B24L	B24M	B24N	B24O	B24P	B24Q	B24R	B24S	B24T	B24U	B24V	B24W	B24X	B24Y	B24Z	B250	B251	B252	B253	B254	B255	B256	B257	B258	B259	B25A	B25B	B25C	B25D	B25E	B25F	B25G	B25H	B25I	B25J	B25K	B25L	B25M	B25N	B25O	B25P	B25Q	B25R	B25S	B25T	B25U	B25V	B25W	B25X	B25Y	B25Z	B260	B261	B262	B263	B264	B265	B266	B267	B268	B269	B26A	B26B	B26C	B26D	B26E	B26F	B26G	B26H	B26I	B26J	B26K	B26L	B26M	B26N	B26O	B26P	B26Q	B26R	B26S	B26T	B26U	B26V	B26W	B26X	B26Y	B26Z	B270	B271	B272	B273	B274	B275	B276	B277	B278	B279	B27A	B27B	B27C	B27D	B27E	B27F	B27G	B27H	B27I	B27J	B27K	B27L	B27M	B27N	B27O	B27P	B27Q	B27R	B27S	B27T	B27U	B27V	B27W	B27X	B27Y	B27Z	B280	B281	B282	B283	B284	B285	B286	B287	B288	B289	B28A	B28B	B28C	B28D	B28E	B28F	B28G	B28H	B28I	B28J	B28K	B28L	B28M	B28N	B28O	B28P	B28Q	B28R	B28S	B28T	B28U	B28V	B28W	B28X	B28Y	B28Z	B290	B291	B292	B293	B294	B295	B296	B297	B298	B299	B29A	B29B	B29C	B29D	B29E	B29F	B29G	B29H	B29I	B29J	B29K	B29L	B29M	B29N	B29O	B29P	B29Q	B29R	B29S	B29T	B29U	B29V	B29W	B29X	B29Y	B29Z	B300	B301	B302	B303	B304	B305	B306	B307	B308	B309	B30A	B30B	B30C	B30D	B30E	B30F	B30G	B30H	B30I	B30J	B30K	B30L	B30M	B30N	B30O	B30P	B30Q	B30R	B30S	B30T	B30U	B30V	B30W	B30X	B30Y	B30Z	B310	B311	B312	B313	B314	B315	B316	B317	B318	B319	B31A	B31B	B31C	B31D	B31E	B31F	B31G	B31H	B31I	B31J	B31K	B31L	B31M	B31N	B31O	B31P	B31Q	B31R	B31S	B31T	B31U	B31V	B31W	B31X	B31Y	B31Z	B320	B321	B322	B323	B324	B325	B326	B327	B328	B329	B32A	B32B	B32C	B32D	B32E	B32F	B32G	B32H	B32I	B32J	B32K	B32L	B32M	B32N	B32O	B32P	B32Q	B32R	B32S	B32T	B32U	B32V	B32W	B32X	B32Y	B32Z	B330	B331	B332	B333	B334	B335	B336	B337	B338	B339	B33A	B33B	B33C	B33D	B33E	B33F	B33G	B33H	B33I	B33J	B33K	B33L	B33M	B33N	B33O	B33P	B33Q	B33R	B33S	B33T	B33U	B33V	B33W	B33X	B33Y	B33Z	B340	B341	B342	B343	B344	B345	B346	B347	B348	B349	B34A	B34B	B34C	B34D	B34E	B34F	B34G	B34H	B34I	B34J	B34K	B34L	B34M	B34N	B34O	B34P	B34Q	B34R	B34S	B34T	B34U	B34V	B34W	B34X	B34Y	B34Z	B350	B351	B352	B353	B354	B355	B356	B357	B358	B359	B35A	B35B	B35C	B35D	B35E	B35F	B35G	B35H	B35I	B35J	B35K	B35L	B35M	B35N	B35O	B35P	B35Q	B35R	B35S	B35T	B35U	B35V	B35W	B35X	B35Y	B35Z	B360	B361	B362	B363	B364	B365	B366	B367	B368	B369	B36A	B36B	B36C	B36D	B36E	B36F	B36G	B36H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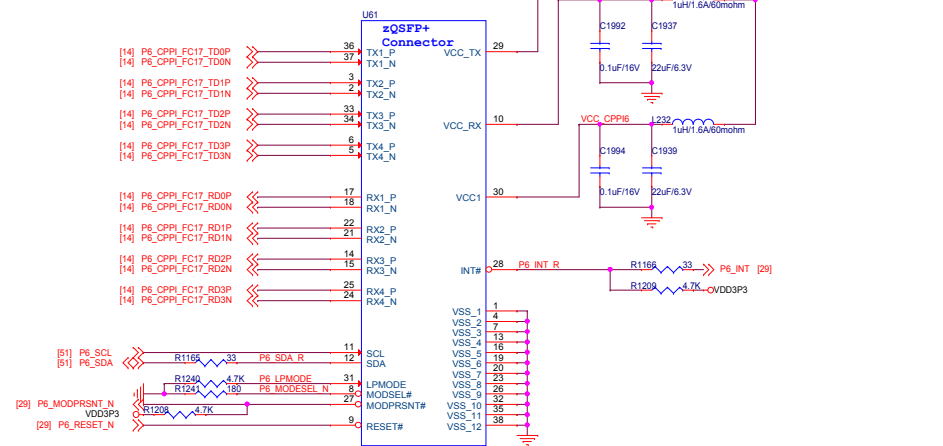




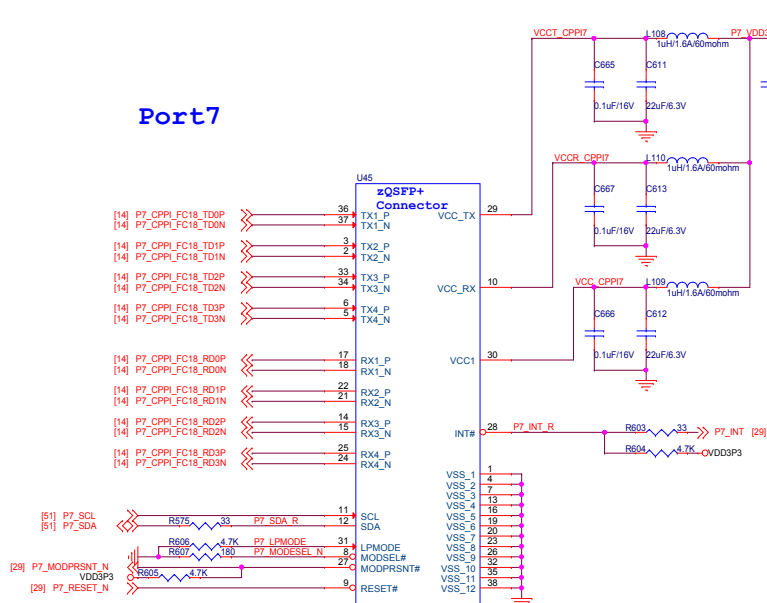
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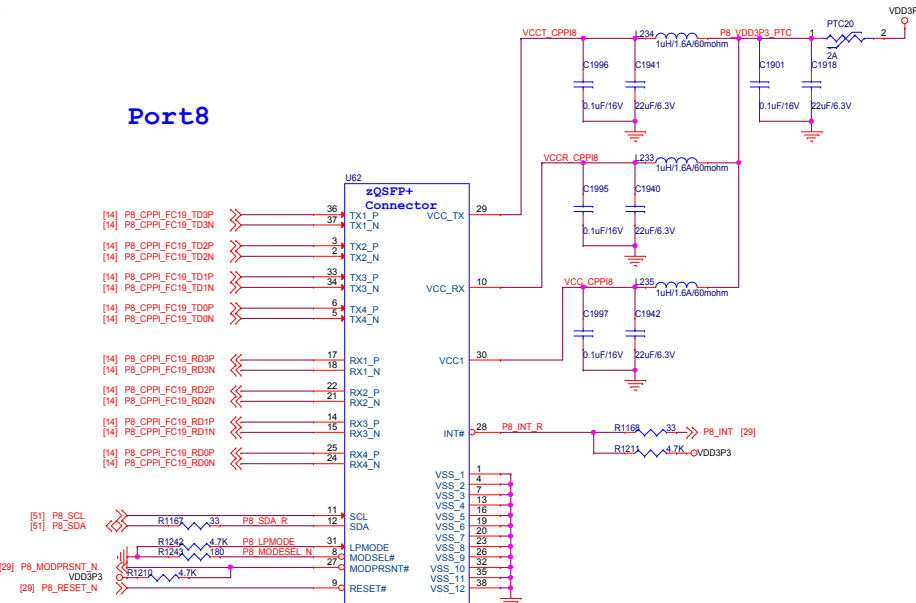
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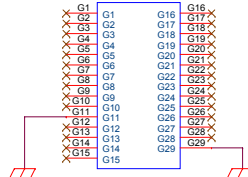
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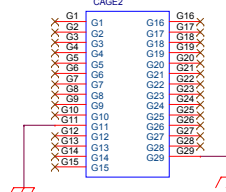
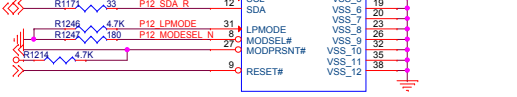
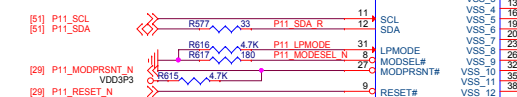
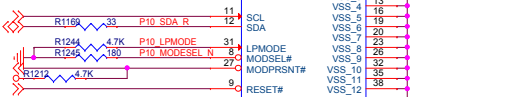
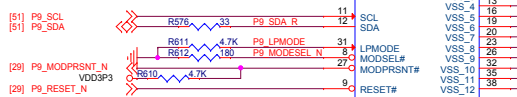


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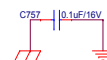
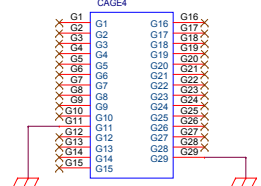
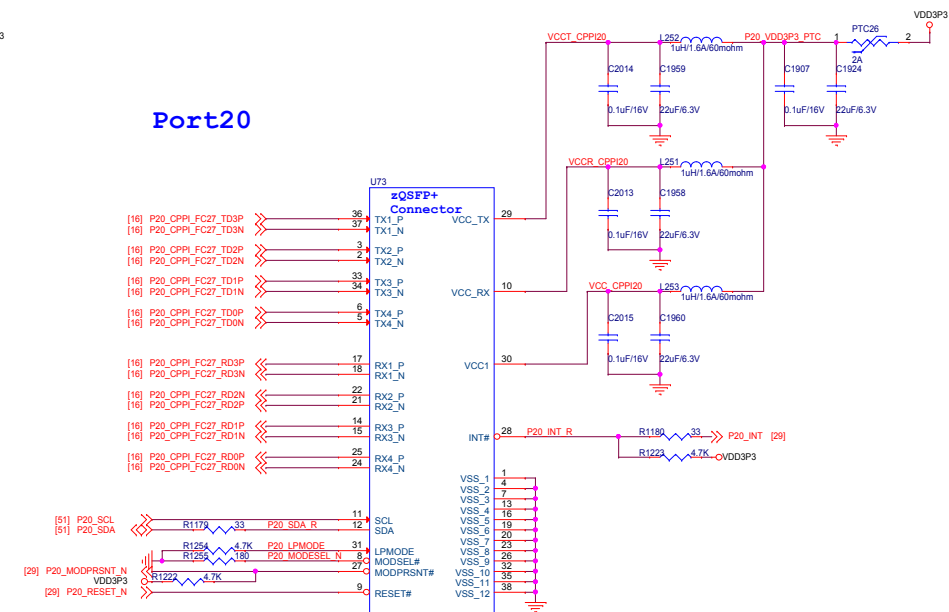
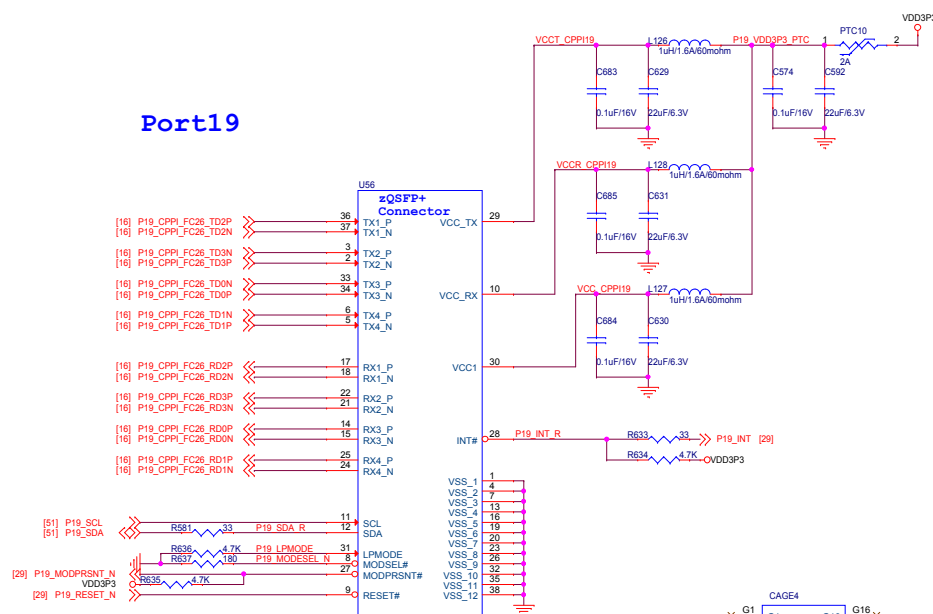
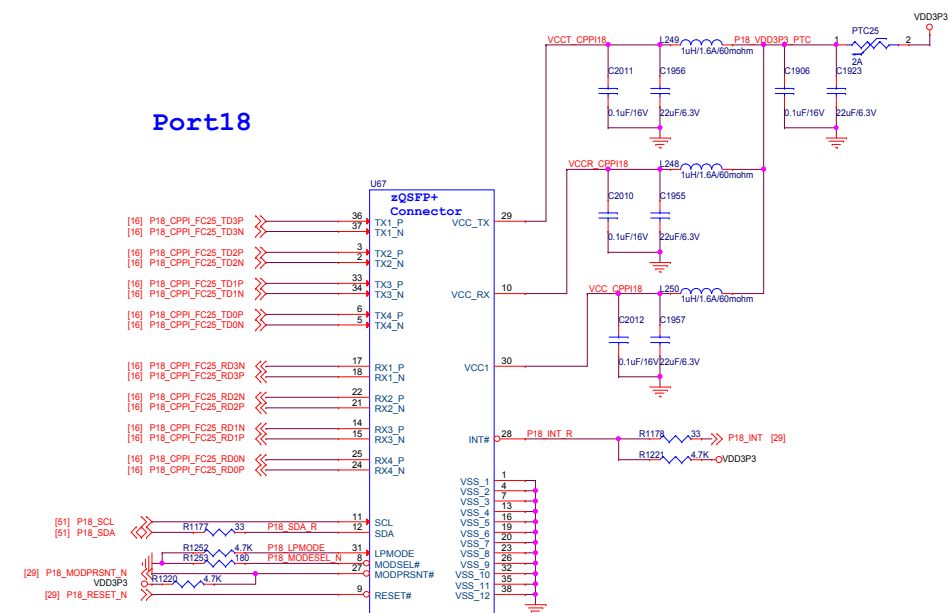
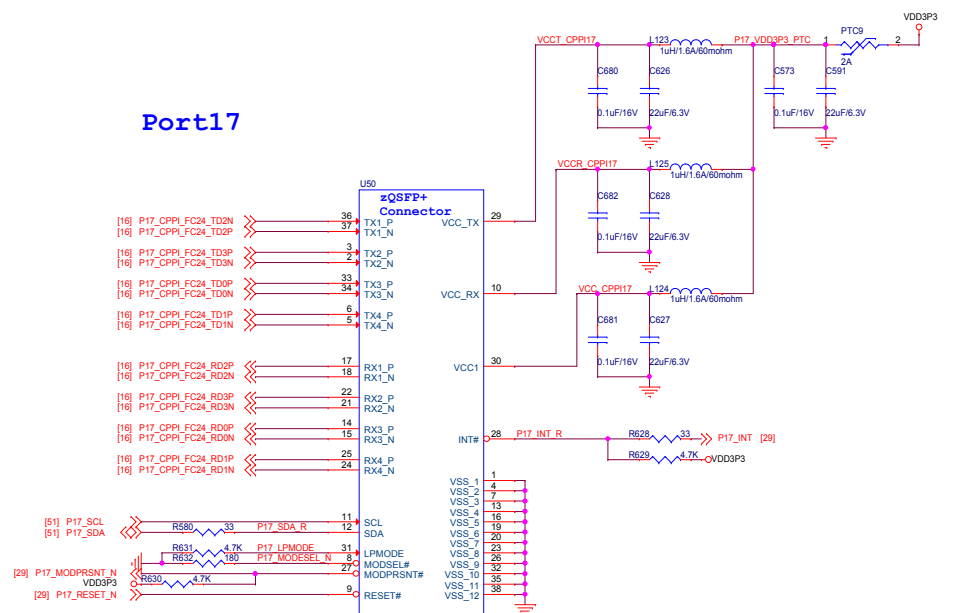
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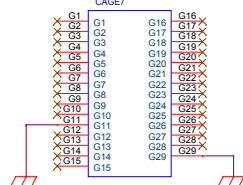
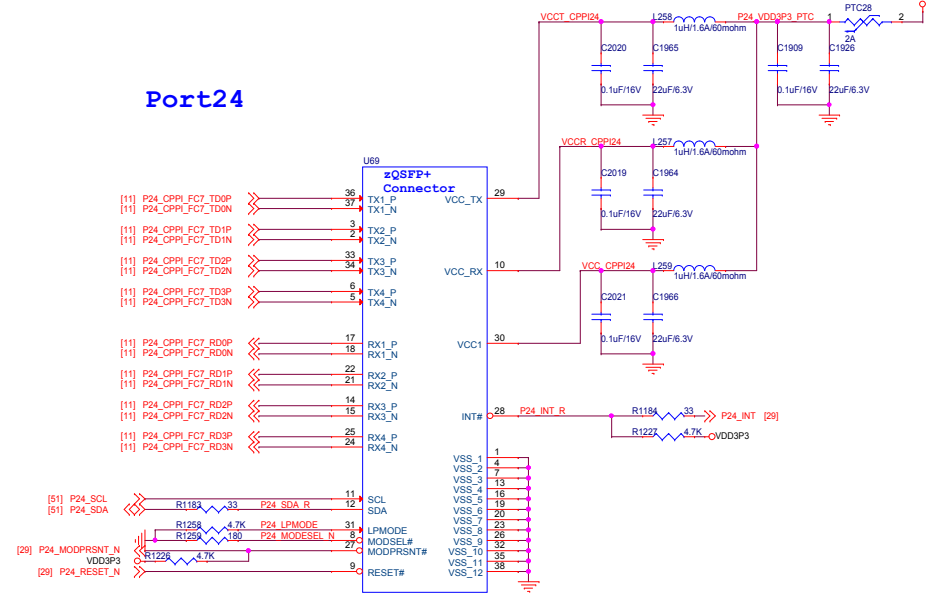
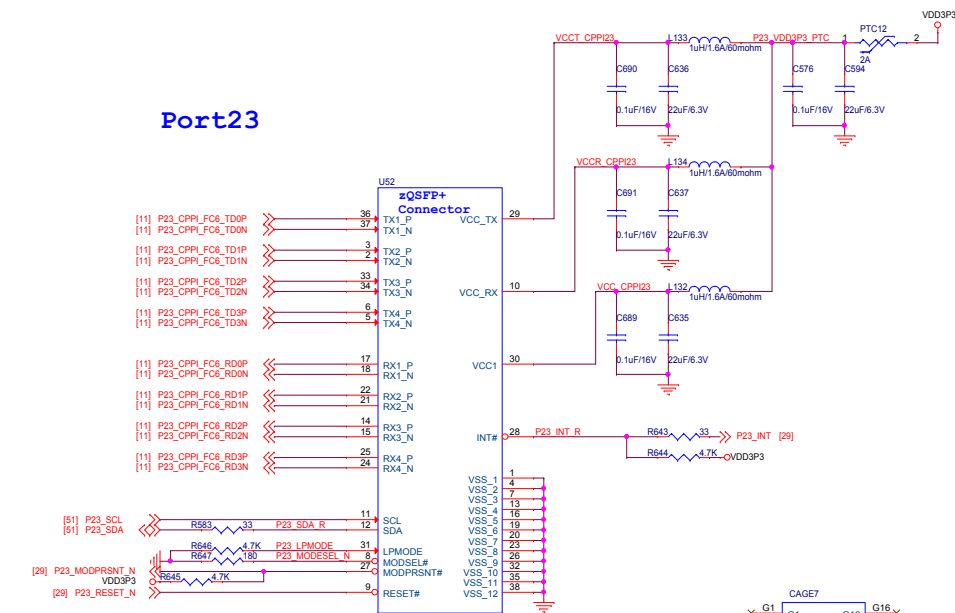
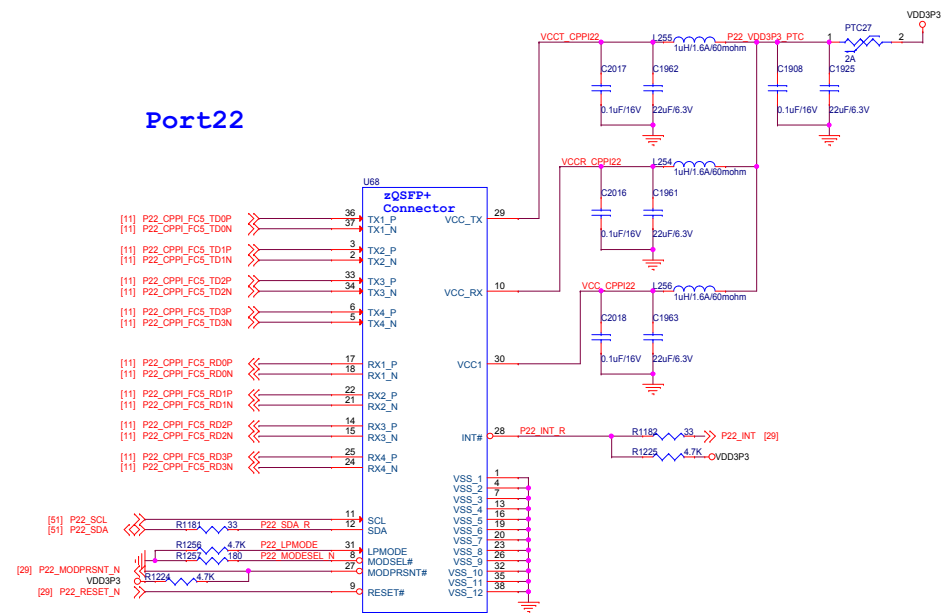
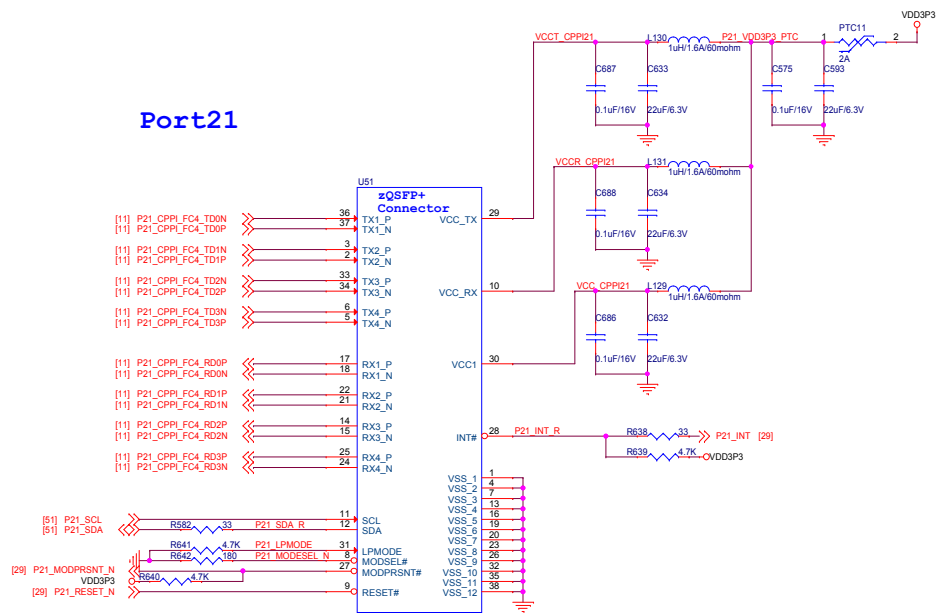




Accton Inc.

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Size	Page Name	QSFP28_P9-P12		Engineer	Fajung_Liu
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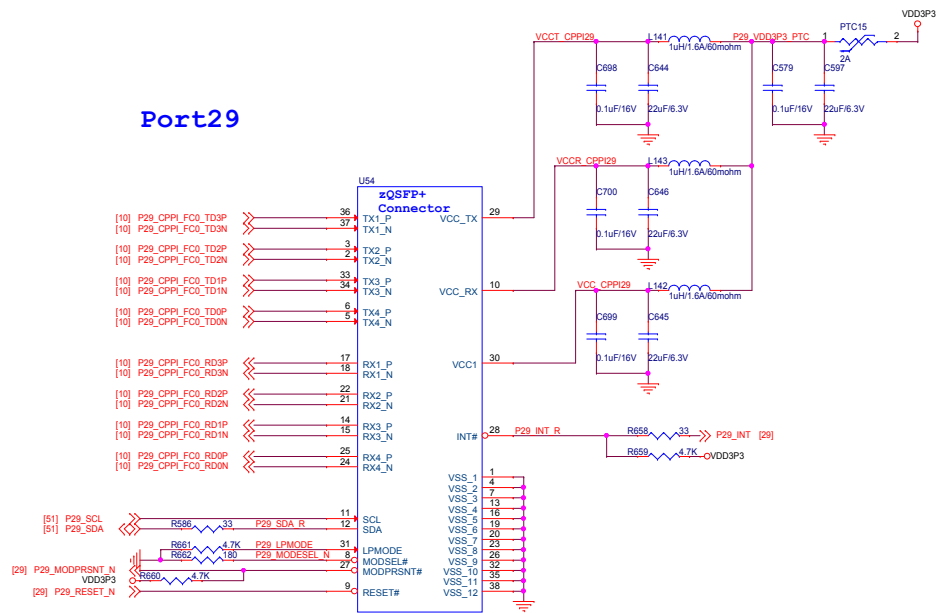




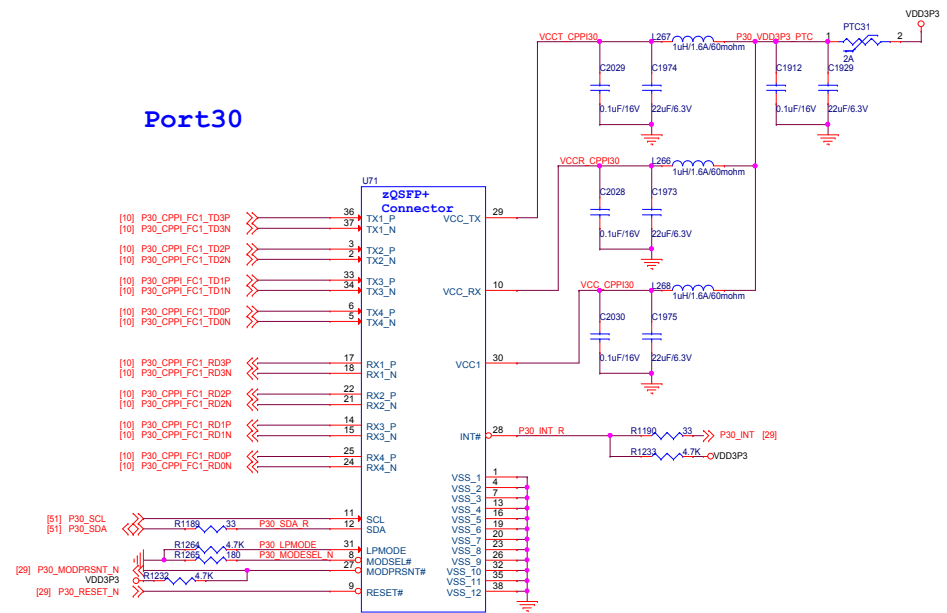
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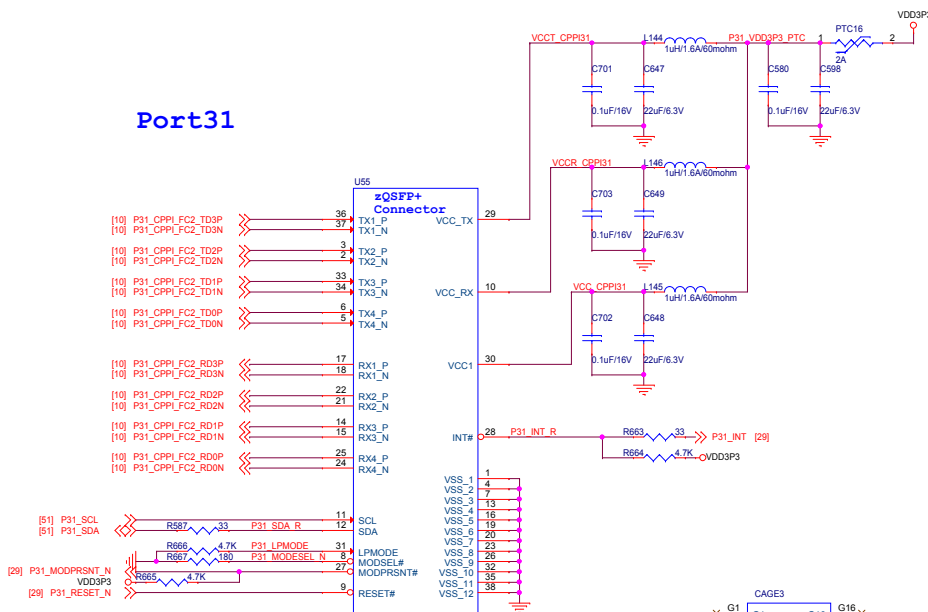
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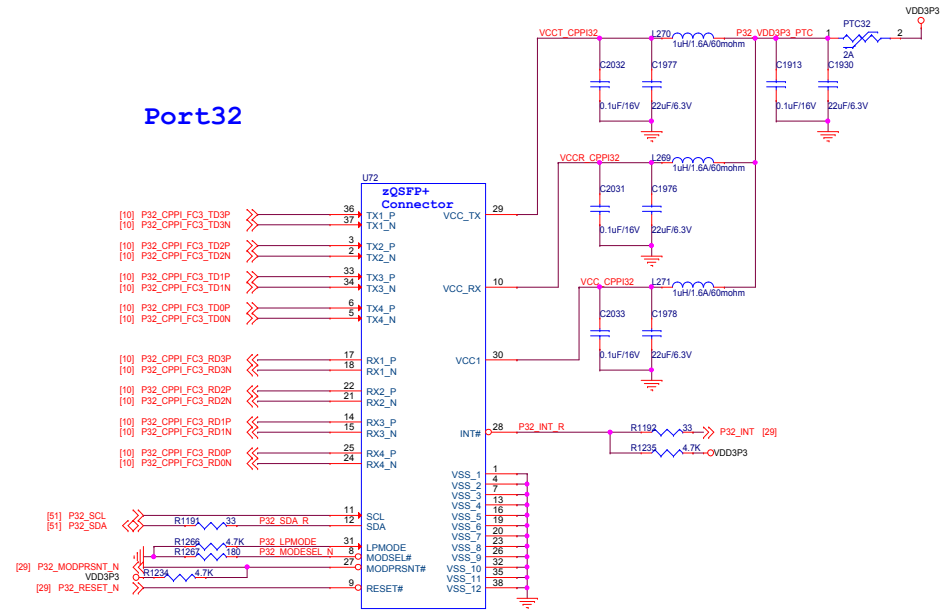
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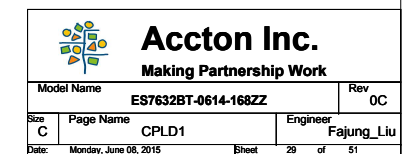


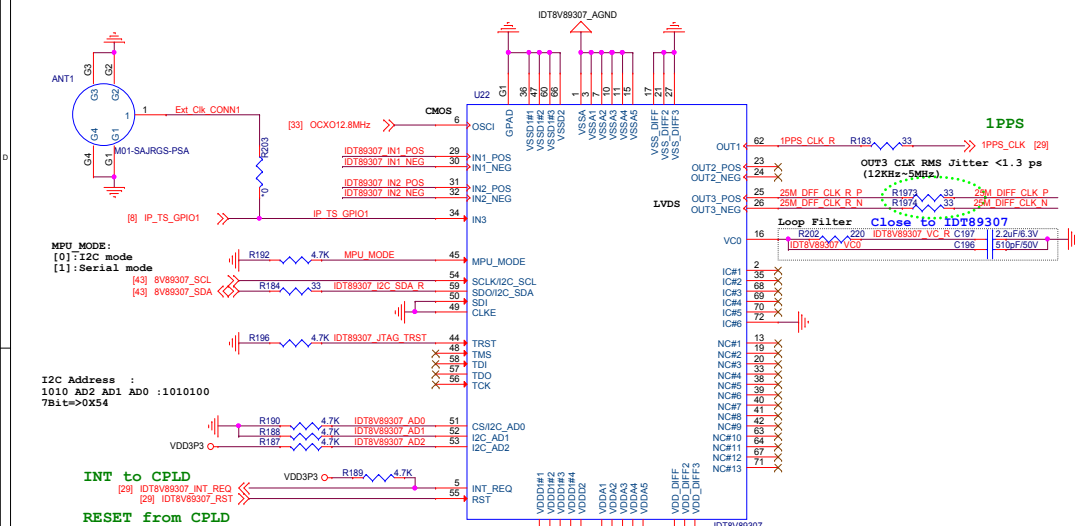
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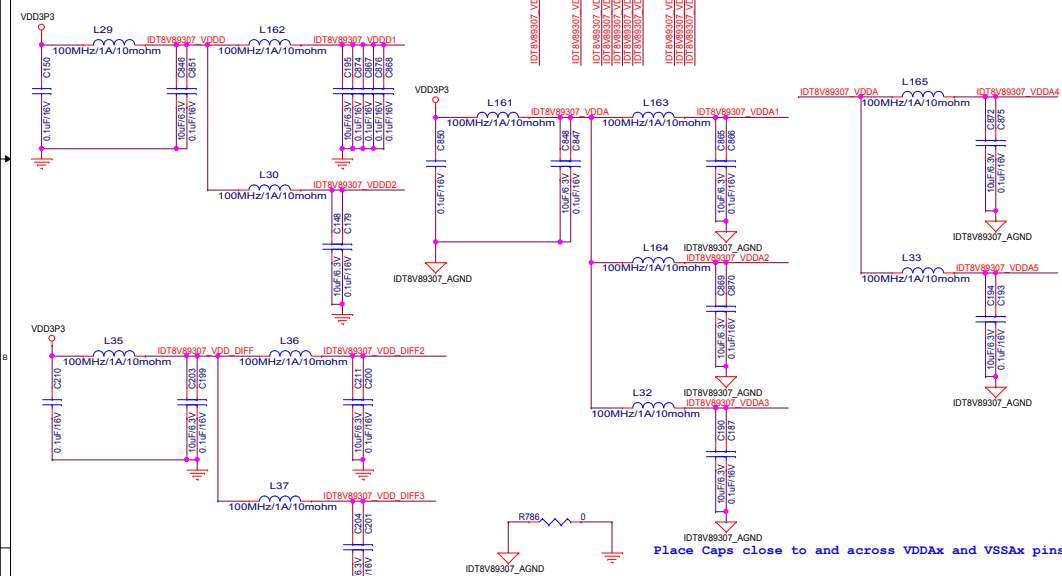
Accton Inc.
Making Partnership Work

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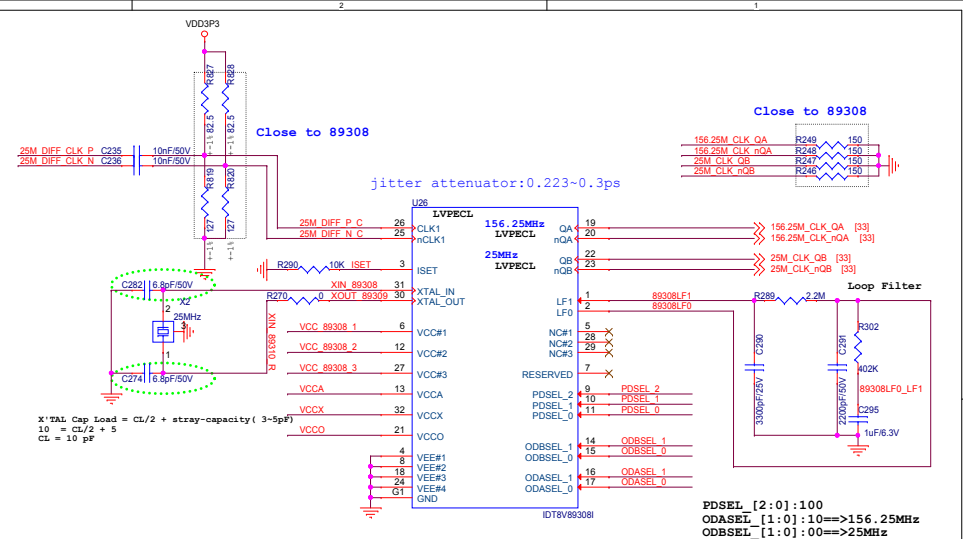
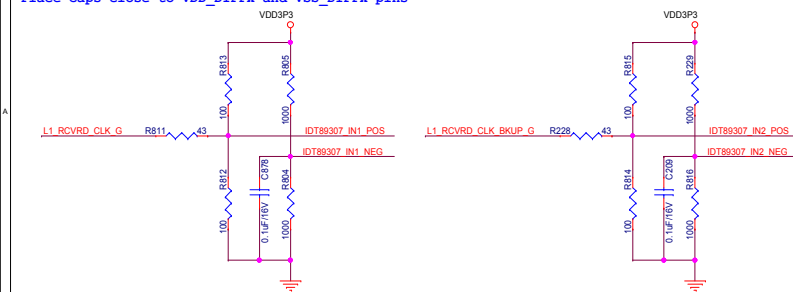


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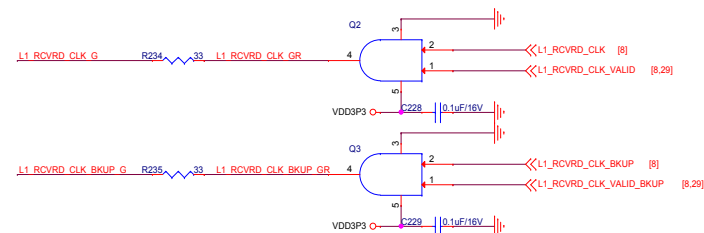
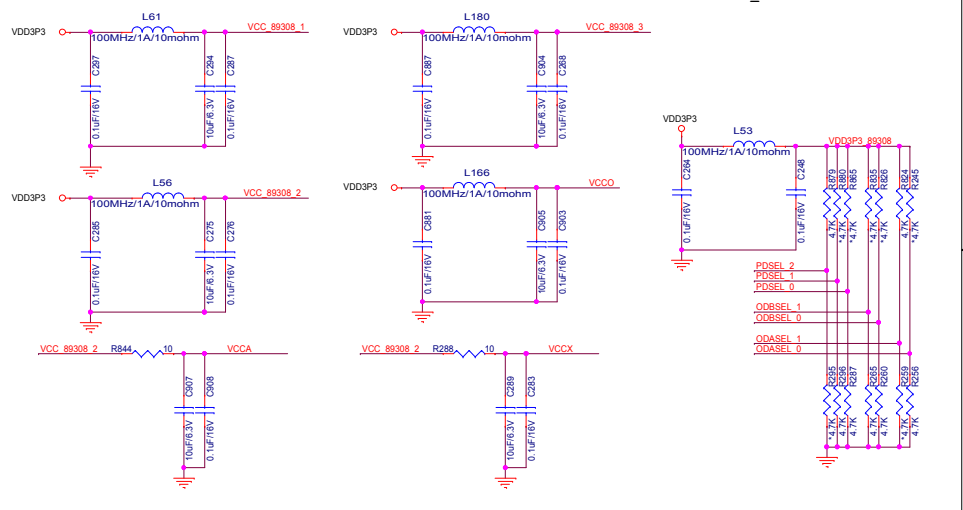


Place Caps close to and across VDDAx and VSSAx pins

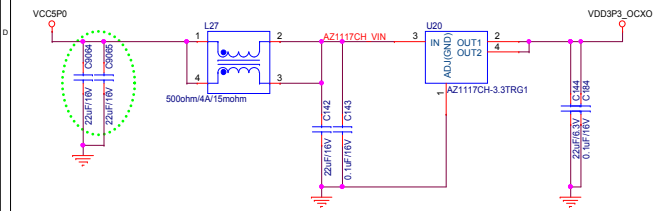
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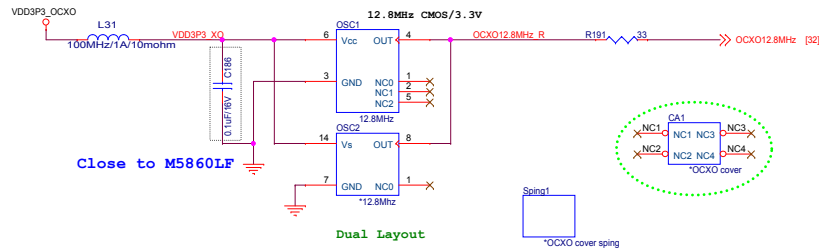
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PDSEL_ [2:0]:100
ODASEL_ [1:0]:10==>156.25MHz
ODBSEL_ [1:0]:00==>25MHz
```



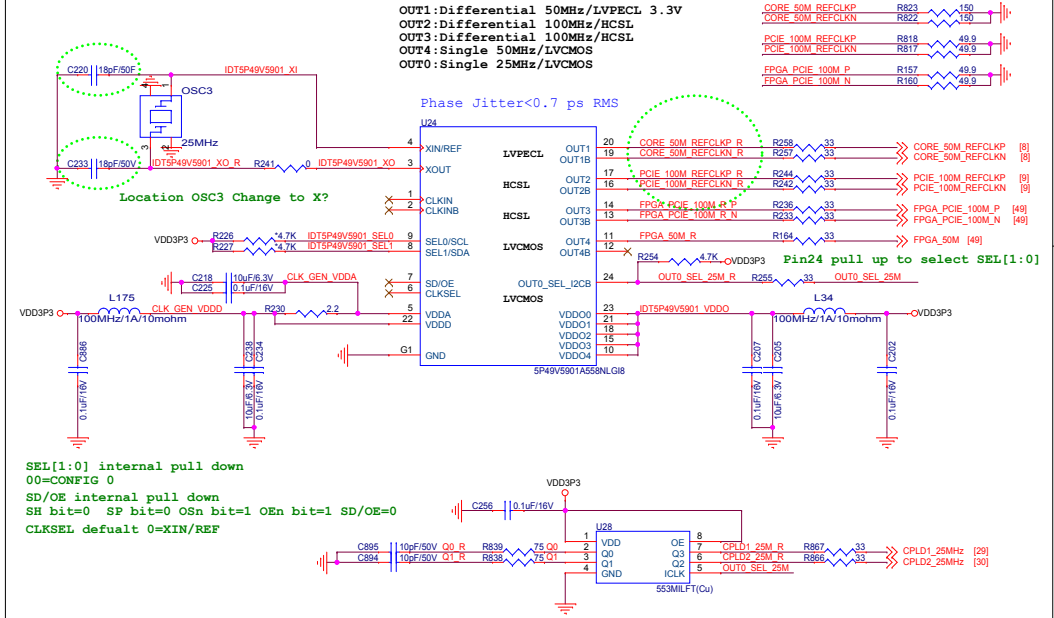
5V convert to 3.3V/0.5A For OCXO



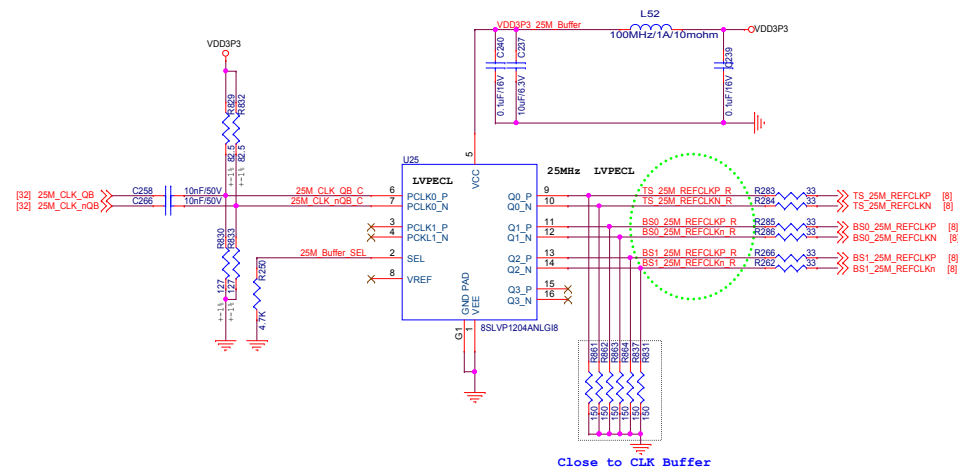
OCXO 12.8MHz For IDT8V89307



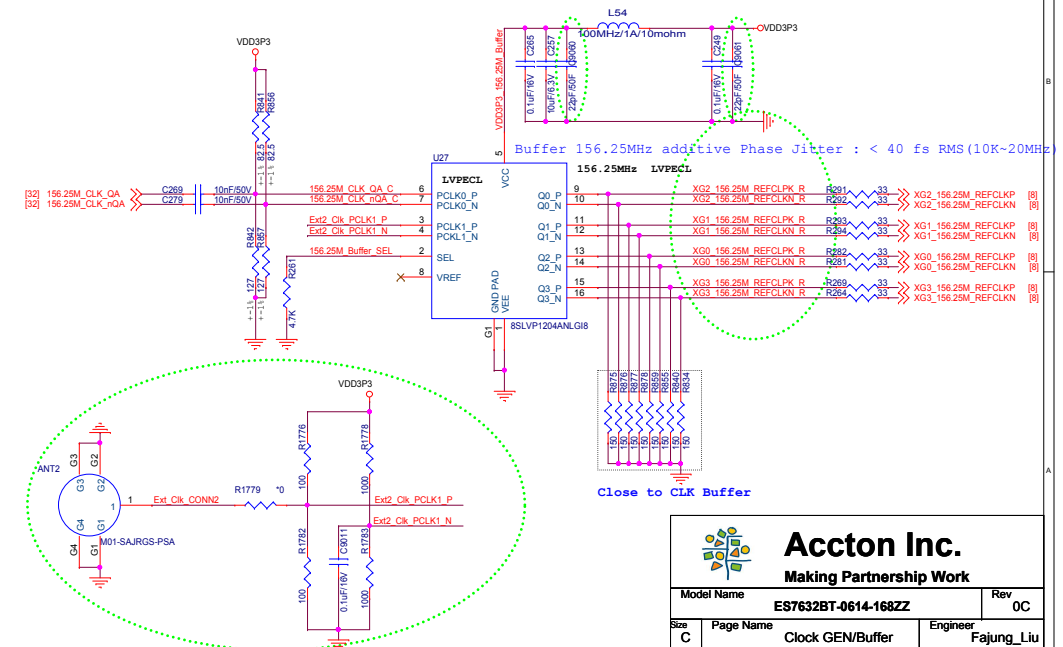
OUT1 50MHz For BCM56960
 OUT2 100MHz For BCM56960 PCIe
 OUT3 100MHz For FPGA PCIe
 OUT4 50MHz For FPGA
 OUT0 25MHz For CPLD

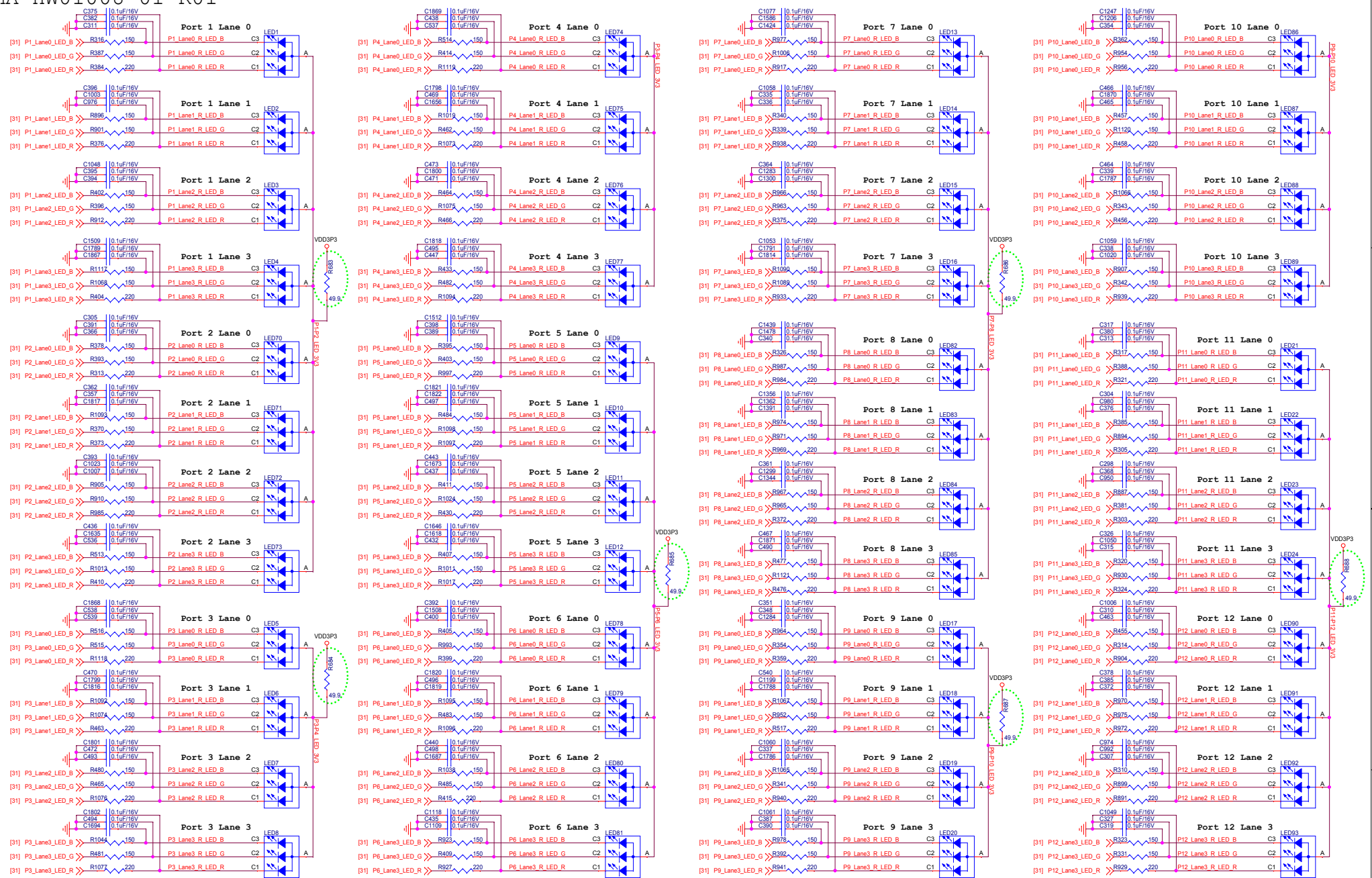


25MHz Buffer(1 to 3) For BCM56960



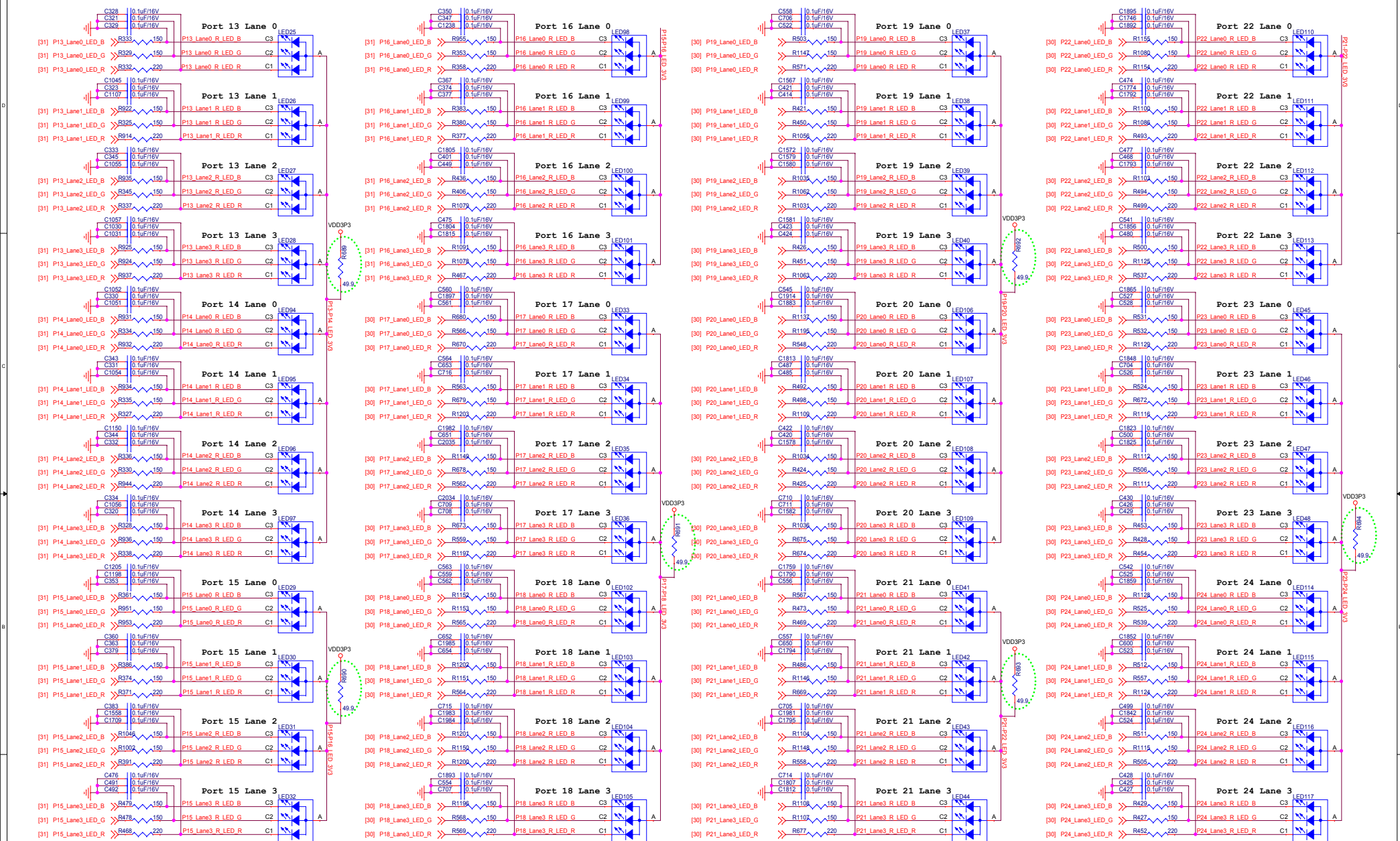
156.25MHz Buffer(1 to 4) For BCM56960

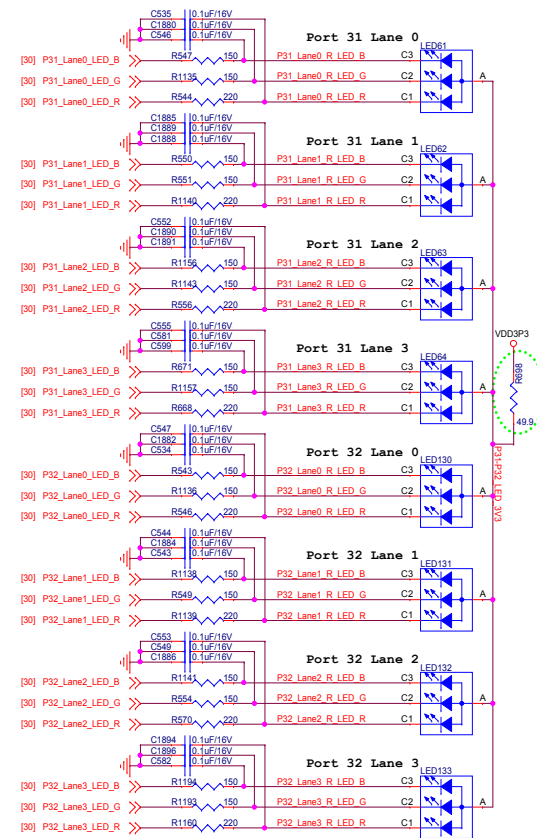
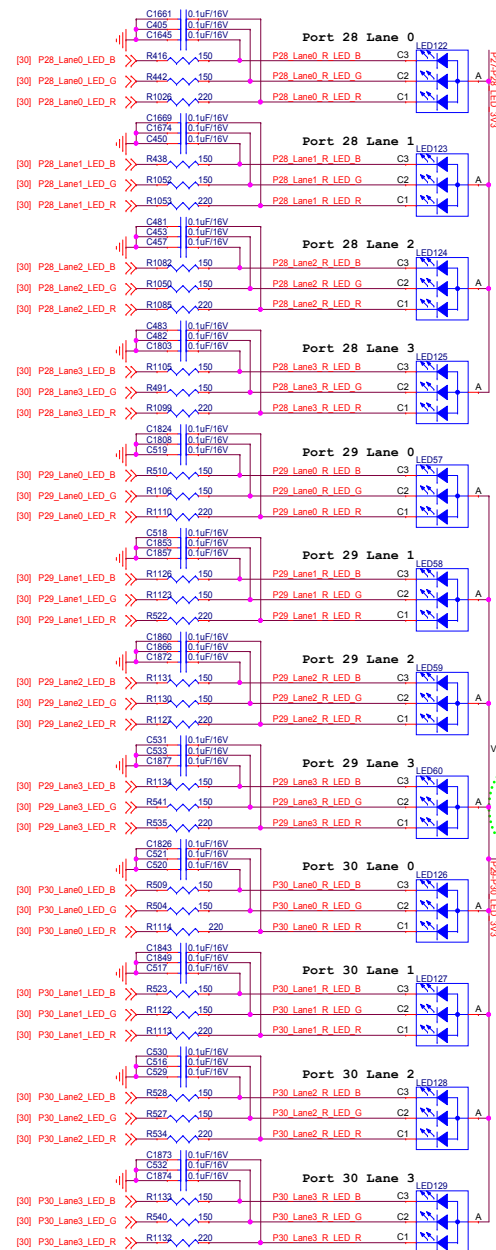
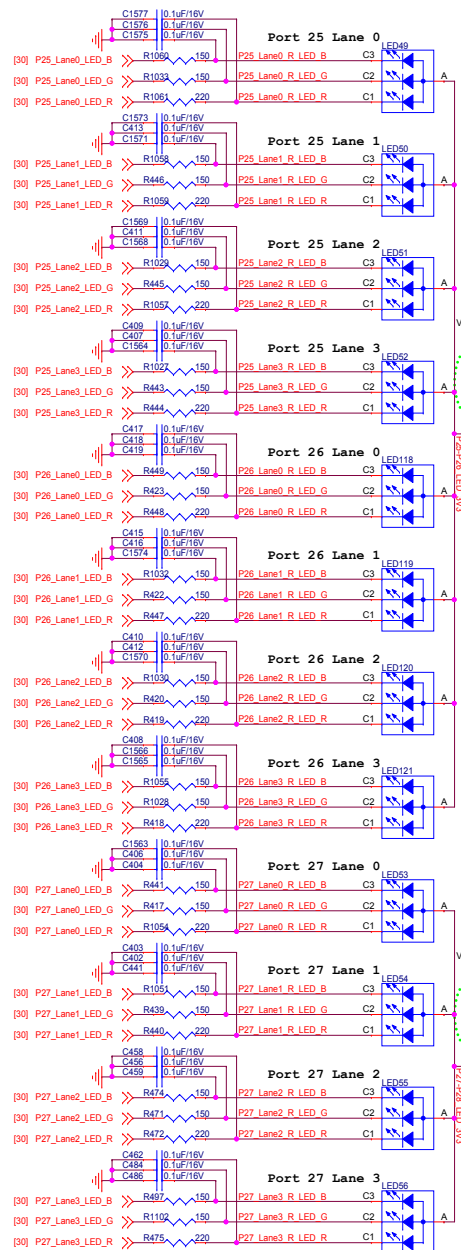




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Size	Page Name	LED_P1-P12	Engineer	Fajung_Liu
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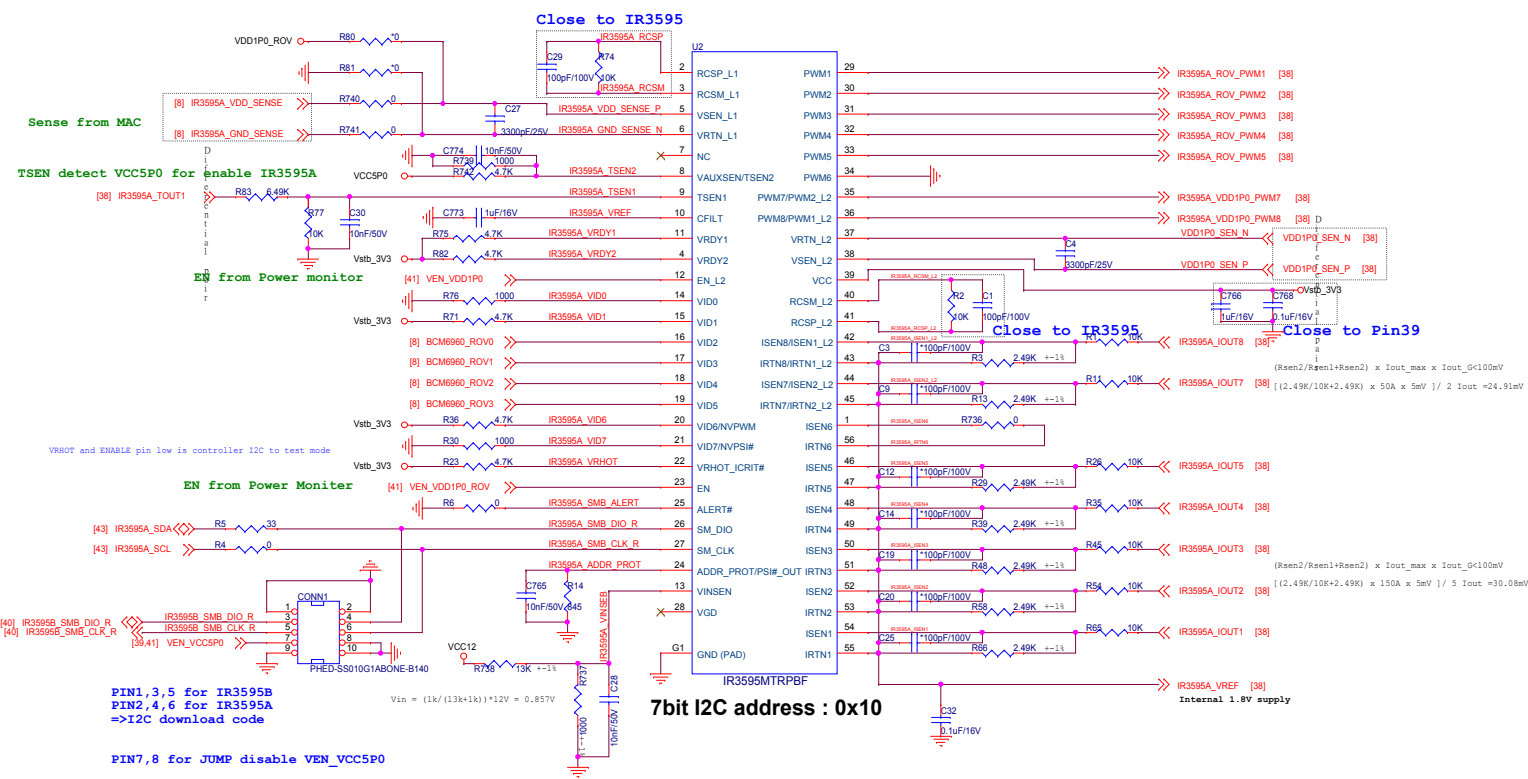


**Accton Inc.**

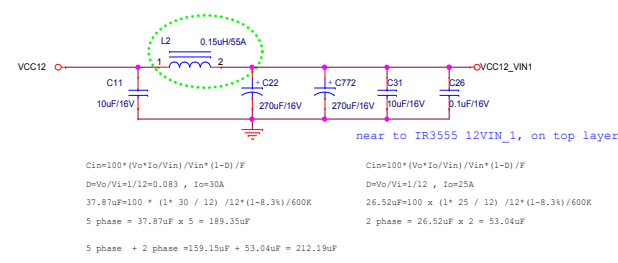
Making Partnership Work

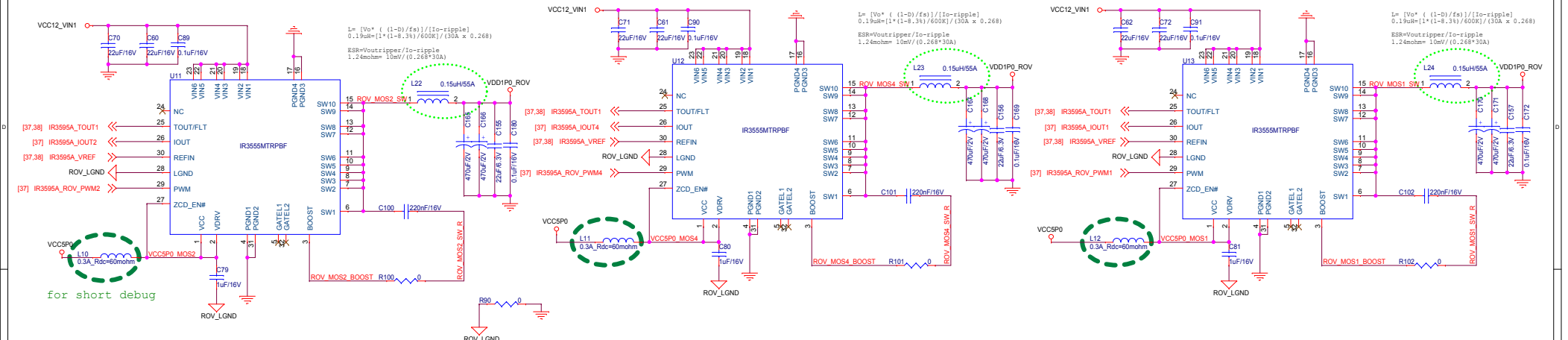
Model Name		ES7632BT-0614-168ZZ	Rev	0C
Size	Page Name	LED_P25-P32	Engineer	Fajung_Liu
C				
Date		Monday, June 08, 2015	Sheet	36 of 51

IR3595 Mutli-Phase
5Phase for VDD1P0_ROV
2Phase for VDD1P0



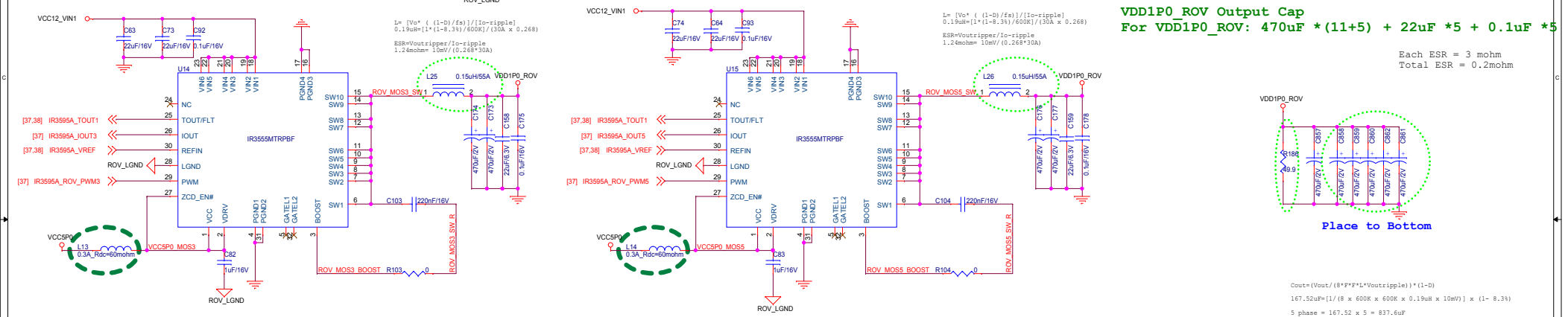
For MOS 12V Input





VDD1P0_ROV Output Cap
For VDD1P0_ROV: $470\mu\text{F} * (11+5) + 22\mu\text{F} * 5 + 0.1\mu\text{F} * 5$

Each ESR = 3 mohm
Total ESR = 0.2mohm

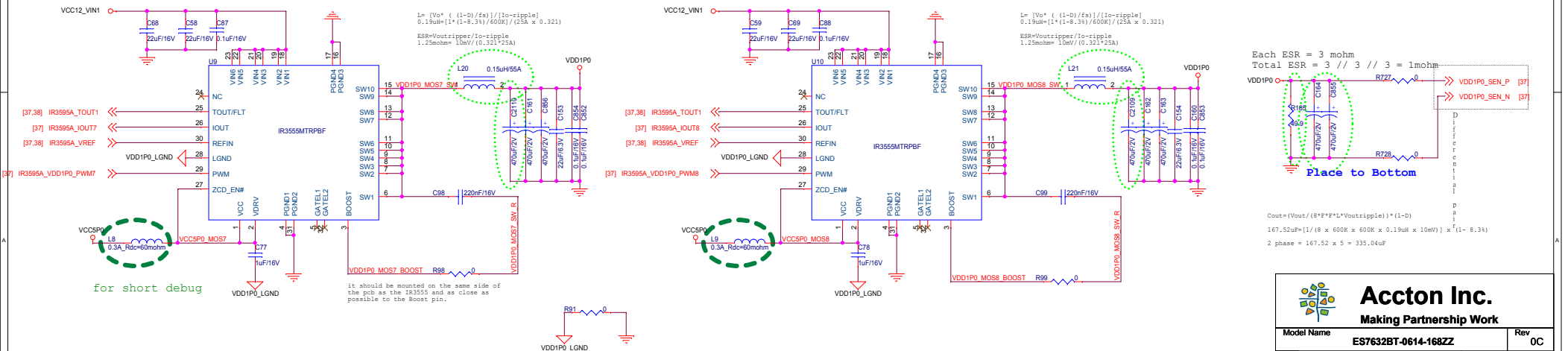

$$C_{out} = (V_{out} / (8 \cdot F \cdot F \cdot L \cdot V_{out_ripple})) \cdot (1 - D)$$
$$167.52 \mu F = [1 / (8 \times 600K \times 600K \times 0.19 \mu H \times 10mV)] \times (1 - 8.3\%)$$
$$5 \text{ phase} = 167.52 \times 5 = 837.6 \mu F$$

For VDD1P0_ROV @150A 5 Phase MOSFET

For VDD1P0 @50A 2 Phase MOSFET

VDD1P0 Output Cap

For VDD1P0: $470\mu\text{F} * (4+2) + 22\mu\text{F} * 4 + 0.1\mu\text{F} * 4$



Each ESR = 3 mohm
Total ESR = 3 // 3 // 3 = 1mohm.....

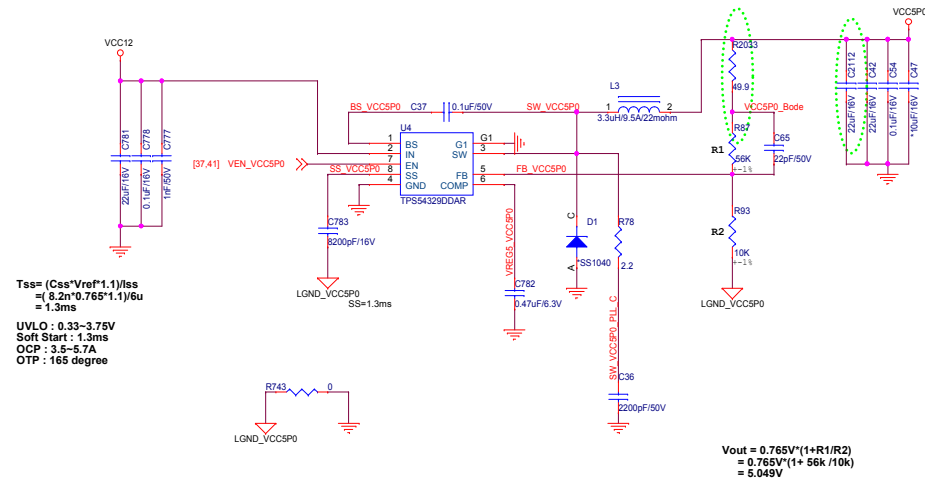
The schematic diagram shows the VDD1P0 pin connected to a network of capacitors (C164, C855) and a resistor (R727) leading to the VDD1P0_SEN_P and VDD1P0_SEN_N pins. A green dashed circle highlights the capacitors and the resistor.

 Place to Bottom
$$C_{out} = (V_{out} / (8 \cdot F \cdot F \cdot L \cdot V_{out_ripple})) \cdot (1 - D)$$

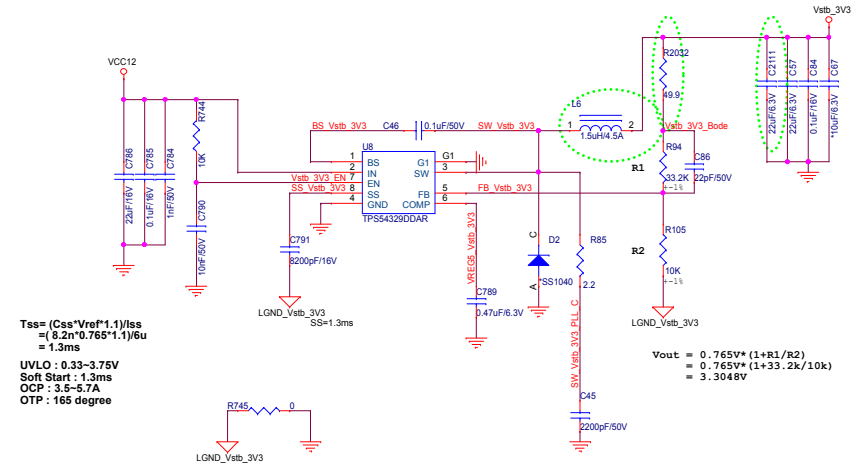
$$167.52 \mu F = [1 / (8 \times 600K \times 600K \times 0.19 \mu H \times 10mV)] \times (1 - 8.3\%)$$

$$2 \text{ phase} = 167.52 \times 5 = 335.04 \mu F$$

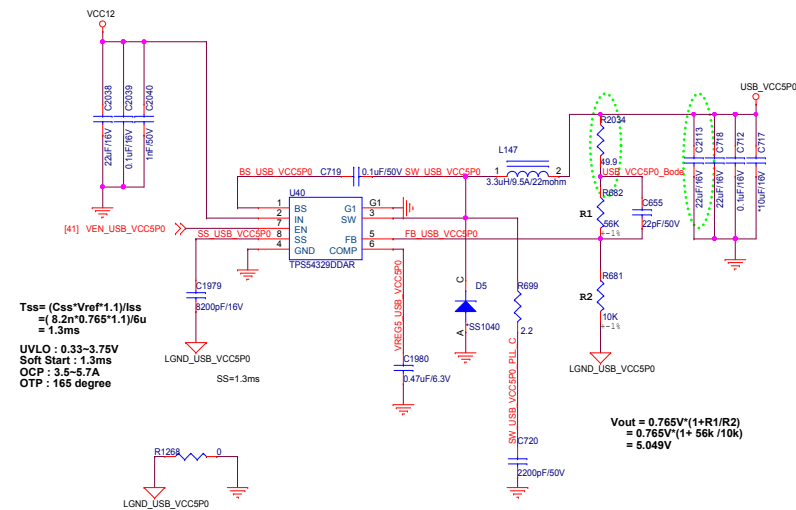
12V convert to 5V @3A For MOS Driver and CPU Board



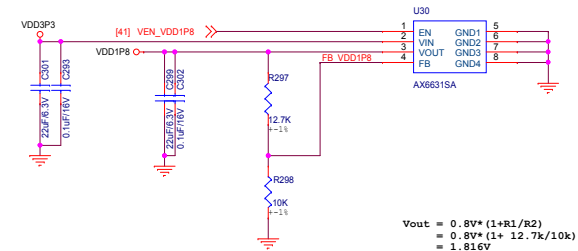
12V convert to 3.3V @0.5A For Standby Voltage (IR Control/Power Monitor)



12V convert to 5V @1A For USB



3.3V convert to 1.8V @0.8A For BCM56960 - CPLD

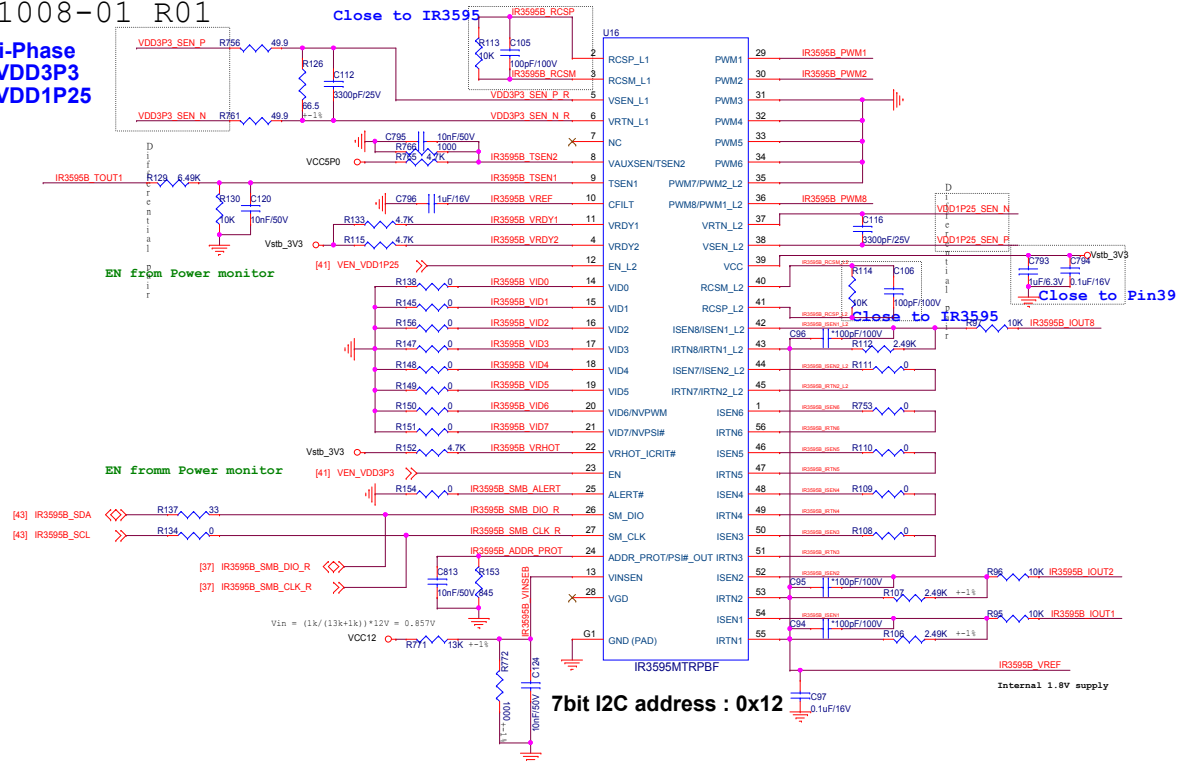


Guaranteed 1.2A output current

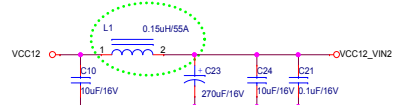

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Model Name		ES7632BT-0614-168ZZ	Rev	0C
Size	Page Name	PWR_5V/ Vstb_3.3V/1.8V	Engineer	Daniel_Yang
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IR3595 Mutli-Phase
2Phase for VDD3P3
1Phase for VDD1P25



For VDD3P3 @50A 2 Phase MOSFET

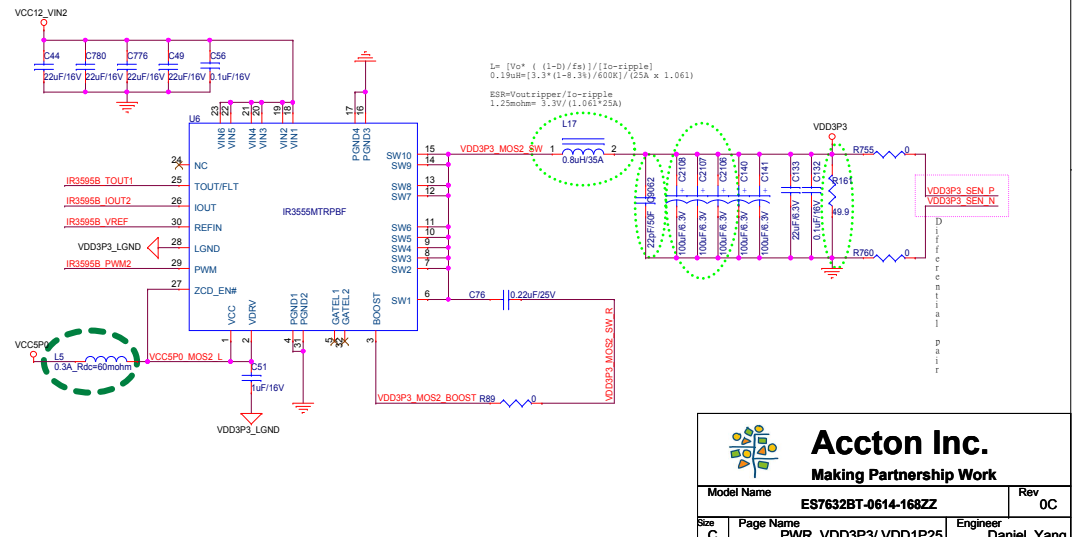
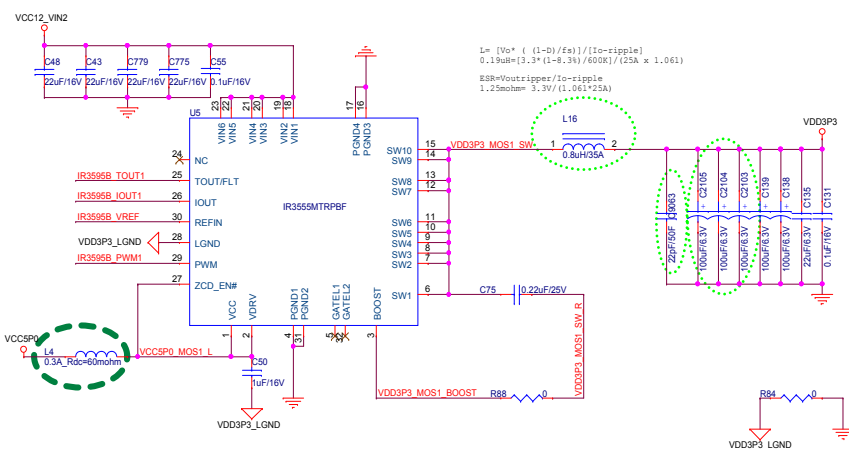


near to IR3555 VCC12VIN_2, on top layer

$Cin=100 \times (Vo \times Io / Vin) / Vin \times (1-D) / F$	$Cin=100 \times (Vo \times Io / Vin) / Vin \times (1-D) / F$
$87.53\mu F=100 \times (3.3 \times 25 / 12) / 12 \times (1-0.34) / 600K$	$6.63\mu F=100 \times (1.25 \times 5 / 12) / 12 \times (1-0.34) / 600K$
$2 \text{ phase} = 87.53\mu F \times 2 = 175.06\mu F$	$1 \text{ phase} = 6.63\mu F$
$2 \text{ phase} + 1 \text{ phase} = 175.06\mu F + 6.63\mu F = 181.69\mu F$	

VDD3P3 Output Cap
For VDD3P3: $100\mu\text{F} * 4 + 22\mu\text{F} * 2 + 0.1\mu\text{F} * 2$

```
Cout=(Vout/(8*F*F*L*Voutripple))*(1-D)
167.52uF=[3.3/(8 x 600K x 600K x 0.19uH x 33mV)] x (1- 8.3%)
2 phase = 167.52 x 2 = 335.04uF
```



For VDD1P25 @5A 1 Phase MOSFET

VDD1P25 Output Cap

For VDD1P25: $470\mu\text{F} * 1 + 22\mu\text{F} * 2 + 0.1\mu\text{F} * 2$

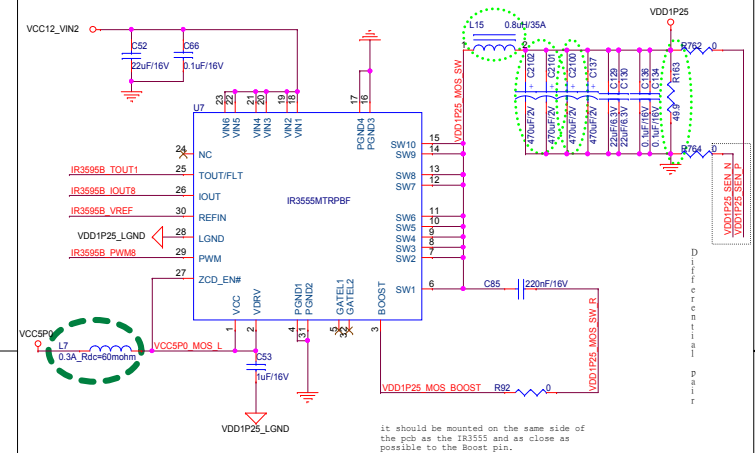
Each ESR = 3 mohm
Total ESR = 3 // 3 // 3 = 1mohm

```
Cout=(Vout/(8*F*L*Voutripple))*(1-D)
167.52uF=[1.25/(8 x 600K x 600K x 0.19uH x 12.5mV)] x (1- 8.3%)
1 phase = 167.52uF
```

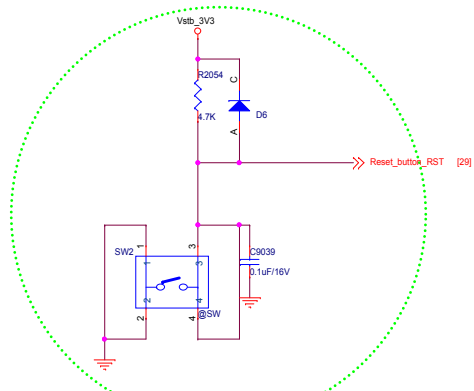


```
L= [Vo * ( (1-D)/fs)]/[Io-ripple]
0.19uH=[1.25*(1-8.3%)/600K]/(5A x 2.01)

ESR=Voutripper/Io-ripple
1.24mohm= 3.3V/(2.01*5A)
```

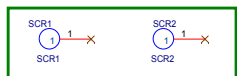


it should be mounted on the same side of the pcb as the IR3555 and as close as possible to the Boost pin.

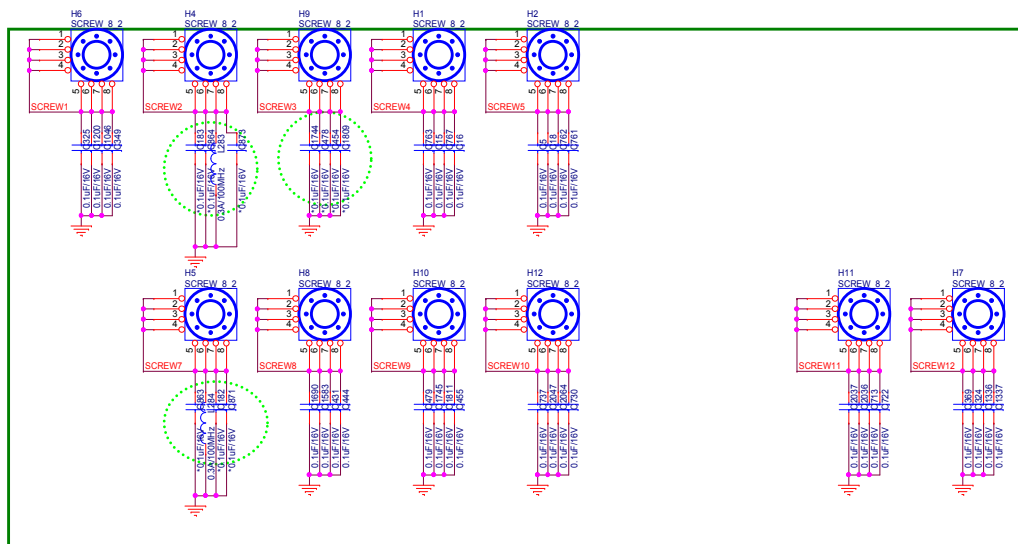


For Front port Reset Button(ZZ)

For Conn9 SCREW

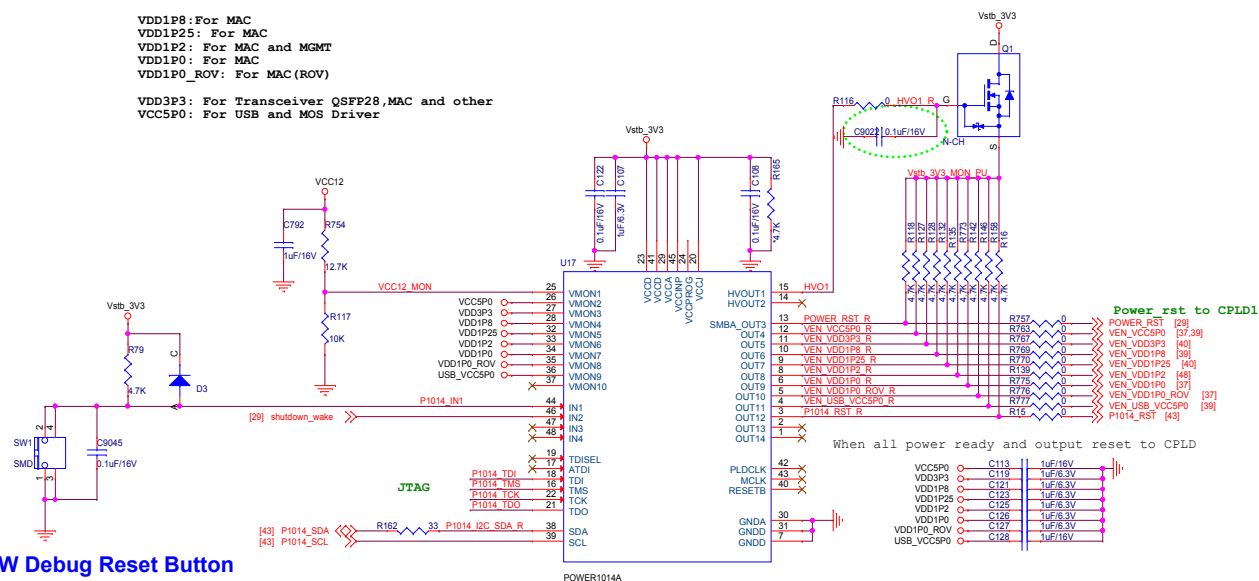


Mounting Hole



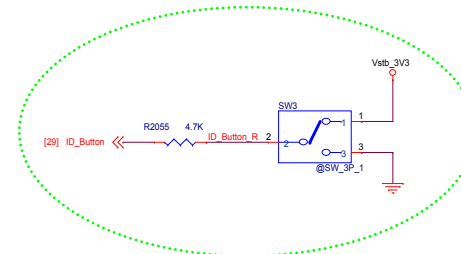
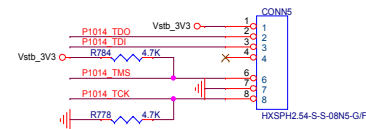
VDD1P8: For MAC
 VDD1P25: For MAC
 VDD1P2: For MAC and MGMT
 VDD1P0: For MAC
 VDD1P0_ROV: For MAC (ROV)

VDD3P3: For Transceiver QSFP28, MAC and other
 VCC5P0: For USB and MOS Driver



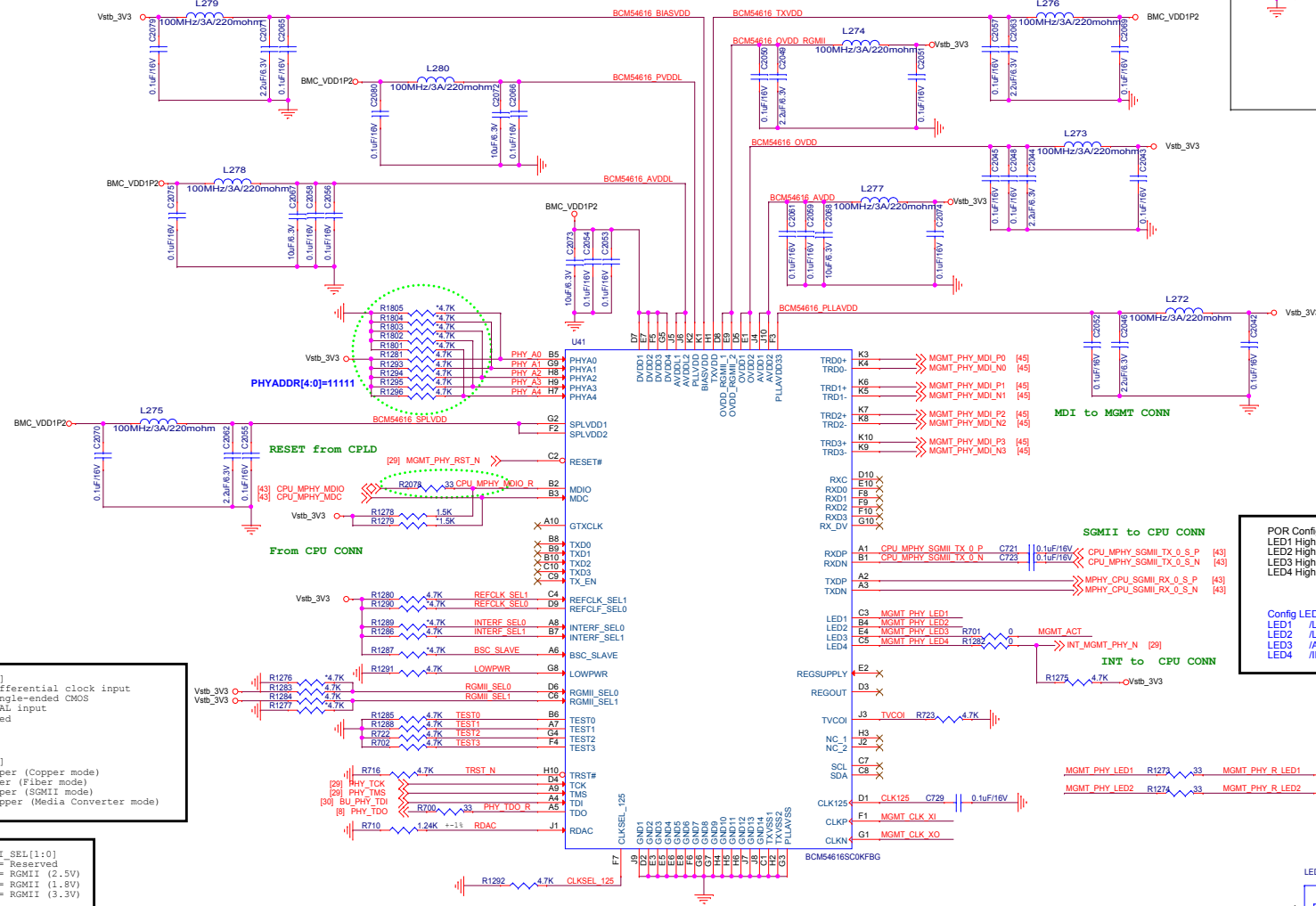
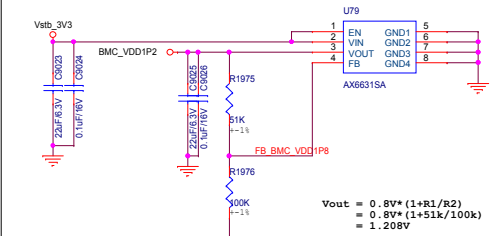
For HW Debug Reset Button

7bit I2C address : 0x25



For BMC ID Button(ZZ)

3V3 convert to 1.2V @3A For BMC MGMT PHY



REFCLK_SEL [1:0]
 00 = Select differential clock input
 01 = Select single-ended CMOS
 *10 = Select XTAL input
 11 = Not allowed

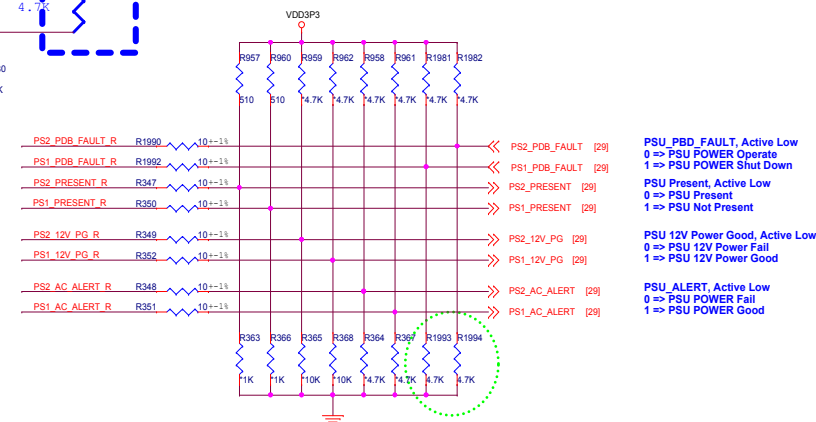
INTERF_SEL [1:0]
 00 = RGMII-Copper (Copper mode)
 01 = RGMII-Fiber (Fiber mode)
 *10 = SGMII-Copper (SGMII mode)
 11 = SerDes-Copper (Media Converter mode)

RGMII_SEL [1:0]
 00 = Reserved
 01 = RGMII (2.5V)
 10 = RGMII (1.8V)
 *11 = RGMII (3.3V)

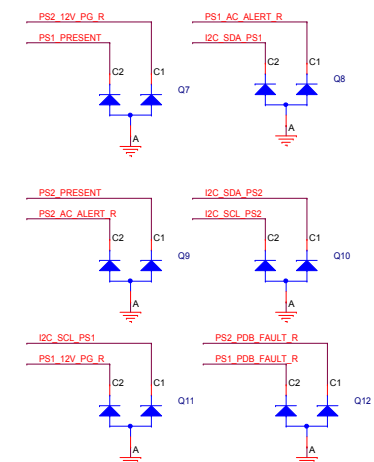
CLKSEL_125
 *0 = 25MHz input reference clock
 1 = 125MHz input reference clock

X'TAL Cap Load = CL/2 + stray-capacity(3~5pF)
 16 = CL/2 + 5
 CL = 22 pF

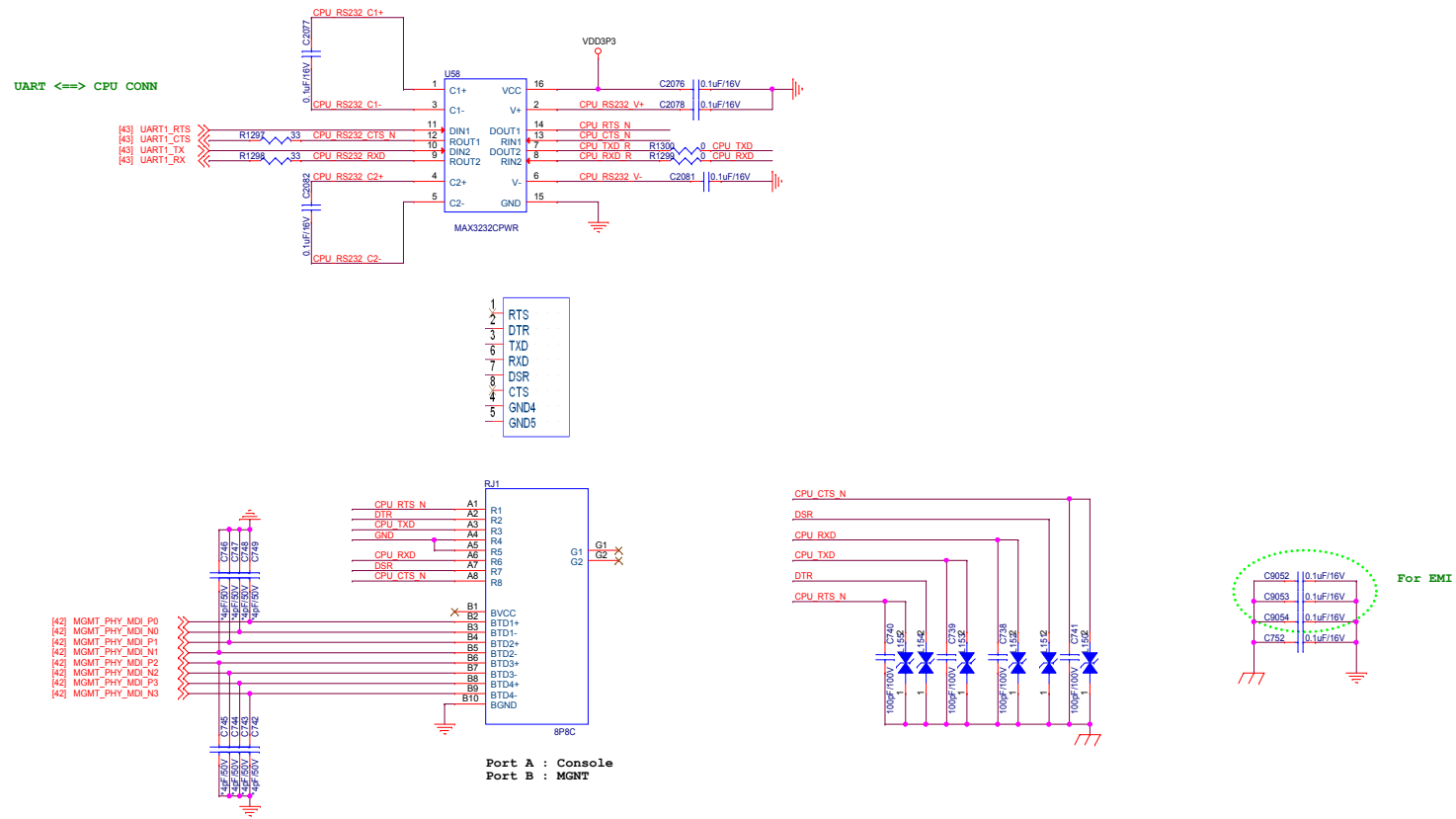
Power Name	Volt	Description
DVDD	1.2V	for digital core
OVDD	3.3V	for non RGMII pads
OVDD_RGMII	3.3V	In SGMII mode, 2.5V or 3.3V should be provided
AVDDL	1.2V	for analog core
AVDD	3.3V	for analog core
PLLVD	1.2V	PLL supply
BIASVDD	3.3V	Bias VDD
TXVDD	1.2V	transmitter and receiver of the SerDes
SPLLVD	1.2V	to the SerDer PLL
PLLAVDD33	3.3V	to the reference clock of SerDes PLL



Pin Name	Description
PS On	0 : 12V output 1 : 12V not output
PS 12V OK	1 : 12V output Good 0 : 12V output fail
PS ALERT	1 : OK 0 : Alert to system
PS Present	0 : Present 1 : Not present



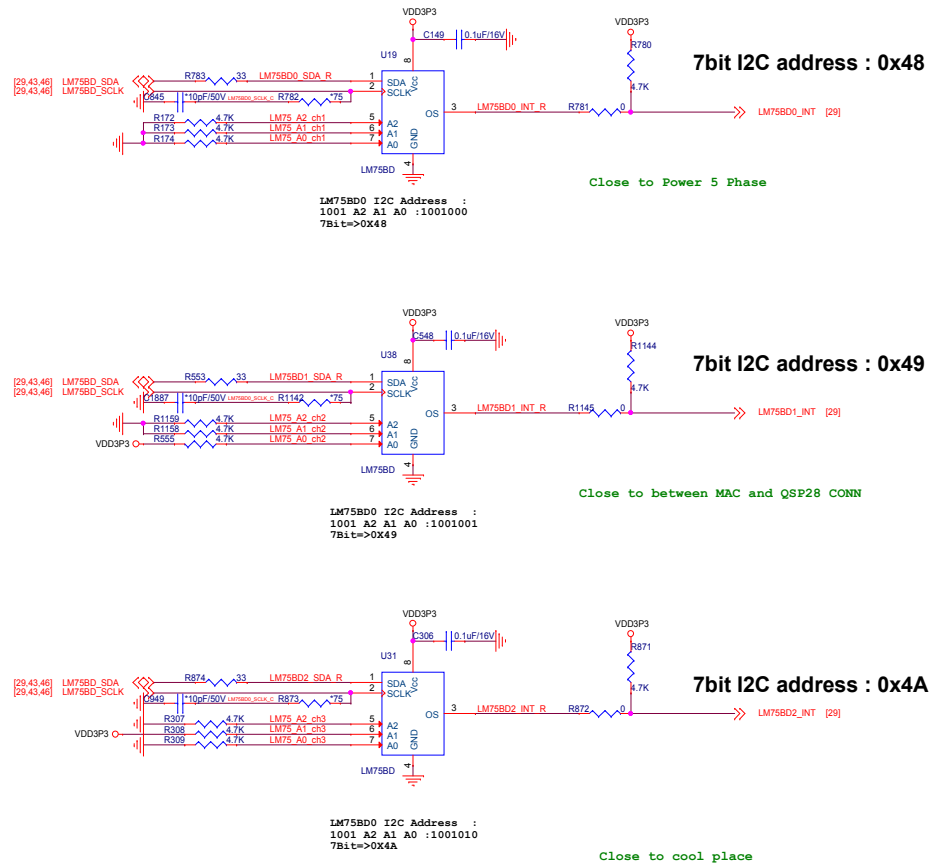
Pin connection diagram for the BH20E-224S4 module. The diagram shows a 24-pin connector labeled 'CONN2' with pins numbered 1 to 24. On the left, pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, and 23 are connected to VCC12. Pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, and 24 are connected to GND. On the right, pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, and 23 are connected to VCC12. Pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, and 24 are connected to GND. Signal pins include FAN_SDA_1 (pin 10), FAN_SDA_2 (pin 11), FAN_INT_L (pin 12), and FAN_SCL_1 (pin 13).



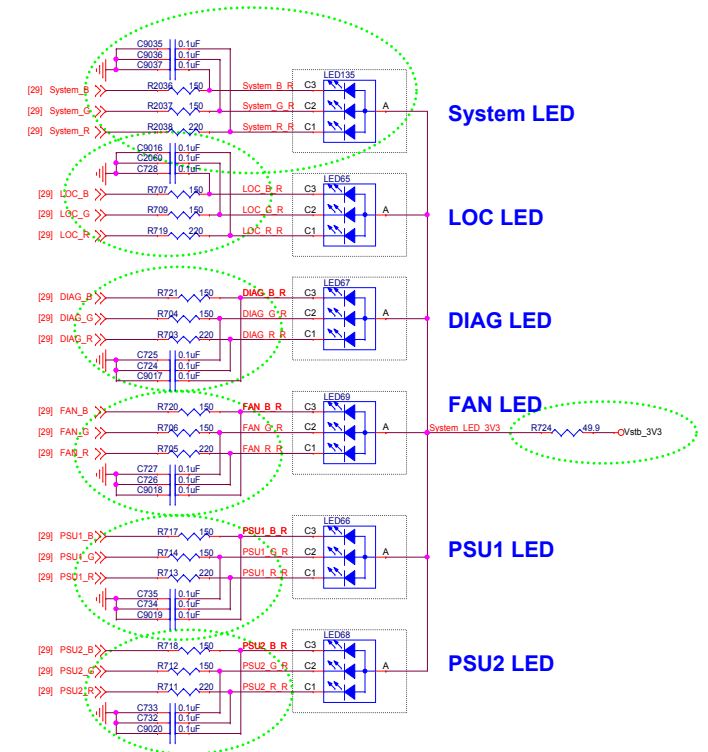
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Size	Page Name	Console/MGMT CONN	Engineer	Fajung_Liu
C				
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Thermal sensor

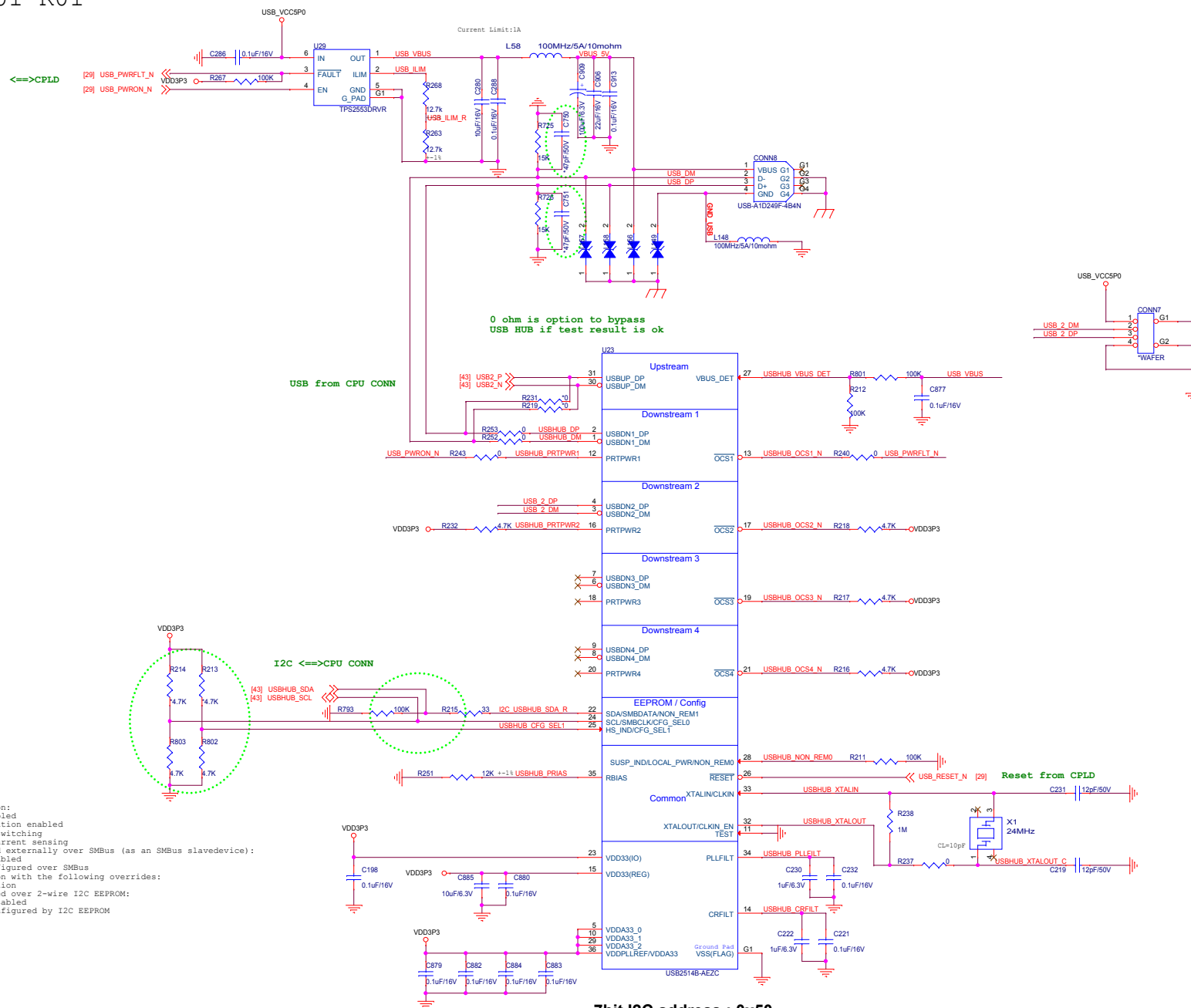


System LED



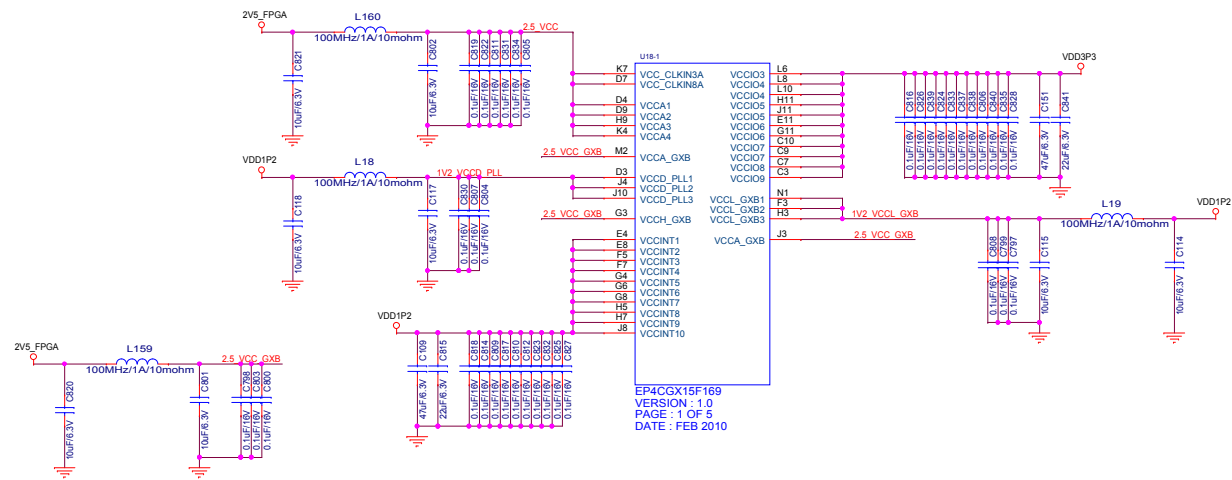
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Size	Page Name	Engineer		
C	Thermal Sensor/SYS LED	Fajung_Liu		
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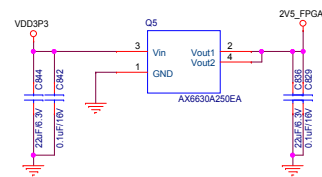


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Model Name		ES7632BT-0614-168ZZ	Rev	OC
Size	Page Name	USB	Engineer	Fajung_Liu
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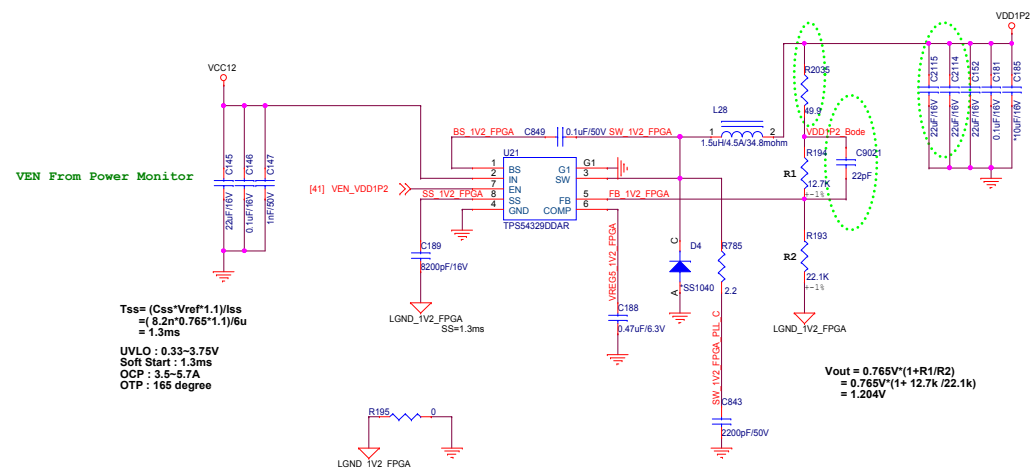


3.3V convert to 2.5V @0.3A For FPGA



Guaranteed 1.2A output current

12V convert to 1.2V @2A For FPGA and BCM54616s



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Model Name		Rev
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Size	Page Name	Engineer
C	FPGA_Power/PWR_2.5V/1.2V	Fajung_Liu
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