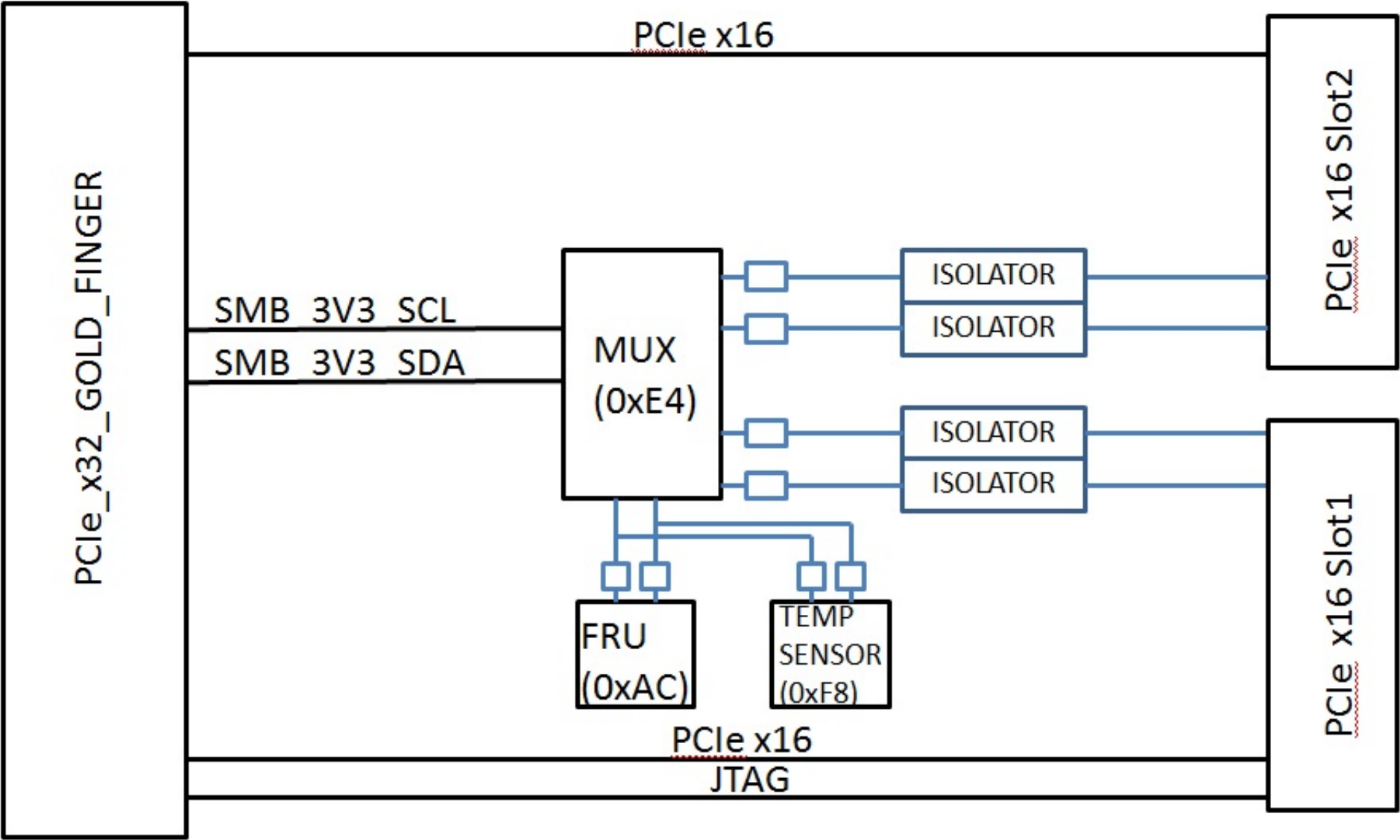


CPU PCIE X32 RISER_4_PCIEX16X16

DATE : 08 - AUGUST - 2018

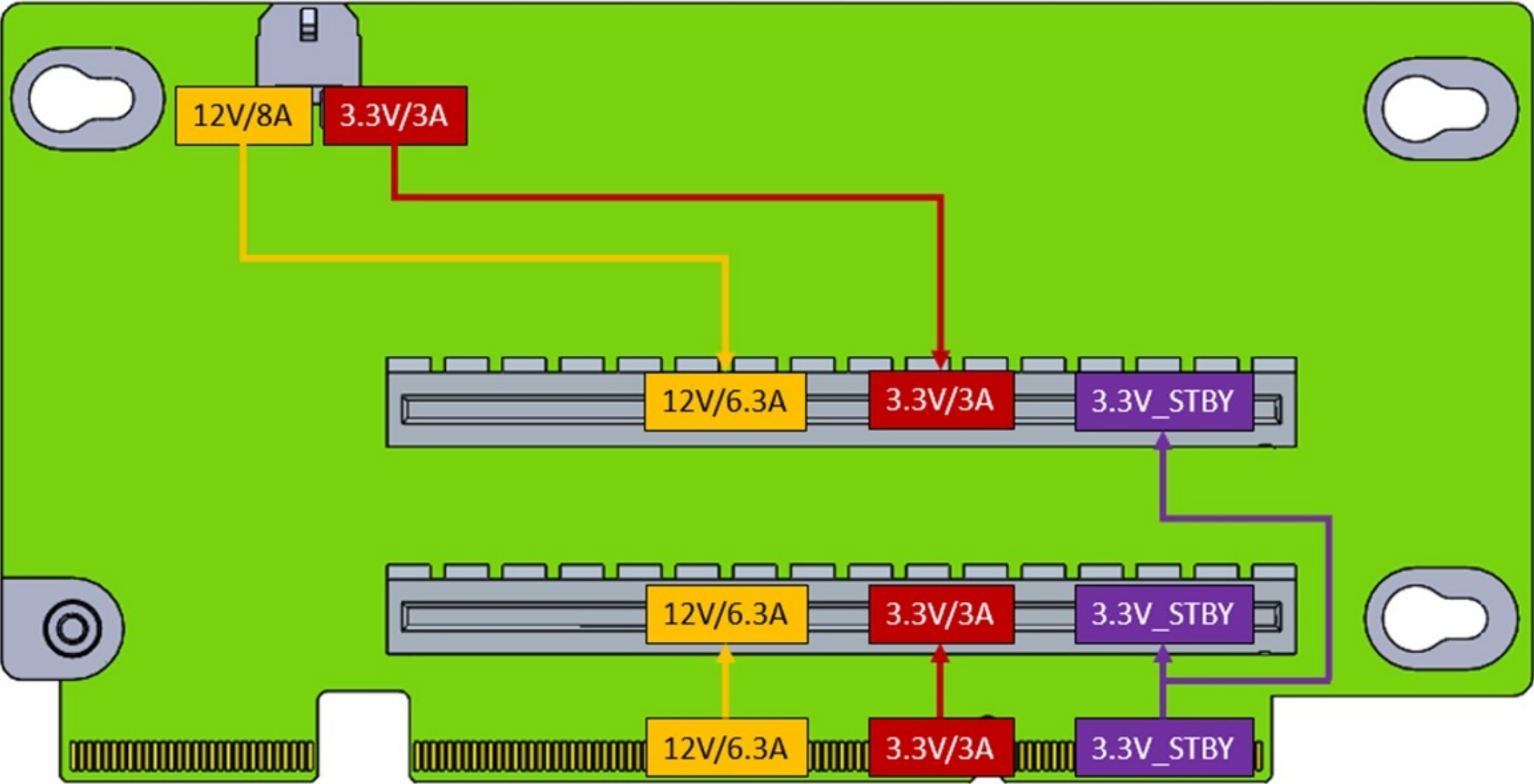
BLOCK_DIAGRAM



STACKUP

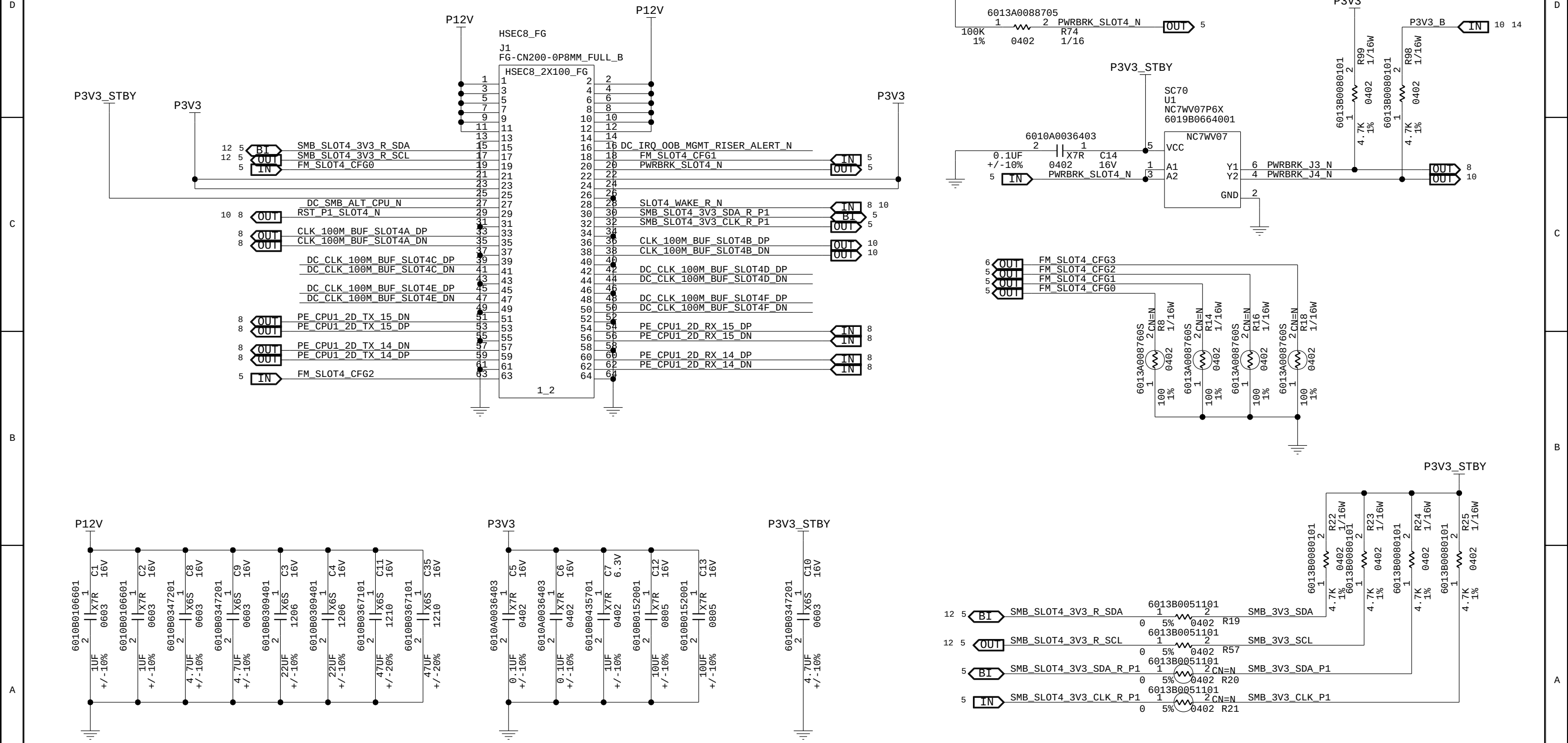
Primary							
Objects		Types <<				Thickness <	
#	Name	Layer	Layer Function	Manufacture	Constraint	Value mil	(+)Total mil
*	*	*	*	*	*	*	*
		Surface					
		Dielectric	Dielectric			0.5	0
1	TOP	Conductor	Conductor			2	0
		Dielectric	Dielectric			2.7	0
2	L2_GND	Plane	Plane			1.2	0
		Dielectric	Dielectric			4	0
3	L3	Conductor	Conductor			1.2	0
		Dielectric	Dielectric			16.5	0
4	L4_PWR	Plane	Plane			1.2	0
		Dielectric	Dielectric			4	0
5	L5_PWR	Plane	Plane			1.2	0
		Dielectric	Dielectric			16.5	0
6	L6	Conductor	Conductor			1.2	0
		Dielectric	Dielectric			4	0
7	L7_GND	Plane	Plane			1.2	0
		Dielectric	Dielectric			2.7	0
8	BOTTOM	Conductor	Conductor			2	0
		Dielectric	Dielectric			0.5	0
		Surface					
◀ ▶							
Info Lock Embedded layers setup Unused pads suppression Refresh materials							
Total thickness:		62.6 mil					
Total thickness without masks:		62.6 mil					
Total mask:		0					

POWER BLOCK DIAGRAM



GOLD_FINGER

OPEN-DRAIN BUFFER



MODULE=PCIE_FG
FUNCTION=PCIE_FG_X32

GOLD_FINGER

D

C

B

A

D

C

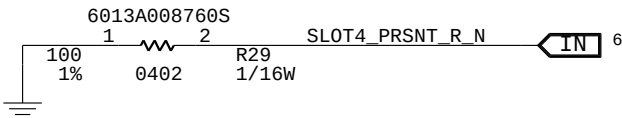
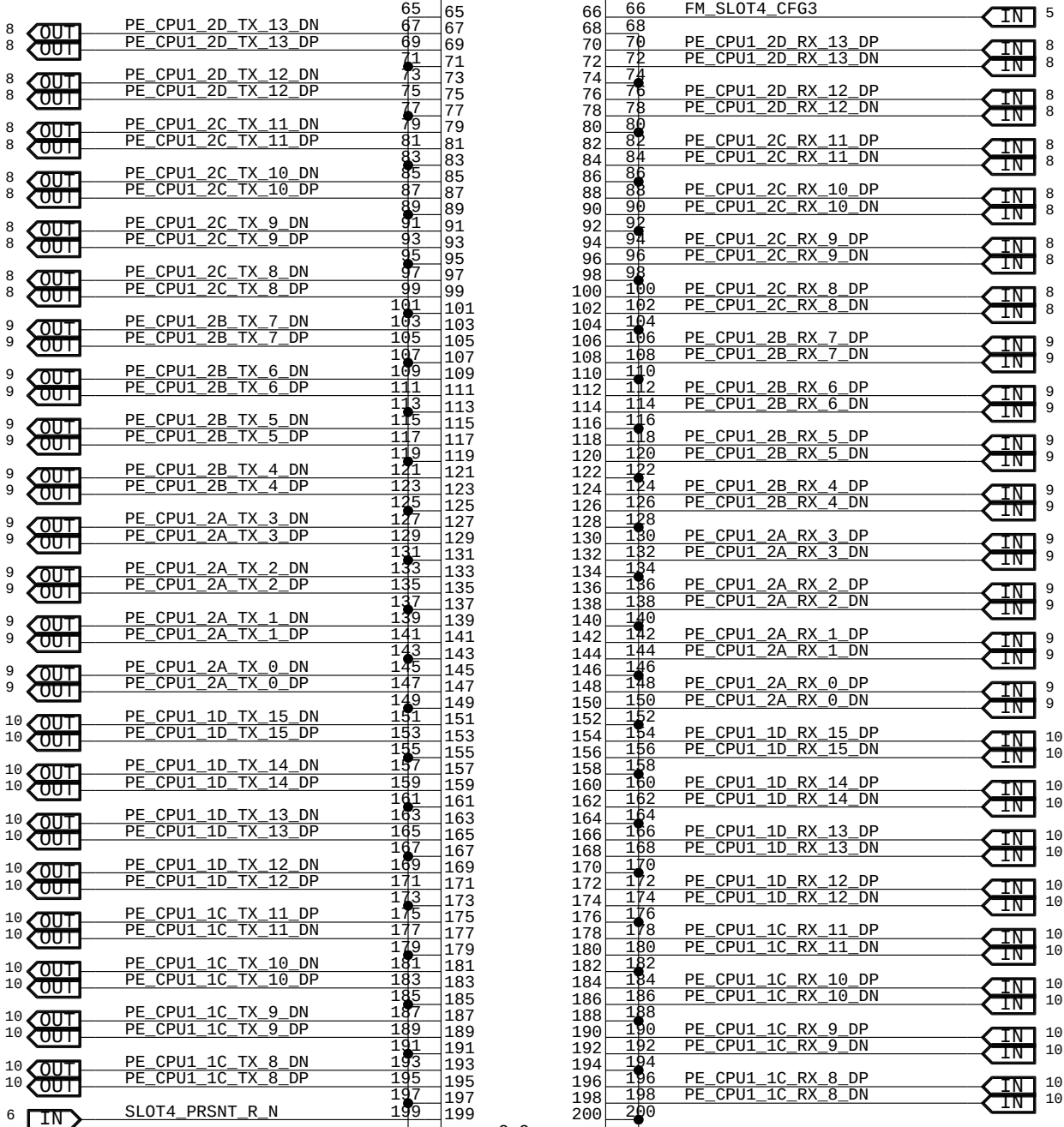
B

A

HSEC8_FG

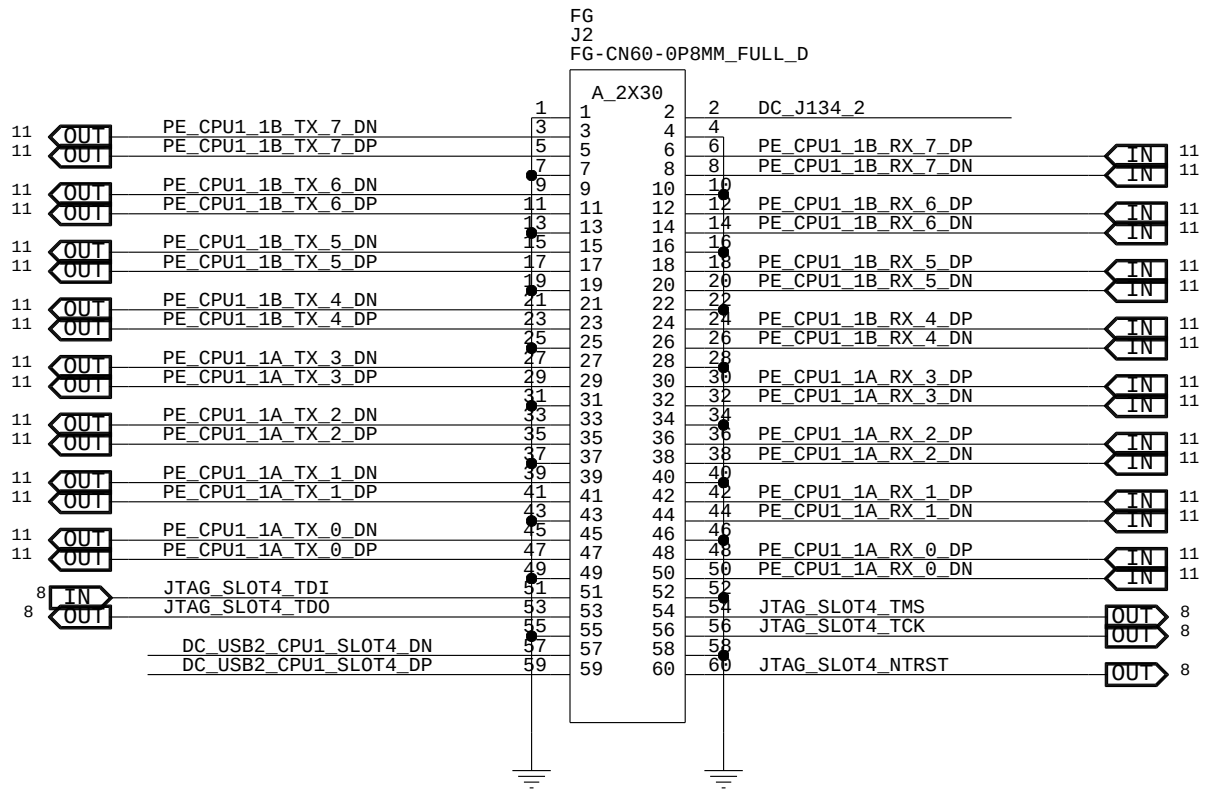
J1
FG-CN200-0P8MM_FULL_B

HSEC8_2X100_FG



MODULE=PCIE_FG
FUNCTION=PCIE_FG_X32

GOLD_FINGER



MODULE=PCIE_FG
FUNCTION=PCIE_FG_X32

D



A

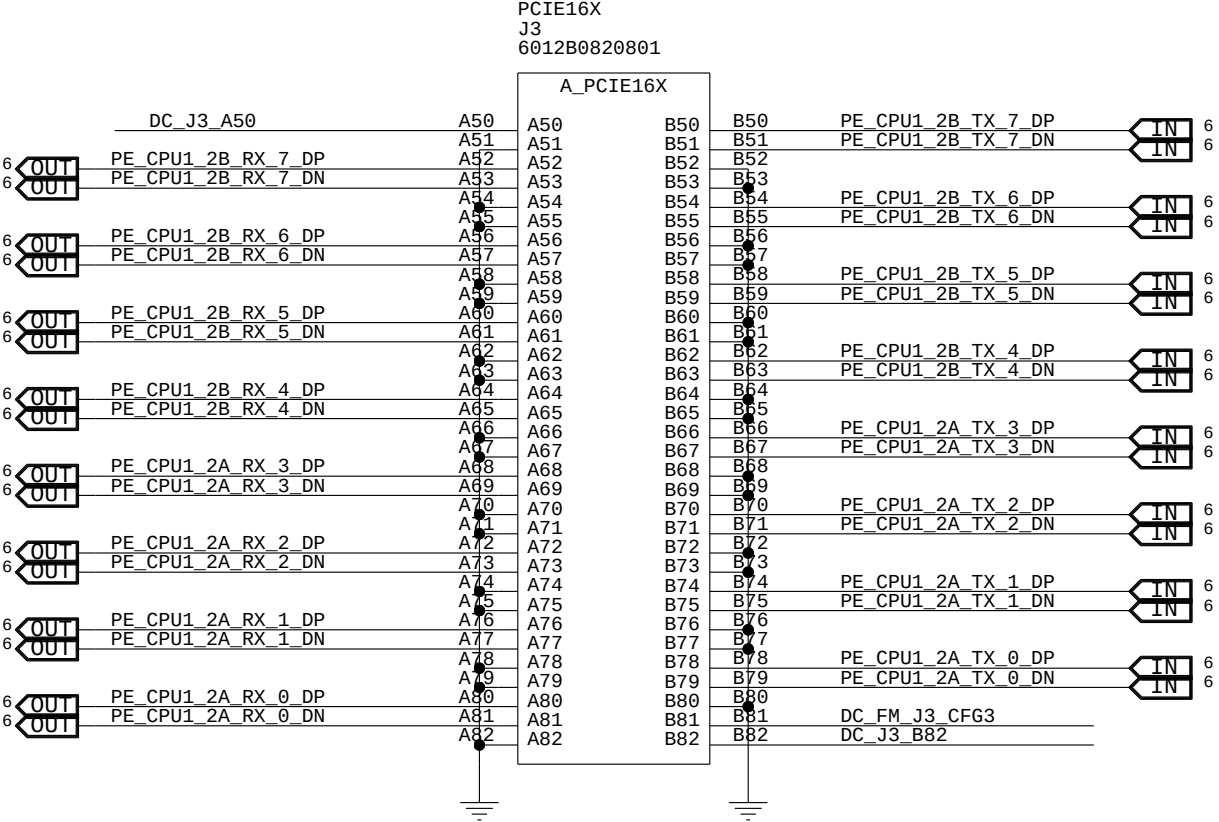


C

D

A

PCIE_X16_CONNECTOR_SLOT1



MODULE=PCIE
FUNCTION=X16_SLOT1

D



B

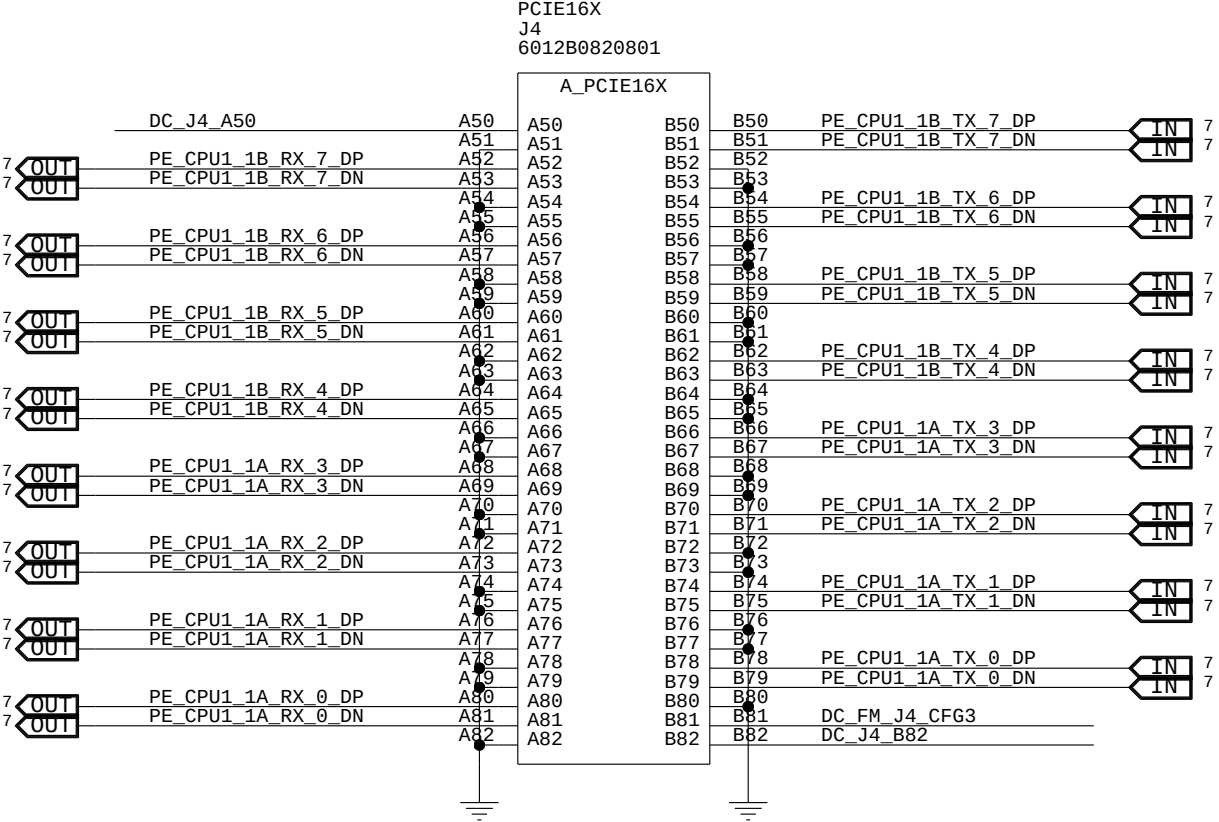


C

B

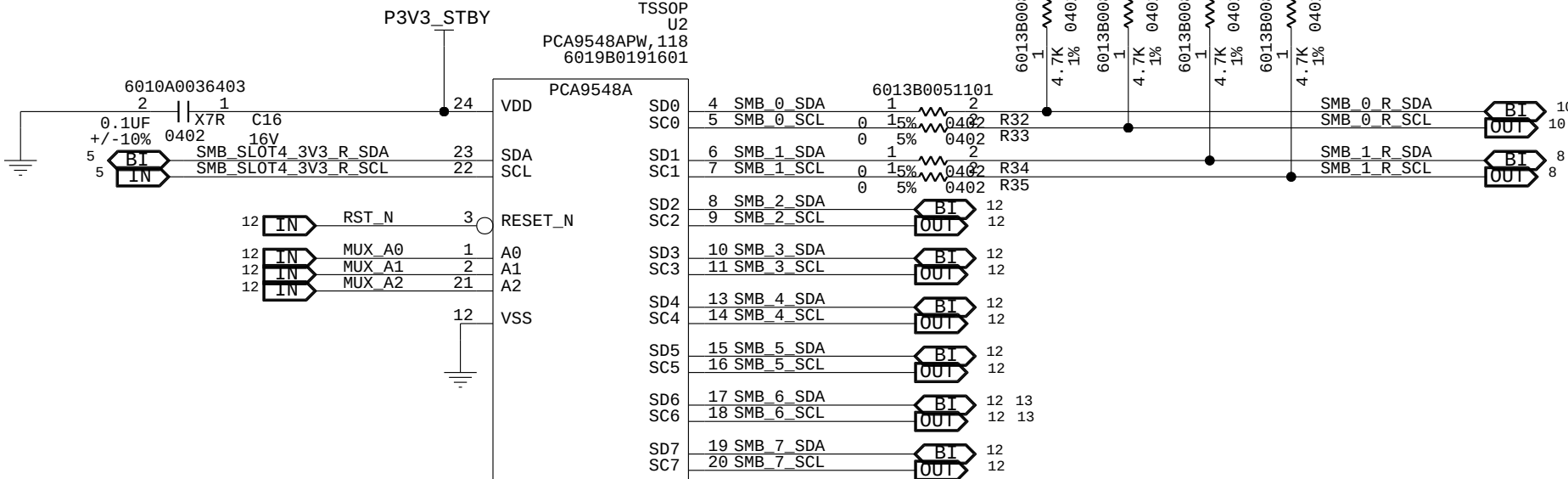
A

PCIE_X16_CONNECTOR_SLOT2

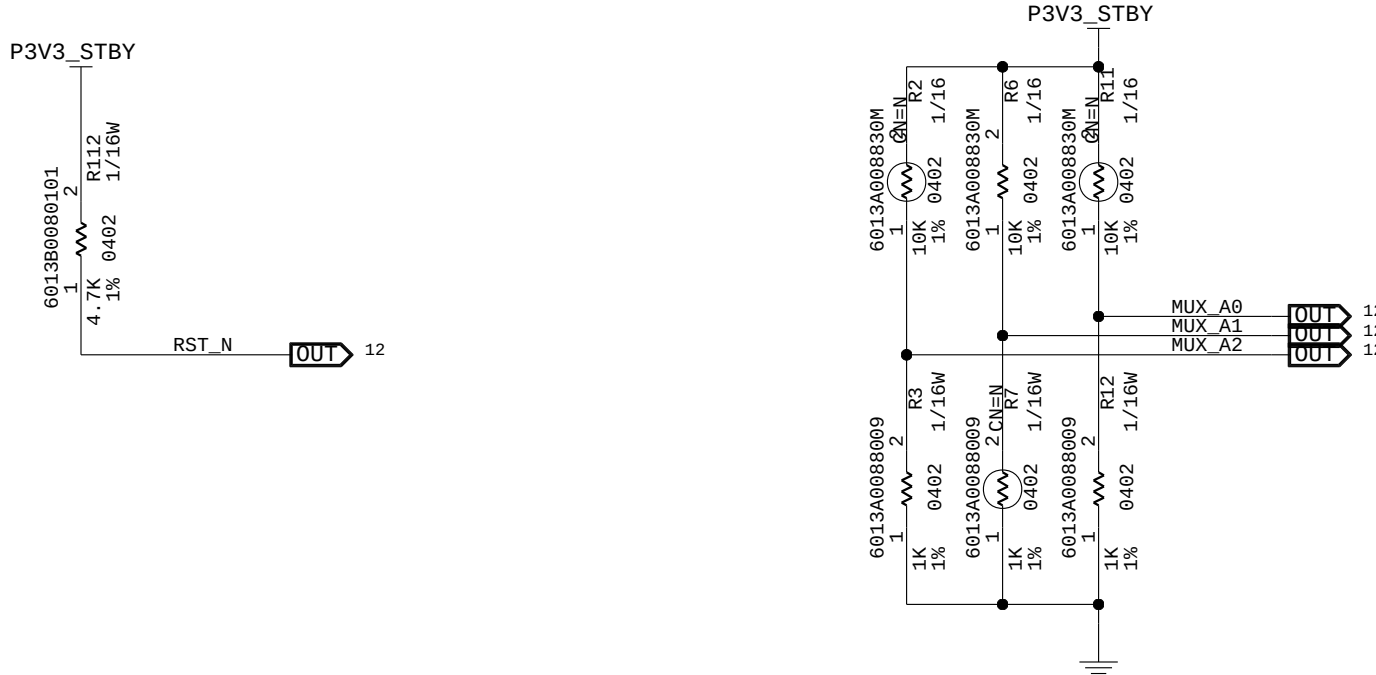
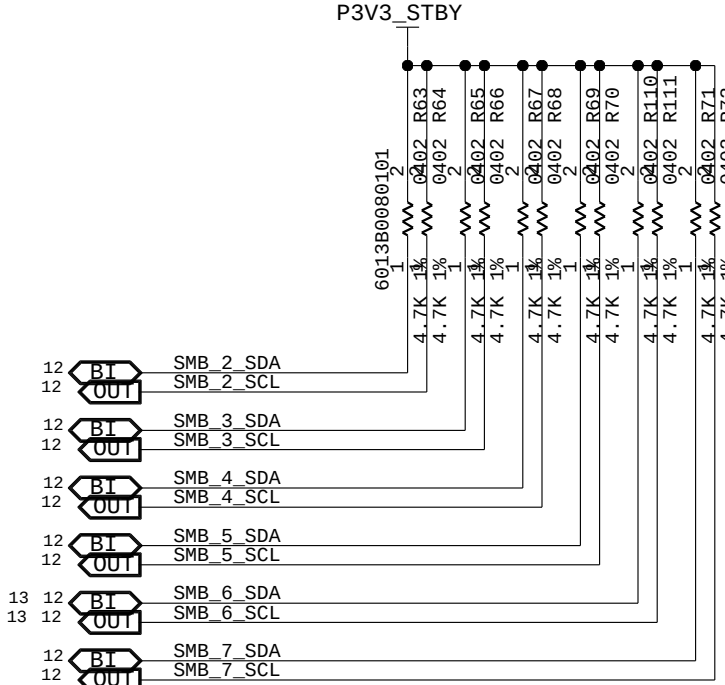


MODULE=PCIE
FUNCTION=X16_SLOT2

MUX



I2C ADDRESS : 1110 010



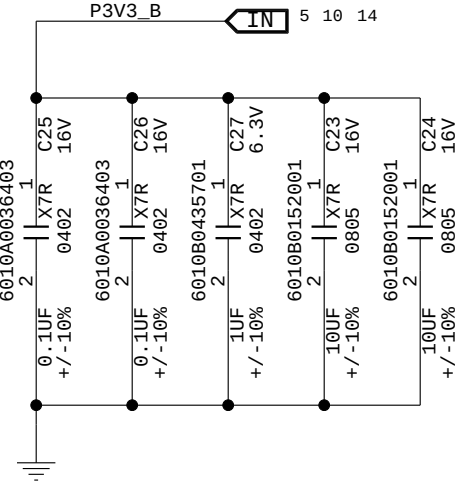
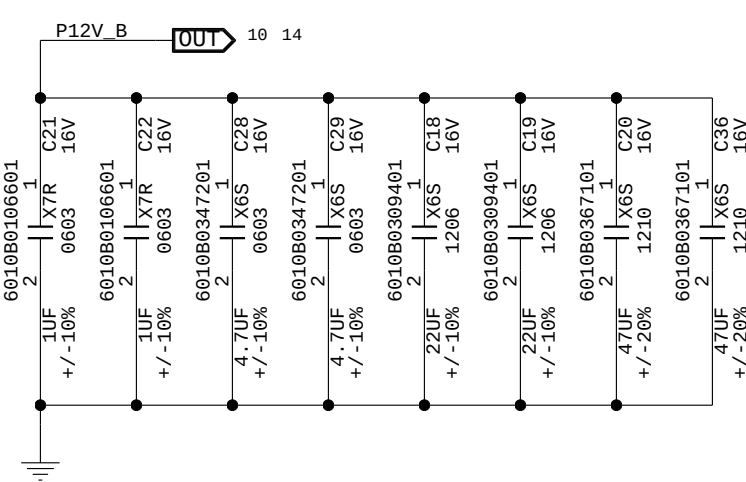
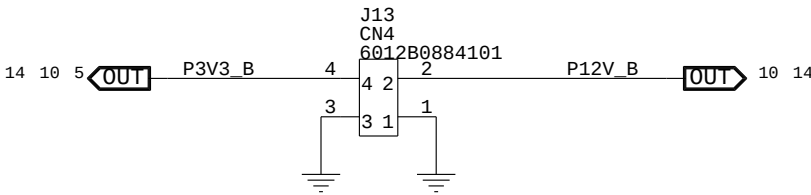
MODULE=MUX
FUNCTION=I2C

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---



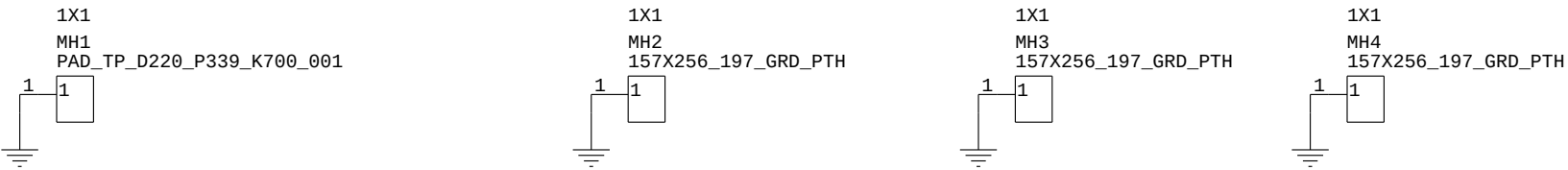
8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

PWR CONNECTOR



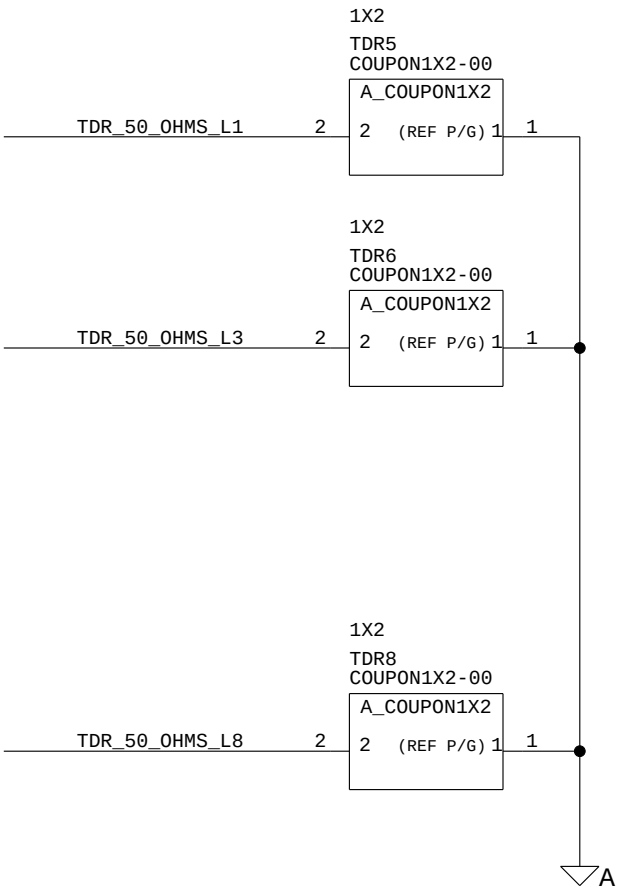
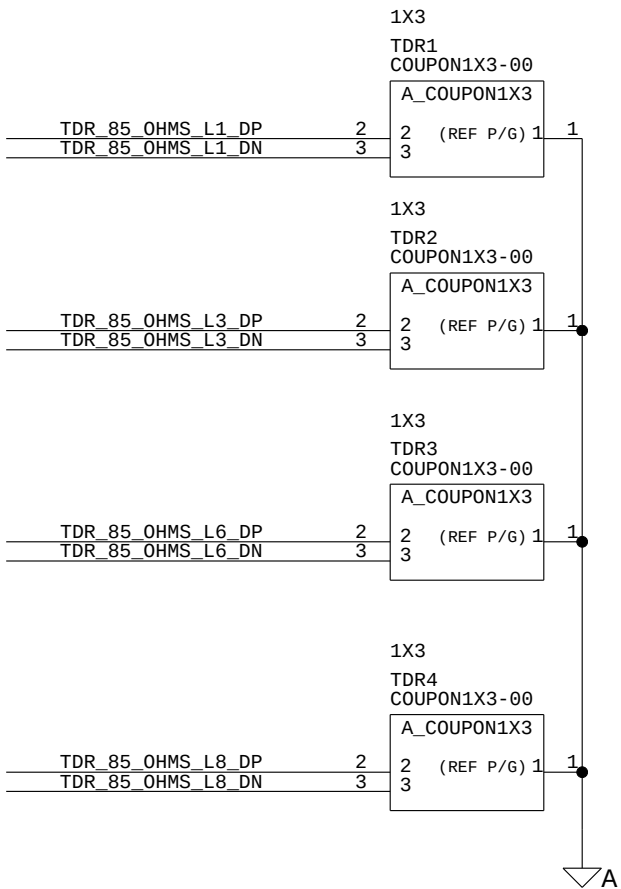
MODULE=POWER CONNECTOR
FUNCTION=P12V/8A, P3V3/3A

SCREW HOLE



MODULE=---
FUNCTION=---

COUPON



MODULE=COUPON
FUNCTION=---

COUPON

D

C

B

A

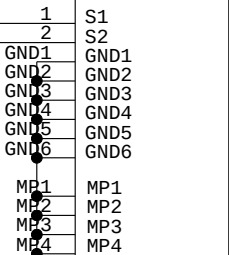
D

C

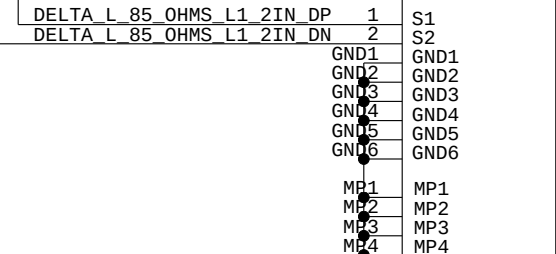
B

A

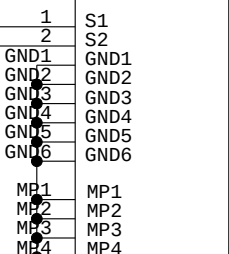
COUPON
J5
SI_DELTAL_COUPON



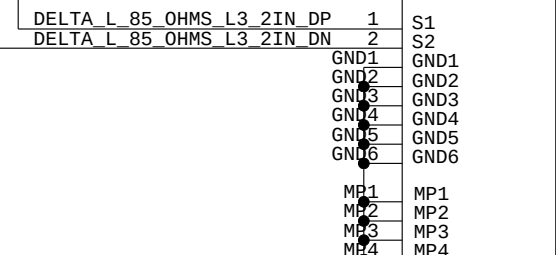
COUPON
J6
SI_DELTAL_COUPON



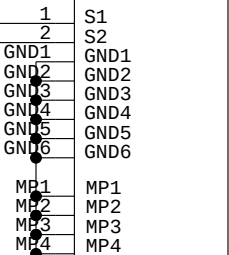
COUPON
J7
SI_DELTAL_COUPON



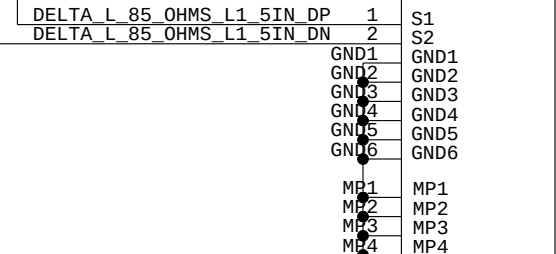
COUPON
J8
SI_DELTAL_COUPON



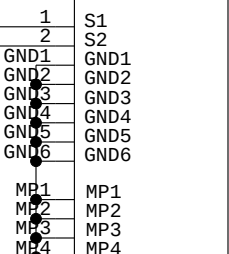
COUPON
J9
SI_DELTAL_COUPON



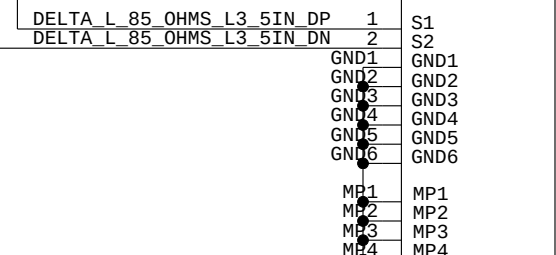
COUPON
J10
SI_DELTAL_COUPON



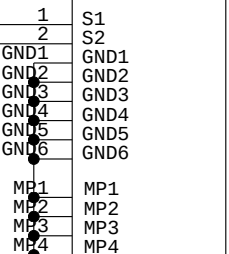
COUPON
J11
SI_DELTAL_COUPON



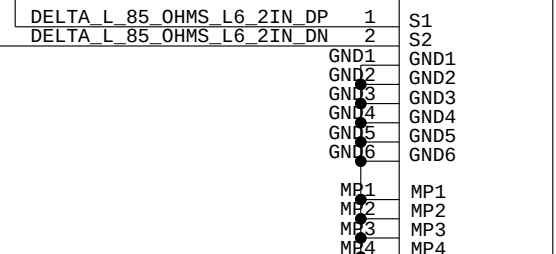
COUPON
J12
SI_DELTAL_COUPON



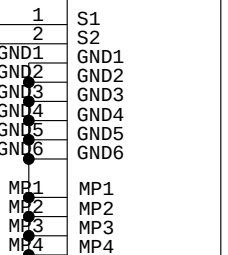
COUPON
J15
SI_DELTAL_COUPON



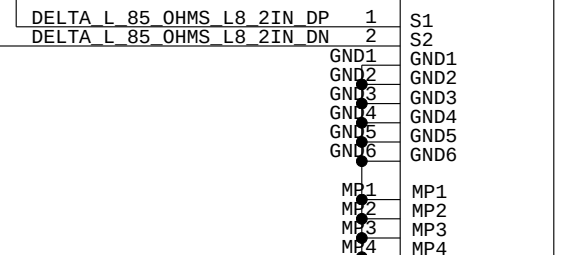
COUPON
J16
SI_DELTAL_COUPON



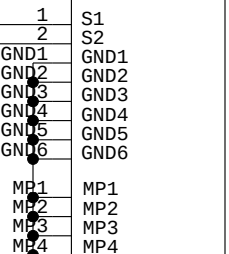
COUPON
J14
SI_DELTAL_COUPON



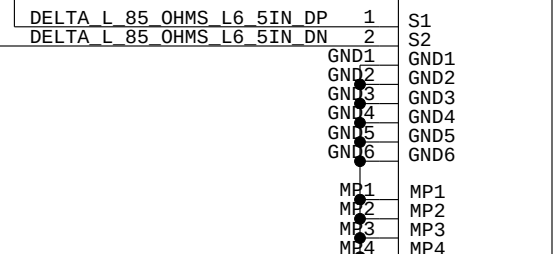
COUPON
J17
SI_DELTAL_COUPON



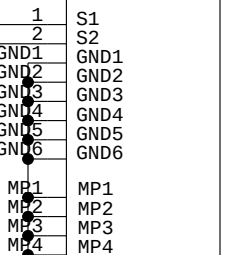
COUPON
J19
SI_DELTAL_COUPON



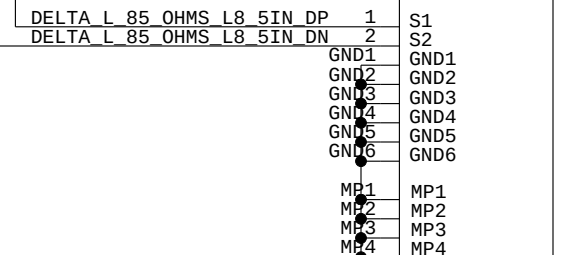
COUPON
J20
SI_DELTAL_COUPON



COUPON
J18
SI_DELTAL_COUPON



COUPON
J22
SI_DELTAL_COUPON



MODULE=COUPON

FUNCTION=- - -

8

7

6

5

4

3

2

1