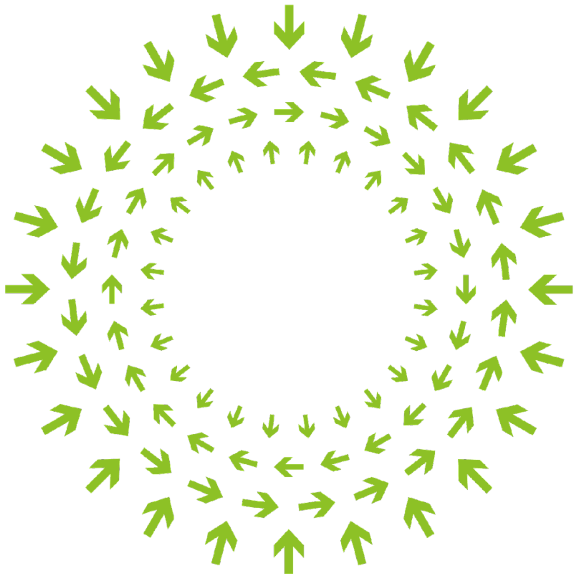


OPEN Compute Project

OCP Base Specification - Evenstar RRU

Base Specification Template
Effective 30/05, 2023

Author: Philip Bates
Author: Behzad Mohebbi

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1. License

1.1. OPTION A: OCP CLA

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Notes:

1) The above license does not apply to the Appendix or Appendices. The information in the Appendix or Appendices is for reference only and non-normative in nature.

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2. Compliance with OCP Tenets

2.1 Openness

Evenstar will be open to the OCP, TIP OpenRAN community, specifically in the RU subgroup. Community members will be able to use this IP to develop product designs for RU

2.2 Efficiency

Evenstar provides a modular and efficient design for optimized BoM. With Evenstar, its component flexibility allows for cost optimization. Optimized DPD and CFR algorithms improve the PA efficiency.

2.3 Impact

Maximize the impact of OpenRAN supply partners. Evenstar enables RU OEMs/ODMs to create enough SKUs to generate early wins and creates supply diversity for MNOs to drive cost competitiveness & feature customization.

2.4 Scale

Faster Innovations. Evenstar improves scaling to new SKUs & easier variation in components to fit customized requirements.

2.5 Sustainability

Evenstar is designed to be as light as possible in the smallest form factor, minimizing material usage. It uses advanced DPD and CFR algorithms to optimize the Power Amplifier efficiency, and reducing energy usage. It uses an optimized BOM to keep the costs as low as possible, allowing more vendors to enter the market to bring more competition.

3. Change Log

Date	Version #	Author	Description
5/26/2023	1	P. Bates	Evenstar RRU OCP CLA
6/5/2023	2	P.Bates	Edits for completeness and readability.

4. Scope

This document defines the technical details for the following specification:

- Base specification for an intended physical ORAN RRU

Note: Any supplier seeking OCP recognition for a hardware product must be 100% compliant with these Base requirements and subsequent design and product specifications as described.

5. Overview

Evenstar is an innovative dual-band RRU intended to accelerate OpenRAN adoption.
The design is based on several novel solutions that address electrical and mechanical design challenges associated with high-power, dual-band radio units, which are emerging in the open RAN ecosystem.
The algorithms associated with complex signal processing algorithms such as digital pre-distortion (DPD) and crest factor reduction (CFR) demonstrate superior performance over current market products.
The architecture was intentionally designed for modularity. The modular packaging is a critical disaggregation tenet which enables radio unit components (power amplifiers, power supply units, etc.) to be mixed and matched from different suppliers, and provides flexibility in rebanding for a range of radio units.

5.1 Standards Compliance

The Evenstar Dual-Band RRU was designed to comply with:
3GPP5G Release 17
ORAN Alliance WG7: White-box Hardware Group Specifications

6. Environmental Regulatory Compliance And Requirements

Operating Parameters	Specification	Comments
Operating Temperature	-40 ℃ to + 55 ℃	The upper temperature limit considers solar radiation of 1120 W/㎡
Temperature Changes	DIN IEC 68-2-14 Nb: Range: -40 - +55 ℃ Time: T1=3H (low - high, high - low) Number of Cycles: 5	
Lightning Protection	IEC 62305-1…4 and 61643-11 for the Surge Protection Device (SPD)	
Maximum Wind Speed	Operation: 150 km/h Survival: 200 km/h	
Acoustic Noise	RRU is passively cooled - no fan noise. ETS 300 743 and ISO 7779	
Icing	Can withstand 30 mm of ice covering.	
Salt	1. DIN IEC 68-2-52 Kb, severity class 2 2. DIN IEC 68-2-11 Ka, 96 hrs.	
Vibration	DIN IEC 68-2-6 Fc	
Shock	DIN IEC 60068-2-27 Ea, Class 1	
Altitude	Full performance: <= 4000m above MSL Derate Max. operating Temperature by 1 ℃ for every 300 m above MSL.	
Water and Dust	Outdoor IP65	EN 60529 for outdoor mounting
Safety	IEC 62305-4 Class II VDE 0855-300:2008-8	
ISO Certification	ISO 9001 ISO 14001	

Table 6.1: Dual-Band RRU Environmental Requirements

7. Physical Specifications

The Evenstar radio is a stand alone unit designed to be mounted to a rail, wall or pole. It is passively cooled. The design is modular allowing for easy disaggregation.

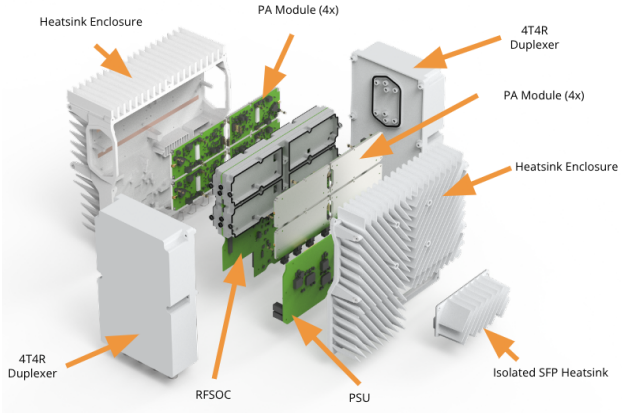


Figure 7.1: Exploded View of the Dual-Band RRU

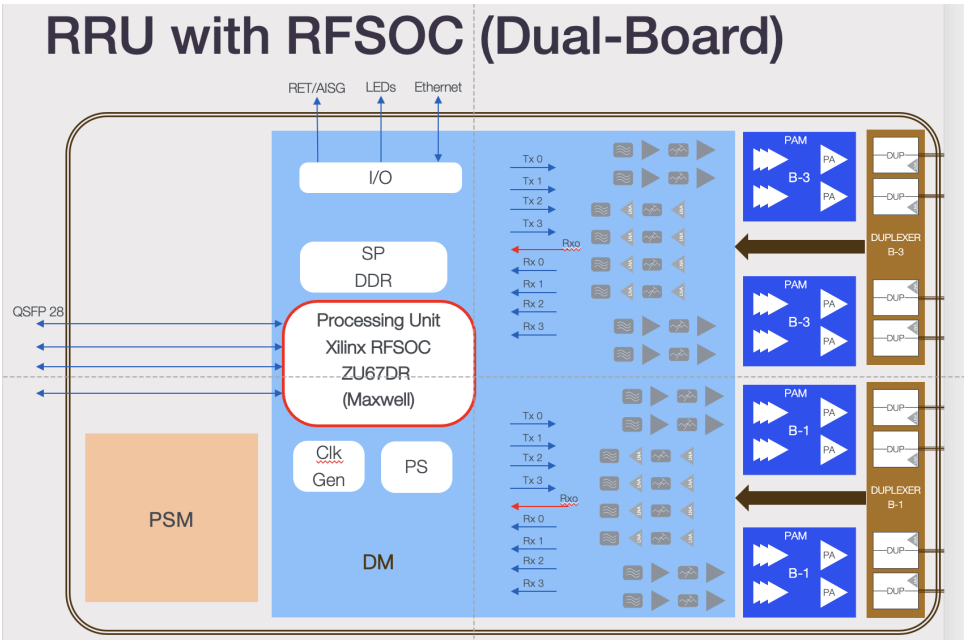


Figure 7.2: Dual-Band RRU Block Diagram

7.1 Mechanical

Mechanical Parameter	Specification	Comments
Physical Interface	4 x 25 GHz SFP28	Or 1 x QSFP
Local Maintenance Port (LMP)	Supported	LMP is included for lab/factory use. LMP includes RS232 serial port, 100baseT ethernet, JTAG, Frame sync, 10 MHz, 1 PPS. Requires external interface board.
Power Supply	-48V DC, 3 wires	
Antenna Connector	4.3-10	PIM performance OK for 40W.
External antenna line device	DIN-8 RET, AISG 2.0	AISG will be supported. Support for TMAs etc. via external smart bias T. No AISG on/off keying (00k) modem in RRU.
External Alarms	4- pin DIN connector	2 alarm wire pairs, open/short: open > 10k ohms, short < 10 ohms.
Mounting	Rail, wall, pole.	
Volume Target	<24 liters	
Weight Target	< 25 kg die cast.	

Table 7.1: Dual-Band RRU Mechanical Requirements

Files containing full details of the mechanical design are listed in Appendix B-5, Mechanical Design Files.

8. Electrical Requirements and Operating Specifications

The Evenstar radio requires a single -48 V, 550 W power supply. It is a dual-band radio, both bands being part of the 5G specifications. The modularity of the design allows vendors to develop hardware for bands suitable for their markets.

System Parameter	Specification	Comments
Air Interface	1. LTE, LTE-A 2. NB-Io (Modes: In-band NB-IoT, Guard-band NB-IoT), NB-IoT boost factor ,=9 dB 3. 5G NR: 8W up to 100 MHz	Stand-Alone NB-IoT not supported. To meet band C requirements in the future, an IBW of 100 MHz needs to be supported.
Operating Mode	FDD	
Single or Multi-band	Multi-band	More than one band. Most likely two, but Depending on bands there can be more than two.
Number of Bands	2	
Frequency Band #1	Band 1	
Band #1 Tx Power	40W	Low band: up to 80W Mid, High bands: Minimum 40W
Band #1 MIMO Order	4T4R	
Frequency Band #2	Band 3	
Band #2 Tx Power	40W	Low band: up to 80W Mid, High bands: Minimum 40W
Band #2 MIMO Order	4T4R	
Antenna Ports	4	
Antenna Configuration	4T4R	
Beam Forming	No	
Instantaneous Bandwidth (IBW)	Full Band	60 MHz for Band1 and 75 MHz for Band 3.

Occupied Bandwidth (OBW)	Full Band	
Carrier Capacity	6 per Band	At least 2 LTE and 2 5G NR carriers.
Carrier Bandwidth	LTE: 5, 10, 15, 20 (MHz) NR: 5, 10, 15, 20, 25, 30 (MHz)	
Sub-Carrier Spacing	LTE: 15 kHz NR: 15, 30, 60 (kHz)	
Physical Interface	4 x 25 GHz SFP28	Or 1 x QSFP
Local Maintenance Port (LMP)	Supported	LMP is included for lab/factory use. LMP includes RS232 serial port, 100baseT ethernet, JTAG, Frame sync, 10 MHz, 1 PPS. Requires external interface board.
Power Supply	-48V DC, 3 wires	
Attenuation Mechanism	RRU must provide a mechanism for Tx and Rx input attenuation adjustment: 0 - 30 dB in .1 dB increments	Compensation of Feeder Loss.
UL Noise Figure	1.9 dB typical, 2.5 dB max.	

Table 8.1: Dual-Band RRU Electrical Requirements and RF Operating Specifications

9. Thermal Design Requirements

The Evenstar radio has been designed to be passively cooled, and can be deployed under the environmental conditions given in Table 9.1.

Thermal Parameter	**Specification**	**Comment**
Operating Temperature	-40 t0 + 55 ℃	The upper temperature limit considers solar radiation of 1120 W/㎡
Temperature Changes	DIN IEC 68-2-14 Nb: Range: -40 - +55 ℃ Time: T1=3H (low - high, high - low) Number of Cycles: 5	
Maximum Wind Speed	Operation: 150 km/h Survival: 200 km/h	
Acoustic Noise	RRU is passively cooled - no fan noise. ETS 300 743 and ISO 7779	
Icing	Can withstand 30 mm of ice covering.	

Table 9.1:Dual-Band RRU Thermal Requirements

10. Interfaces

10.1 Signal List

Interfaces	Specification	Comment
Local Maintenance Port (LMP)	Supported	LMP is included for lab/factory use. LMP includes RS232 serial port, 100baseT ethernet, JTAG, Frame sync, 10 MHz, 1 PPS. Requires external interface board.
Logical Fronthaul Interface	OAM: NetConf/Yang (ORAN) Fronthaul: ORAN split 7.2x, RRU type A	The ORAN specification shows all OAM functions including: software upgrades, KPIs, alarms, etc. are managed from the BBU/DU via the M-Plane ORAN interface.
ORAN RRU Type	RRU Type A (RRU Type B not supported and not upgradeable to)	PHY functions on RRU: DL: IFFT and CP addition UL: FFT, CP removal, PRACH filtering /handling
Power Supply	-48 V nominal, -57 V < Vin < -40.5 V, 550 W	
Antenna Connector	4.3-10	PIM performance OK for 40W.
External antenna line device	DIN-8 RET, AISG 2.0	AISG will be supported. Support for TMAs etc. via external smart bias T. No AISG on/off keying (00k) modem in RRU.
External Alarms	4- pin DIN connector	2 alarm wire pairs, open/short: open > 10k ohms, short < 10 ohms.

Table 10.1: Dual-Band RRU External Interface Requirements

11. Onboard Power System

Power Supply	-48 V nominal, -57 V < Vin < -40.5 V, 550 W

Table 11.1: Dual-Band RRU Power Supply Requirements

The list of available design files for the PSU (Power Supply Unit) is given in Appendix B-2.

12. Prescribed Materials

13. System Firmware

The hardware has been tested to show basic functionality. However there is no documented firmware from Meta. Firmware would need to be developed by any prospective vendor using the design. The processor, a Xilinx RFSOC 7U67DR, is an industry standard for which firmware is available and should be easily procurable.

The Evenstar Radio is designed to be managed through the O-RAN M-Plane which is implemented through the Fronthaul interface, shown in Section 10 above. All configuration, alarms, KPIs and software updates are managed through the M-Plane.

Out-of-band management is possible through the Local Maintenance Port (LMP) also shown in Table 10.1.

14. Security

Security has been designed into the Evenstar RRU at the hardware level. There is secure boot. All the hardware hooks are available. Security is defined by ORAN and needs to be coded into the firmware.

15. Software Support.

Solidworks: Mechanical design.
ANSYS IcePak: Electronic system design and thermal analyses.
Autodesk Eagle: PCB design
MATLAB: Algorithm simulations.

Appendices

Appendix A - Checklist for IC approval of this Specification (to be completed by contributor(s) of this Spec)

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Item	Status	Link to detailed explanation
Has this contribution been presented to an OCP Project group during a project call or engineering workshop?	Yes	
Approval by Project Leads	Yes or No	If “No”, please state the reason.
Is this contribution entered into the OCP Contribution Portal?	Yes or No	If “No”, please state the reason.
Was it approved in the OCP Contribution Portal?	Yes or No	If “No”, please state the reason.

Appendix B- List of Evenstar RRU Design Files

Appendices B-1 to B-8 contain files of types:

Digital card including RF-FE (FEM), PAM & PSU

Project CAD Files which includes: .prj
Schematics(.DSN)
Layout files (.brd files) & Gerber files
BOM (includes manufacturer part numbers)
Design constrain drawing showing (emn file)
Stackup details showing manufacturer part numbers
Top level BOM
Power Tree
Clock Tree

Duplexer

Specification
Mechanical CAD files(fully featured step files .stp and or fully featured solid work project files)
Design constraint drawings showing tolerance analysis.
Top level assembly drawing.
Document for tuning the filter post assembly.

Mechanical design

CAD files:
Mechanical CAD files(fully featured step files .stp and or fully featured solid work project files)
Design constraint drawings showing tolerance analysis.
Assembly instructions
Top level Assembly drawings.
Top level BOM
Thermal analysis project files(icepack,...)

System Requirements
System Specification
Test Reports

B-1: Power Amplifier Modules (PAM)

PAM_SinglePA_B1_Ampleon_20220126200456_ExtDstr.xlsx
Single PAM_B1_Ampleon_working-gerbers.rar
Single_PAM_B1_Ampleon-0118-0950.brd
Pam_b1_amp_spa_p1_20220112.pdf

PAM_B1_AMP_SPA_P1_20220112.DSN
PAM module stackup-20210510-(RO4350B_2Layer)ISU Hunan-A.xlsx
Facebook B1 Module 40W Sept2022 Xilinx DPD.xlsx
FB_B1_#2_DRV 28V pnaScreenShot.png
FB_B1_#2_DRV 28V Modulation.png
PPAM_SinglePA_B1_Ampleon_20220126200456_ExtDstr.xlsx
Single PAM_B1_Ampleon_working-gerbers.rar
Single_PAM_B1_Ampleon-0118-0950.brd
Pam_b1_amp_spa_p1_20220112.pdf
PAM_B1_AMP_SPA_P1_20220112.DSN
PAM module stackup-20210510-(RO4350B_2Layer)ISU Hunan-A.xlsx
Facebook B1 Module 40W Sept2022 Xilinx DPD.xlsx
FB_B1_#2_DRV 28V pnaScreenShot.png
FB_B1_#2_DRV 28V Modulation.png
PAM_SinglePA_B3_Ampleon_20220126200422_ExtDstr.xlsx
Single PAM_B3_Ampleon_working-gerbers.rar
EVENSTAR_PAM-SPA-Ampleon-0103-1010.brd
Pam_b3_amp_spa_p1_20211215.pdf
PAM_B3_AMP_SPA_P1_20211215.DSN
PAM module stackup-20210510-(RO4350B_2Layer)ISU Hunan-A.xlsx
Data Summary.docx
BOM, Including Only Tuned Components and Parameters.docx
AR220003_FB_Prototype_ B3_line Up_Driver_Final_1805-1880MHz-28v.pdf
AR220002 BLM9D1822-30B Doherty 1805-1880MHz_28V.pdf
AR22001_FB_Prototype_ B3_Final_BLC10G18XS-360AVT_1805-1880MHz-28v.pdf

B-2: Power Supply Module (PSU)

Dual-Band
Allegro_free_viewer.jrl
RFSOC_DUAL_BAND_PSU-0420-1600.brd
Cases.cfg
Cases.cfg,1
Waived_drc.htm
DUAL_BAND_PSU-B-0420.zip
DUAL_BAND_PSU-C-0420.zip
DUAL_BAND_PSU-G-0420.zip
DUAL_BAND_PSU-M-0420.zip
DUAL_BAND_PSU-P-0420.zip

Dualband_finalschematics.pdf
DUALBAND_PSU_0324.DSN
Evenstar_PSU_DB_DVT_LLC (1).xlsx

B-3: Digital Control Module

RFSOC_FB_DB_8PA_20220525163139_ExtDstr_Build2.xlsx
RFSOC_DUALBAND-G-0120-1.zip
Rfsoc_fb_db_8pa_rev1.brd
Rfsoc_fb_db_8pa_rev2.pdf
rfsoc_fb_db_8pa_rev2.DSN
RFSOC_DB.pptx
TU-768 DkDf_10GHz_1910B.pdf
TU-768 data sheet.pdf
RFSOC_231-000187_REV1 2021-12-16 STACKUP (185HR_10Layer) ISUHN_H1.xlsx
RF-TX-Chain-Analysis-B3-FB-RFSOC-03_17_2022.xlsm
RFSOC_FB_DB_8PA_P2.xlsx
WMX Fiona RFSOC Pilot Run Issue 03222022.pptx
RFSOC Dual Band.pptx
Fiona RFSOC Feedback_040622_V2.pptx
2nd PR_RF SOC DFM Report_20220728.pdf
RFSOC_FB_DB_8PA_P2.pdf
RFSOC_FB_DB_8PA_P2.DSN

B-4: Resonant Cavity Duplexer Filter Module

SideWind0p5mps_MediumFilter.tzr
SideWind0p5mps_LargeFilter.tzr

B-5: Mechanical Design Files

EvenStar_8T8R_ASM_OFTI-STEP.STEP
XTAL_FC13A_FC13A 32.7680KAA_XTAL_FC13A_3.SLDPRT
XFMR_27W21L9D4H_TRANS_T1_T1_0_XFMR_27W21L9D4H_T.SLDPRT
WE13X13_WE13X13_L1E_WE13X13_7_3UH.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR8_VRL10W5P6MM_V10.SLDPRT

VRL10W5P6MM_VRL10W5P6MM_VR7_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR6_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR5_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR4_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR3_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR2_VRL10W5P6MM_V10.SLDPRT
VRL10W5P6MM_VRL10W5P6MM_VR1_VRL10W5P6MM_V10.SLDPRT
vc-106-70-3.SLDPRT
U_LSOP4_LTV816_OPTOCOUPLER_SINGLE_0_U_LSOP4_LT.SLDPRT
TSSOP_D65X5D775_14SP_4D4W5L1D2H_SN74AHC02PWR_TSSOP_D65X5D775_14.SLDPRT
TSSOP_24_IC_I2C_EXPANDER_TCA9548APWR_TSS.SLDPRT
TSSOP_24_ICTRANS_1V23V6_24TSSOP_TSSOP.SLDPRT
TSSOP8_65P440X300MM_M24C08RDW6TP_TSSOP8_65P440X300.SLDPRT
TSOP48_MT29F2G08ABAEAWPAATXE_TSOP48_M.SLDPRT
TP_276C_1SP_TEST_POINT_POWER_TP_276C_1SP_DI.SLDPRT
TP_1SP_45X75_TEST_POINT_POWER_TP_1SP_45X75_D.SLDPRT
TP_1SP_45X75_T POINT R_TP_1SP_45X75_TP_CONN.SLDPRT
TP_1SP_45X75_T POINT R_TP_1SP_45X75_TP.SLDPRT
TPBGA_24_5X5_ICFLASH_2GB_1V72V0_1_TPBGA_24.SLDPRT
ThermalGreaseLayer.SLDPRT
TCXO_1D27X6D25_10SP_7D5W5D5L_TCXO_10P_U8429LF_TCXO_1D27X6D25.SLDPRT
TANT_D_CAP_ALUM_470UF_2V_20%_SMD_TANT_.SLDPRT
SW_TL3301_G_4P_TL3301EF100QG_SW_TL3301_G_4P_TL.SLDPRT
SW_2D54X8D9_6SP_8D62W6D2L4D3H_SW_DIP_2_SW_2D54X8D9_6SP_8D62W6.SLDPRT
SOT_23_FET_N_8V_20V_1.3A_0.5W_SM_SOT_2.SLDPRT
SOT89_1D5_3SP_PHA1H+_3_SOT89_1D5_3SP__SKY651.SLDPRT
SOT583_D5X1D4_8SP_2D1W1D2L_MP2332C_3_SOT583_D5X1D4_8SP_2D1.SLDPRT
SOT323_1D3X1D9_3SP_2D2W_BEC_TRANS_BJT_NPN_30V_0.1A_BC848BW_.SLDPRT
SOT323_1D3X1D9_3SP_2D2W_BEC_MMBT3904_0_SOT323_1D3X1D9_3SP_2.SLDPRT
SOT323_1D3X1D9_3SP_2D2W_BEC_BC856BW_0_SOT323_1D3X1D9_3SP_2D.SLDPRT
SOT323_1D3X1D9_3SP_2D15W_GSD_DMN62D0UW_0_SOT323_1D3X1D9_3SP_.SLDPRT
SOT23_D95X2D35_5SP_1D5W2D9L1D1H_MIC55043.3YM5_SOT23_D95X2D35_5.SLDPRT
SOT236L_MPQ8112_U4D_SOT236L_MPQ8112.SLDPRT
SOT236L_MPQ8112_U4C_SOT236L_MPQ8112.SLDPRT
SOT236L_MPQ8112_U4B_SOT236L_MPQ8112.SLDPRT
SOT236L_MPQ8112_U4A_SOT236L_MPQ8112.SLDPRT
SOT236L_MPQ8112_U12_SOT236L_MPQ8112A.SLDPRT
SOT235_0O95MM_H1O45_TI_TPS382833DBV_SOT235_0O95MM_H1.SLDPRT
SOP_D65X4D35_8SP_3WL_1_TMP1075DGKR_SOP_D65X4D35_8SP_3W.SLDPRT
SOP_1D27X5D165_8SP_3D9W4D9L_G1_TPS7A7001_0_SOP_1D27X5D165_8SP_.SLDPRT
SOIC_1D27X5D165_14SP_4W8D75L_MCP6569TEST_SOIC_1D27X5D165_1.SLDPRT

SOIC8_EP_MP4581_U20_U20_0_SOIC8_EP_MP458.SLDPRT
SOIC8_50MIL_150MIL_H1O75_TI_IC_TRANS_3V03V6_35MV_8SOIC_SO.SLDPRT
SOIC16_10_5X7_6MM_MP27631_U10_SOIC16_10_5X7_6MM_M.SLDPRT
SOIC16_10_5X7_6MM_MID1W0505_U9_SOIC16_10_5X7_6MM_.SLDPRT
SOD_123_DSCTKY_SOD123_BAT54T1G_SOD_123_.SLDPRT
OD_123_DIODE SCHOTTKY_1_SOD_123_PDZ15B.SLDPRT
SOD523_DIODE_D2D_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D2C_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D2B_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D2A_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D1E_SOD523_DIODE_D1E.SLDPRT
SOD523_DIODE_D1D_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D1C_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D1B_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODE_D1A_SOD523_1SS389,L3F.SLDPRT
SOD523_DIODESSOD323_D3E_SOD523_1SS38.SLDPRT
SOD523_DIODESSOD323_D2E_SOD523_1SS38.SLDPRT
SOD123_3D28_2SP_1D65W2D7L1H_AC_SBD;90V;1A;SMA_0_SOD123_3D28_2S.SLDPRT
SOD123_2D9_2SP_2D7W1D75L_AC_TVS DIODE;64V;SMA_0_SOD123_2D9_.SLDPRT
SO8_3D75X1D27_5SP_MOSFET_N_FDMS86250_5_SO8_3D75X1.SLDPRT
SO8_1D27_5SP_4D9W6D15L_MOSFET_N_FDMS86250_5_SO8_1D27_5.SLDPRT
SC70_D65X1D74_6SP_2W1D25L_NLAS4599DFT2G_0_SC70_D65X1D74_6.SLDPRT
SC70_6_SN74AVC1T45DCKR_U_0_SC70_6_SN74.SLDPRT
RTS6BS14N3P03.SLDPRT
RP_EXB_0603X4_RESARRAY_1206_10K_5%_SMT_RP_EX.SLDPRT
RF_1SP_2D6WL1D25H_U_0.FLRSMT1_RF_1SP_2D6WL1D25.SLDPRT
RF_1SP_2D6WL1D25H_COAX_CONN_U_0.FLRSMT1_RF_1S.SLDPRT
RF_1D92_4SP_2D3WLD35H_20549001E_RF_1D92_4SP_2D3WLD35.SLDPRT
RFSOC_SUB.SLDASM
RFSOC_GASKET_SUB_THREADED.SLDASM
RFSOC_GASKET_SUB.SLDASM
rfsoc_fb_202201201330.SLDPRT
rfsoc_fb_202201201330.SLDASM
RFSOC_FB_20210330-1830_Shield_Threaded.SLDPRT
RFSOC_FB_20210330-1830_Shield_gasket.SLDPRT
RFSOC_FB_20210330-1830_Shield.SLDPRT
RFSOC_DUAL_BAND_PSU04201600_EE.SLDASM
RFSOC_DUAL_BAND_PSU04201600.SLDPRT
RES_CSS2775_RES_0402_10R0_0.1W_1%_SMD_RES_C.SLDPRT
RES_0603_RES0603_R9E_RES_0603_100K.SLDPRT
ES_0603_RES0603_R9E_RES_0603_100K.SLDPRT
RES_0603_RES0603_R96E_RES_0603_115.SLDPRT

RES_0603_RES0603_R95E_RES_0603_30.SLDPRT
RES_0603_RES0603_R6E_RES_0603_100K.SLDPRT
RES_0603_RES0603_R5E_RES_0603_39K.SLDPRT
RES_0603_RES0603_R14E_RES_0603_10K, 060.SLDPRT
RES_0603_RES0603_R13E_RES_0603_12K.SLDPRT
RES_0603_RES0603_R12E_RES_0603_51K.SLDPRT
RES_0603_RES0603_R10E_RES_0603_1MEG.SLDPRT
RES_0603_RES0201_R8D_RES_0603_115.SLDPRT
RES_0603_RES0201_R8C_RES_0603_115.SLDPRT
RES_0603_RES0201_R8B_RES_0603_115.SLDPRT
RES_0603_RES0201_R8A_RES_0603_115.SLDPRT
RES_0603_RES0201_R51_R51_RES_0603_0.SLDPRT
RES_0603_RES0201_R50_R50_RES_0603_0.SLDPRT
RES_0603_RES0201_R42_R42_RES_0603_0.SLDPRT
RES_0603_RES0201_R41_R41_RES_0603_0.SLDPRT
RES_0603_RES0201_R2D_RES_0603_115.SLDPRT
RES_0603_RES0201_R2C_RES_0603_115.SLDPRT
RES_0603_RES0201_R2B_RES_0603_115.SLDPRT
RES_0603_RES0201_R2A_RES_0603_115.SLDPRT
RES_0603_RES0201_R25_R25_RES_0603_0.SLDPRT
RES_0603_RES0201_R24_R24_RES_0603_0.SLDPRT
RES_0603_RES0201_R21_R21_RES_0603_0.SLDPRT
RES_0603_RES0201_R20_R20_RES_0603_0.SLDPRT
RES_0508_RES0603_R2_R2_RES_0508_1M.SLDPRT
RES_0508_RES0603_R1_R1_RES_0508_1M.SLDPRT
RES_0402_RES;1%;0402_RES_0402_NS.SLDPRT
RES_0402_RES;1%;0402_RES_0402_22_6K.SLDPRT
RES_0402_RES;1%;0402_RES_0402_20K.SLDPRT
RES_0402_RES;1%;0402_RES_0402_1K.SLDPRT
RES_0402_RES;1%;0402_RES_0402_17_4K.SLDPRT
RES_0402_RES;1%;0402_RES_0402_10K.SLDPRT
RES_0402_RES;1%;0402_RES_0402_0.SLDPRT
RES_0402_RES0603_R8E_RES_0402_0.SLDPRT
RES_0402_RES0603_R79_RES_0402_2_05K.SLDPRT
RES_0402_RES0603_R76_RES_0402_47K.SLDPRT
RES_0402_RES0603_R75_RES_0402_2_05K.SLDPRT
RES_0402_RES0603_R68_RES_0402_2_05K.SLDPRT
RES_0402_RES0603_R64_RES_0402_2_05K.SLDPRT
RES_0402_RES0603_R63_RES_0402_47K.SLDPRT
RES_0402_RES0603_R19E_RES_0402_R0402.SLDPRT
RES_0402_RES0603_R19E_RES_0402_0.SLDPRT
RES_0402_RES0603_R11E_RES_0402_169K.SLDPRT

RES_0402_RES0402_R82D_RES_0402_100.SLDPRT
RES_0402_RES0402_R82C_RES_0402_100.SLDPRT
RES_0402_RES0402_R82B_RES_0402_100.SLDPRT
RES_0402_RES0402_R82A_RES_0402_100.SLDPRT
RES_0402_RES0402_R81D_RES_0402_1K.SLDPRT
RES_0402_RES0402_R81C_RES_0402_1K.SLDPRT
RES_0402_RES0402_R81B_RES_0402_1K.SLDPRT
RES_0402_RES0402_R81A_RES_0402_1K.SLDPRT
RES_0402_RES0402_R80D_RES_0402_1K.SLDPRT
RES_0402_RES0402_R80C_RES_0402_1K.SLDPRT
RES_0402_RES0402_R80B_RES_0402_1K.SLDPRT
RES_0402_RES0402_R80A_RES_0402_1K.SLDPRT
RES_0402_RES0402_R72_RES_0402_82K.SLDPRT
RES_0402_RES0402_R71_RES_0402_82K.SLDPRT
RES_0402_RES0402_R67_RES_0402_82K.SLDPRT
RES_0402_RES0402_R66_RES_0402_82K.SLDPRT
RES_0402_RES0402_R4E_RES_0402_0.SLDPRT
RES_0402_RES0201_R8_R99_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R8_R98_RES_0402_0.SLDPRT
RES_0402_RES0201_R8_R97_RES_0402_0.SLDPRT
RES_0402_RES0201_R8_R96_RES_0402_0.SLDPRT
RES_0402_RES0201_R8_R95_RES_0402_0.SLDPRT
RES_0402_RES0201_R8_R94_RES_0402_2.SLDPRT
RES_0402_RES0201_R8_R8_RES_0402_33_2K.SLDPRT
RES_0402_RES0201_R8_R85_RES_0402_100K.SLDPRT
RES_0402_RES0201_R8_R84_RES_0402_200K.SLDPRT
RES_0402_RES0201_R7_R7_RES_0402_100.SLDPRT
RES_0402_RES0201_R7D_RES_0402_2.SLDPRT
RES_0402_RES0201_R7C_RES_0402_2.SLDPRT
RES_0402_RES0201_R7B_RES_0402_2.SLDPRT
RES_0402_RES0201_R7A_RES_0402_2.SLDPRT
RES_0402_RES0201_R6D_RES_0402_2.SLDPRT
RES_0402_RES0201_R6C_RES_0402_2.SLDPRT
RES_0402_RES0201_R6B_RES_0402_2.SLDPRT
RES_0402_RES0201_R6A_RES_0402_2.SLDPRT
RES_0402_RES0201_R69_R69_RES_0402_100K.SLDPRT
RES_0402_RES0201_R62_RES_0402_0.SLDPRT
RES_0402_RES0201_R60_RES_0402_0.SLDPRT
RES_0402_RES0201_R5_R5_RES_0402_10K.SLDPRT
RES_0402_RES0201_R5D_RES_0402_2.SLDPRT
RES_0402_RES0201_R5C_RES_0402_2.SLDPRT
RES_0402_RES0201_R5B_RES_0402_2.SLDPRT

RES_0402_RES0201_R5A_RES_0402_2.SLDPRT
RES_0402_RES0201_R58_RES_0402_0.SLDPRT
RES_0402_RES0201_R56_RES_0402_100K.SLDPRT
RES_0402_RES0201_R55_RES_0402_100K.SLDPRT
RES_0402_RES0201_R54_RES_0402_0.SLDPRT
RES_0402_RES0201_R52_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R4_R4_RES_0402_10K.SLDPRT
RES_0402_RES0201_R4_R100_RES_0402_RES0.SLDPRT
RES_0402_RES0201_R4_R100_RES_0402_10K.SLDPRT
RES_0402_RES0201_R4D_RES_0402_2.SLDPRT
RES_0402_RES0201_R4C_RES_0402_2.SLDPRT
RES_0402_RES0201_R4B_RES_0402_2.SLDPRT
RES_0402_RES0201_R4A_RES_0402_2.SLDPRT
RES_0402_RES0201_R49_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R48_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R47_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R46_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R45_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R44_RES_0402_4_7K.SLDPRT
RES_0402_RES0201_R43_RES_0402_49_9K.SLDPRT
RES_0402_RES0201_R43_RES_0402_10K.SLDPRT
RES_0402_RES0201_R40_RES_0402_10K.SLDPRT
RES_0402_RES0201_R3_R3_RES_0402_49_9K.SLDPRT
RES_0402_RES0201_R39_RES_0402_10K.SLDPRT
RES_0402_RES0201_R38_RES_0402_10K.SLDPRT
RES_0402_RES0201_R37_RES_0402_750.SLDPRT
RES_0402_RES0201_R36_RES_0402_750.SLDPRT
RES_0402_RES0201_R35_RES_0402_750.SLDPRT
RES_0402_RES0201_R34_RES_0402_750.SLDPRT
RES_0402_RES0201_R29_RES_0402_2M.SLDPRT
RES_0402_RES0201_R28_RES_0402_25K.SLDPRT
RES_0402_RES0201_R27_R27_RES_0402_1.SLDPRT
RES_0402_RES0201_R26_R26_RES_0402_1.SLDPRT
RES_0402_RES0201_R23_R23_RES_0402_1.SLDPRT
RES_0402_RES0201_R22_R22_RES_0402_1.SLDPRT
RES_0402_RES0201_R19_R19_RES_0402_1.SLDPRT
RES_0402_RES0201_R18_R18_RES_0402_1.SLDPRT
RES_0402_RES0201_R18E_RES_0402_2 OHM.SLDPRT
RES_0402_RES0201_R17_R17_RES_0402_1.SLDPRT
RES_0402_RES0201_R17E_RES_0402_2 OHM.SLDPRT
RES_0402_RES0201_R16_R93_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R92_RES_0402_1.SLDPRT

RES_0402_RES0201_R16_R91_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R90_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R89_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R88_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R87_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R86_RES_0402_1.SLDPRT
RES_0402_RES0201_R16_R16_RES_0402_1.SLDPRT
RES_0402_RES0201_R16E_RES_0402_2 OHM.SLDPRT
RES_0402_RES0201_R15E_RES_0402_2 OHM.SLDPRT
RES_0402_RES0201_R14_R14_RES_0402_2K.SLDPRT
RES_0402_RES0201_R12_R12_RES_0402_2K.SLDPRT
RES_0402_RES0201_R11_R11_RES_0402_2K.SLDPRT
RES_0402_RES0201_R10_R10_RES_0402_RES0.SLDPRT
RES_0402_RES0201_R10_R10_RES_0402_2K.SLDPRT
RES_0402_RES THERMAL_RT4_RES_0402_47K.SLDPRT
RES_0402_RES THERMAL_RT3_RES_0402_47K.SLDPRT
RES_0402_RES THERMAL_RT2_RES_0402_47K.SLDPRT
RES_0402_RES THERMAL_RT1_RES_0402_47K.SLDPRT
RES_0402_06_1019_R77_R77_RES_0402_80_6K.SLDPRT
RES_0402_06_1016_R78_R78_RES_0402_10K.SLDPRT
RES_0402_06_0882_R73_R73_RES_0402_40_2K.SLDPRT
RC1210_CAP_RC1210__2.2UF_PAC_CAP_02458.SLDPRT
RC1206_RES_RC1206_DISCRETE_9_76K,1206.SLDPRT
RC1206_RC0201FR0768K1L_RC1206_510K,12.SLDPRT
RC1206_CAP_C113_RC1206_1UF,1206.SLDPRT
RC0805_RES_RC0805__0E.SLDPRT
RC0805_RES_RC0805_DISCRETE_10K, 0_5%,.SLDPRT
RC0805_RES_0603_60R4_0.1W_1%_SMD_RC080.SLDPRT
RC0603_CRCW04026K04FKED_RC0603_549,0_5.SLDPRT
RC0402_TVS_DIODE_BIDIRECTIONAL_RC0402_.SLDPRT
RC0402_RES_RC0402__4.99K.SLDPRT
RC0402_RES_RC0402_47E.SLDPRT
RC0402_RES_RC0402_22E.SLDPRT
RC0402_RES_RC0402_10K.SLDPRT
RC0402_RES_RC0402_10.SLDPRT
RC0402_RES_RC0402_0E.SLDPRT
RC0402_RES_RC0402_0.2.SLDPRT
RC0402_RES_0402_698R0_0.1W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_4M7_0.1W_5%_SMD_RC0402.SLDPRT
RC0402_RES_0402_4K99_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_4K7_0.1W_1%_SMD_RC04_2.SLDPRT
RC0402_RES_0402_4K7_0.1W_1%_SMD_RC0402.SLDPRT

RC0402_RES_0402_49K9_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_499R0_0.1W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_39R2_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_330R0_0.1W_5%_SMD_RC04.SLDPRT
RC0402_RES_0402_30R0_0.1W_1%_SMD_RC0_4.SLDPRT
RC0402_RES_0402_30R0_0.1W_1%_SMD_RC0_3.SLDPRT
RC0402_RES_0402_30R0_0.1W_1%_SMD_RC0_2.SLDPRT
RC0402_RES_0402_30R0_0.1W_1%_SMD_RC0_1.SLDPRT
RC0402_RES_0402_30R0_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_2K0_0.1W_1%_SMD_RC0402.SLDPRT
RC0402_RES_0402_261R0_0.1W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_240R0_0.1W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_22R0_0.063W_1%_SMD_RC0.SLDPRT
RC0402_RES_0402_20R0_0.063W_1%_SMD_RC0.SLDPRT
RC0402_RES_0402_20K5_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_8.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_7.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_6.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_5.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_4.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_3.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC_2.SLDPRT
RC0402_RES_0402_1K0_0.063W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_10R0_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_10K0_0.1W_1%_SMD_RC0_1.SLDPRT
RC0402_RES_0402_10K0_0.1W_1%_SMD_RC040.SLDPRT
RC0402_RES_0402_100R0_0.1W_1%_SMD_RC04.SLDPRT
RC0402_RES_0402_0R0_0.1W_JMPR_SMD_RC04.SLDPRT
RC0402_RES_0402_0R0_0.1W_JMPR_DNP_RC04.SLDPRT
RC0402_RES0201_R8_R8_RC0402_205K.SLDPRT
RC0402_GRM188R6YA475ME15D_RC0402__4.7U.SLDPRT
RC0402_ERJ2RKF49R9X_RC0402__49.9E_ERJ.SLDPRT
RC0402_ERJ2RKF2150X_RC0402__866E_.SLDPRT
RC0402_ERJ2GEJ680X_RC0402__68E_ERJ2G.SLDPRT
RC0402_ERJ2GEJ5R6X_RC0402__0E_.SLDPRT
RC0402_ERJ2GEJ514X_RC0402__510K_ERJ2.SLDPRT
RC0402_ERJ2GEJ511X_RC0402__510E_ERJ2.SLDPRT
RC0402_ERJ2GEJ473X_RC0402__10K_ERJ2G.SLDPRT
RC0402_ERJ2GEJ472X_RC0402__4.7K_ERJ2.SLDPRT
RC0402_ERJ2GEJ393X_RC0402__0E_ERJ2GE.SLDPRT
RC0402_ERJ2GEJ392X_RC0402__10K_ERJ2G.SLDPRT
RC0402_ERJ2GEJ332X_RC0402__3.32K_.SLDPRT

RC0402_ERJ2GEJ330X_RC0402__33R_ERJ2G.SLDPRT
RC0402_ERJ2GEJ330X_RC0402__33E_ERJ2G.SLDPRT
RC0402_ERJ2GEJ272X_RC0402__2.7K_.SLDPRT
RC0402_ERJ2GEJ222X_RC0402__22K_ERJ2R.SLDPRT
RC0402_ERJ2GEJ220X_RC0402__147E_ERJ2.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__47K_ERJU0.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__40.2K_ER_1.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__40.2K_ERJ.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__37.4K_ERJ.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__32.4K_ERJ.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__300K_ERJ2.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__20K_ERJ2R.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__2.43K_ERJ.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__19.1K_ERJ.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__182K_ERJ2.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__178K_RC040.SLDPRT
RC0402_ERJ2GEJ203X_RC0402__160K_ERJ2.SLDPRT
RC0402_ERJ2GEJ182X_RC0402__1.8K_ERJ2.SLDPRT
RC0402_ERJ2GEJ104X_RC0402__100K_ERJ2.SLDPRT
RC0402_ERJ2GEJ104X_RC0402__1.21K_ERJ.SLDPRT
RC0402_ERJ2GEJ103X_RC0402__30.1K_ERJ.SLDPRT
RC0402_ERJ2GEJ103X_RC0402__274K_ERJ2.SLDPRT
RC0402_ERJ2GEJ103X_RC0402__10K_ERJ2G.SLDPRT
RC0402_ERJ2GEJ101X_RC0402__100E_ERJ2.SLDPRT
RC0402_ERJ2GE0R00X_2_RC0402__0E_ERJ2.SLDPRT
RC0402_ERJ2GE0R00X_0_RC0402__33E_ERJ.SLDPRT
RC0402_ERJ2GE0R00X_0_RC0402__0E_ERJ2.SLDPRT
RC0402_ECJ1VB1H102K_0_RC0402__1000PF_.SLDPRT
RC0402_ECJ0EF1C104Z_RC0402__0.1UF_ECJ.SLDPRT
RC0402_ECJ0EC1H101J_1_RC0402__100PF_E.SLDPRT
RC0402_ECJ0EC1H101J_0_RC0402__100PF_E.SLDPRT
RC0402_ECJ0EC1H100D_1_RC0402__10PF_EC.SLDPRT
RC0402_ECJ0EB1C103K_RC0402__0.01UF_EC.SLDPRT
RC0402_ECJ0EB0J105M_RC0402__1UF_ECJ0.SLDPRT
RC0402_CAP_NPOL_RC0402__1UF.SLDPRT
RC0402_CAP_NPOL_RC0402__1000PF.SLDPRT
RC0402_CAP_NPOL_RC0402__0.1UF.SLDPRT
RC0402_CAP_NPOL_RC0402_DISCRETE_NA.SLDPRT
RC0402_CAP_NPOL_RC0402_DISCRETE_10UF.SLDPRT
RC0402_CAP_NPOL_RC0402_DISCRETE_0.1UF.SLDPRT
RC0402_CAP_0402_4.7UF_6.3V_20%_X5R_RC0.SLDPRT
RC0402_CAP_0402_33PF_50V_5%_NP0_RC0402.SLDPRT

RC0402_CAP_0402_22PF_50V_5%_NP0_RC0402.SLDPRT
RC0402_CAP_0402_2.2UF_6.3V_20%_X5R_RC0.SLDPRT
RC0402_CAP_0402_1UF_35V_10%_X5R_RC0402.SLDPRT
RC0402_CAP_0402_100PF_50V_10%_X7R_RC04.SLDPRT
RC0402_CAP_0402_0.22UF_25V_10%_X6S_R_3.SLDPRT
RC0402_CAP_0402_0.22UF_25V_10%_X6S_R_2.SLDPRT
RC0402_CAP_0402_0.22UF_25V_10%_X6S_R_1.SLDPRT
RC0402_CAP_0402_0.22UF_25V_10%_X6S_RC0.SLDPRT
RC0402_CAP_0402_0.1UF_25V_10%_X5R_RC_2.SLDPRT
RC0402_CAP_0402_0.1UF_25V_10%_X5R_RC_1.SLDPRT
RC0402_CAP_0402_0.1UF_25V_10%_X5R_RC04.SLDPRT
RC0402_CAP_0402_0.047UF_10V_10%_X5R_RC.SLDPRT
RC0402_CAP_0402_0.01UF_25V_10%_X5R_RC0.SLDPRT
RC0402_4_7UF_0402.SLDPRT
RC0402_4_70K_0402.SLDPRT
RC0402_499_0402.SLDPRT
RC0402_39_2_0402.SLDPRT
RC0402_36_0402.SLDPRT
RC0402_240_0402.SLDPRT
RC0402_22X22_8OCT_RES_0402_4K7_0.1W_1%_SMD_RC04_1.SLDPRT
RC0402_22X22_8OCT_RES_0402_2K49_0.1W_1%_SMD_RC040.SLDPRT
RC0402_22X22_8OCT_RES_0402_1K0_0.063W_1%_SMD_RC_1.SLDPRT
RC0402_0_47UF_0402.SLDPRT
RC0402_0_1UF_0402.SLDPRT
RC0402_0_068UF_0402.SLDPRT
RC0402_0_01UF_0402.SLDPRT
RC0201_RES;1%;0201_RC0201_NS.SLDPRT
RC0201_RES;1%;0201_RC0201_62K.SLDPRT
RC0201_RES;1%;0201_RC0201_3_6K.SLDPRT
RC0201_RES;1%;0201_RC0201_10K.SLDPRT
RC0201_RES;1%;0201_RC0201_0.SLDPRT
RC0201_ERJ2RKF2150X_RC0201__866E_ERJ.SLDPRT
RC0201_ERJ2RKF2150X_RC0201__2K_ERJ1G.SLDPRT
RC0201_ERJ2RKF1691X_RC0201__866E_ERJ.SLDPRT
RC0201_ERJ2RKF1691X_RC0201__11.5_RMCF.SLDPRT
RC0201_ERJ2GEJ5R6X_RC0201__5.6E_ERJ1.SLDPRT
RC0201_ERJ2GEJ5R6X_RC0201__0E_ERJ1GN.SLDPRT
RC0201_ERJ2GEJ5R6X_RC0201__0E_.SLDPRT
RC0201_ERJ2GEJ103X_RC0201__1K_.SLDPRT
RC0201_ERJ2GEJ103X_RC0201__10K_.SLDPRT
RC0201_ERJ2GE0R00X_2_RC0201__0E_ERJ2.SLDPRT
RC0201_ECJ0EF1C104Z_RC0201__0.01UF_EC.SLDPRT

RC0201_ECJ0EC1H101J_0_RC0201__3.6PF_0.SLDPRT
RC0201_ECJ0EC1H101J_0_RC0201__20PF_EC.SLDPRT
RC0201_ECJ0EC1H101J_0_RC0201__100PF_0.SLDPRT
RC0201_ECJ0EC1H101J_0_RC0201__100PF_.SLDPRT
RC0201_ECJ0EC1H101J_0_RC0201__1.2NH_.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__8.2PF_E.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__5.1PF_E.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__3PF_ECJ.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__1PF_ECJ.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__1.8PF_E.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__1.5PF_E.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__0.8PF_E.SLDPRT
RC0201_ECJ0EC1H100D_1_RC0201__0.1UF_C.SLDPRT
RC0201_CAP_0201_0.01UF_16V_10%_X5R_RC0.SLDPRT
RC0201_CAP;25V;X7R;0201_RC0201_220NF.SLDPRT
RC0201_CAP;25V;X7R;0201_RC0201_10NF.SLDPRT
RC0201_0402CS18N_0_RC0201__3.6NH_0402.SLDPRT
RC0201_0402CS18N_0_RC0201__2NH_0402CS.SLDPRT
RC0201_0402CS18N_0_RC0201__10NH_0402C.SLDPRT
R0508_R0508_RS1E_R0508_3MOHM.SLDPRT
R0508_R0508_RS1D_R0508_1MOHM.SLDPRT
R0508_R0508_RS1C_R0508_3MOHM.SLDPRT
R0508_R0508_RS1B_R0508_3MOHM.SLDPRT
R0508_R0508_RS1A_R0508_3MOHM.SLDPRT
QFN_D65X4D_16SP_4WL1H_G1_SKY13372467LF_1_QFN_D65X4D_16S.SLDPRT
QFN_D5_14SP_3WL_MPQ1918AEC1_U13_3_QFN_D5_14SP_.SLDPRT
QFN_D5X4D_24SP_4WL1H_2D8SQ_FB_RFSA3714TR13_0_QFN_D5X4D_24SP_4.SLDPRT
QFN_D5X3D_16SP_3WLD9H_FB_TQP7M9104_3_QFN_D5X3D_16SP_3WLD.SLDPRT
QFN_D5X3D_12SP_3WL_PMA3_83LN+_QFN_D5X3D_12SP_3WL_I.SLDPRT
QFN_D5X2D_8SP_2WL_G35X63_TQL9063_2_QFN_D5X2D_8SP_2WL_G35.SLDPRT
QFN_D5X2D_8SP_2WL_G35X63_SKY67153_396LF_QFN_D5X2D_8SP_2W.SLDPRT
QFN_D5X2D8_20SP_3W5L_MPM3632C_2_QFN_D5X2D8_20SP_3W5L.SLDPRT
QFN_D5X1D9_14SP_2W3L1H_MP2332C_2_QFN_D5X1D9_14SP_2W3L1.SLDPRT
QFN_D5X10D_72SP_10WL1H_SQ340_SI5500_72PIN_CUSTOMER_2_QFN_D5X.SLDPRT
QFN_D4X3D1_30SP_5WL1H_IC;MP5048_30PIN_QFN_D4X3D1_30SP.SLDPRT
QFN_D4X2D7_16SP_3WL1H_1_MP8770CGQP_1_QFN_D4X2D7_16SP_3.SLDPRT
QFN_D4X2D5X4D7_25SP_4W5L_MP8796B_2_QFN_D4X2D5X4D7_25SP_4.SLDPRT
QFN8_2X2MM_EP_MPQ20056_U21_QFN8_2X2MM_EP_MPQ2.SLDPRT
QFN56_6X6MM_EP_MP2845A_U15_QFN56_6X6MM_EP_MP28.SLDPRT
QFN24_4X4MM_EP_MP2981_U11_U11_1_QFN24_4X4MM_EP.SLDPRT
QFN203X4MM_MP2908GL_U3E_QFN203X4MM_MP2908.SLDPRT
PARTBODY_3.SLDPRT

PARTBODY_2.SLDPRT
PAR-00304847_ASM.SLDASM
PAM_THERMAL_COMPONENTS.SLDPRT
PAM_SHIELD_GASKET.SLDPRT
PAD30MIL_PAD20MIL_TL1PE_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1PD_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1PC_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1PB_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1PA_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1NE_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1ND_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1NC_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1NB_PAD30MIL_0_5X0_5.SLDPRT
PAD30MIL_PAD20MIL_TL1NA_PAD30MIL_0_5X0_5.SLDPRT
OUTLINE_PANEL0323_OUTLINE_PANEL0323.SLDPRT
OUTLINE2112211_OUTLINE2112211.SLDPRT
OSC_5D08X8D5_4SP_14W9L3H_CVHD950_OSC_5D08X8D5_4SP_14W9L.SLDPRT
OSC_1D7X2D2_4SP_2D5W3D2L1D1H_OSC_4PIN_OE_OSC_1D7X2D2_4SP_2D5.SLDPRT
OSC_1D55X1D65_4SP_2D5W2LD9H_OSC_OSC_1D55X1D65_4SP_2D5W2LD9H.SLDPRT
OSC_1D2X4D_8SP_5W3D2L_TCXO_8P_VT803_OSC_1D2X4D_8SP_5W.SLDPRT
MTH_276C134D_8DP_SCREW_8_1_MTH_276C134D_8DP_SCRE.SLDPRT
MSOP_D65X4D35_8SP_3WL_G1_TPS54061_2_MSOP_D65X4D35_8SP_3W.SLDPRT
MPLG10B31_EPC4025_U2E_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U2D_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U2C_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U2B_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U2A_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U1E_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U1D_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U1C_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U1B_MPLG10B31_EPC2045.SLDPRT
MPLG10B31_EPC4025_U1A_MPLG10B31_EPC2045.SLDPRT
Mounting_Pole.SLDPRT
M8X25_Button Head Torx Screws_92832A596.SLDPRT
M6_WASHER_98688A116.SLDPRT
M6X16_Button Head Torx Screws_92832A544.SLDPRT
M4X6 Button Head Torx Screw 90991A119.SLDPRT
M4X30_Button Head Torx Screw_90910A580.SLDPRT
M4X16_Button Head Torx Screw_90991A124.SLDPRT
M3X8_Button Head Torx Screw_90991A113.SLDPRT
M3X12_Button Head Torx Screw_90991A115.SLDPRT
LED_0603_LED_UNI_GRN_2V2_0A02_SS_SM_0_LE.SLDPRT

LED_0603_LED_RED_2V0_20MA_SMLLX0603IW_0.SLDPRT
LC0402_FX_IND_15NH_1.2A_0402DC15NXGRW.SLDPRT
LC0402_0402CS51N_LC0402__8.7NH_MLG060.SLDPRT
LC0402_0402CS51N_LC0402__39NH_MLG0603.SLDPRT
LC0402_0402CS51N_LC0402__39NH_0402CS.SLDPRT
LC0402_0402CS18N_0_LC0402__56NH_0402C.SLDPRT
LC0402_0402CS18N_0_LC0402__18NH_0402C.SLDPRT
LABEL_20D5X5MM_.SLDPRT
IN_7P8X7P6MM_IN8X8MM_DML1_IN_7P8X7P6MM_1UH.SLDPRT
IND_SRP4020TA_SRP4020TA2R2M_2_2UH.SLDPRT
IND_SRP4020TA_SRP4020TA1R2M_1_2UH.SLDPRT
IND_SRP4020TA_FX_IND_3.3UH_5.A_SRP4020TA3R3M.SLDPRT
IND_MPS_SE6040_03_1002_L1_L1_IND_MPS_SE6040_22.SLDPRT
IND_IHLP2020BZ_ER_IND_4.7U_3.2_20_5.49X5.18_SMD_H.SLDPRT
IND_8D95_2SP_10WL4H_XAL1510333MEB_IND_8D95_2SP_10W.SLDPRT
IND_8D29_2SP_11D43W11D18L_INDUCTOR_270NH.SLDPRT
IND_5D45_2SP_6D6WL5H_SRP7050TA1R2M_1P2UH.SLDPRT
IND_4D4_2SP_5D2WL3H_IND_IND_4D4_2SP_5D2WL3H_4.7UH.SLDPRT
IND_2P_SMT_XAL7070_IND_1UH_20%_25A_XAL7070102MEC_.SLDPRT
IND_15D1X13D15_2SP_19D9W18D5L_TDK_B82559B_L1D_0_IND_15D1X13D1.SLDPRT
IND_15D1X13D15_2SP_19D9W18D5L_TDK_B82559B_L1C_0_IND_15D1X13D1.SLDPRT
IND_15D1X13D15_2SP_19D9W18D5L_TDK_B82559B_L1B_0_IND_15D1X13D1.SLDPRT
IND_15D1X13D15_2SP_19D9W18D5L_TDK_B82559B_L1A_1_IND_15D1X13D1.SLDPRT
HSMF_C15X_HSMFC155.SLDPRT
HEATSINK_SFP_OFTI_SUB.SLDASM
HEATSINK_SFP_OFTI_GASKET.SLDPRT
HEATSINK_SFP_OFTI.SLDPRT
HDR_1X3_LOCK_MOLEX_WM2701ND.SLDPRT
GUTBN6-8-20.stp
GUTBN6-8-20.SLDPRT
GNSS_LEAM8F_N_LEAM8T_9_GNSS_LEAM8F_N_LEAM8.SLDPRT
FUSE_SMD_3X3MM_FUSESMD_3X4P5MM_F2_FUSE_SMD_3X.SLDPRT
FUSE_SMD_3X3MM_FUSESMD_3X4P5MM_F1_FUSE_SMD_3X.SLDPRT
FULLAXS_BULKEAD_02_WITH_CAP_ASM.SLDASM
Filter_Gasket.SLDPRT
FILTER_D51XD79_6SP_1D6WD79L_TCW2282+_FILTER_D51XD79_6SP_1D.SLDPRT
FILTER_3D25X1D855_4SP_3D2W1D6L_PHA1H+_1_FILTER_3D25X1D855_4_3.SLDPRT
FILTER_3D25X1D855_4SP_3D2W1D6L_PHA1H+_1_FILTER_3D25X1D855_4_2.SLDPRT
FILTER_3D25X1D855_4SP_3D2W1D6L_PHA1H+_1_FILTER_3D25X1D855_4_1.SLDPRT
FILTER_3D25X1D855_4SP_3D2W1D6L_PHA1H+_1_FILTER_3D25X1D855_4SP.SLDPRT
FILTER_1D1X2D25_6SP_3WL1D09H_SF2133E_FILTER_1D1X2D25_6SP_3_1.SLDPRT
FIDBRD_40C120SM_.SLDPRT

FCQ1710T2169B1B3-3.SLDASM
FCQ1710T2169B1B3-3-PCB.SLDPRT
FCQ1710T2169B1B3-3-LPF.SLDPRT
FCQ1710T2169B1B3-3-CWG.SLDPRT
FB1206_IND_1206_50OHM_12A_FERRITEBEAD_.SLDPRT
evenstar_rfsoc_components_120821_Thermal.stp
evenstar_rfsoc_components_120821.stp
EVENSTAR_RFSOC_BOARD.SLDASM
EVENSTAR_PSU_ASM.SLDASM
evenstar_pam_components_120221.stp
evenstar_pam_components_120221.SLDASM
EVENSTAR_PAM-SPA-1124-1340 (1).stp
EvenStar_8T8R_Radio_Bracket_Adapter.SLDPRT
EvenStar_8T8R_Radio_Bracket.SLDPRT
EvenStar_8T8R_PAM_PALLET.SLDPRT
EvenStar_8T8R_PAM_Board.SLDPRT
EvenStar_8T8R_PAM_ASM.SLDASM
Evenstar_8T8R_Hand_Bracket_V2.SLDPRT
EvenStar_8T8R_ASM_OFTI_FEA.SLDASM
EvenStar_8T8R_ASM_OFTI.SLDASM
EPC2218_EPC2218_U8_U8_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U7_U7_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U6_U6_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U5_U5_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U4_U4_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U3_U3_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U2_U2_EPC2218_MPTG10B53.SLDPRT
EPC2218_EPC2218_U1_U1_EPC2218_MPTG10B53.SLDPRT
EMI_Shield_Ring.SLDPRT
DPAK_4D572X7D18_3SP_6D54W_A1A2C_DIODE1_4_DPAK_4D572X7D18_3SP_6D.SLDPRT
DFN_D5X3D_10SP_3WLD9H_IC_REG_TPS51200QDRCRQ1_0_DFN_D5.SLDPRT
DB_PAM_TEST_ASM.SLDASM
CONN_RF_1SP_7D5W6L3D5H_PSMP CONN_0_CONN_RF_1SP_7D5W6L3.SLDPRT
CONN_RF_1SP_5WL_SMP CONN_0_CONN_RF_1SP_5WL_19S1.SLDPRT
CONN_5D87_4DP_7WL_CON_THREADED_4_CONN_5D87_4DP_7W.SLDPRT
CONN_40PIN_2MM_SOCKET_CONN40PIN2MMSOCKET_J2_CONN_4.SLDPRT
CONN_2D_1X4SP_8W2D15H_HEADER 4_CONN_2D_1X4SP_8W2D15H_.SLDPRT
CONN_2DX5D12_2X8SP_16W4L_HDR_2MM_VER_2X8_CONN_2DX5D12_2X.SLDPRT
CONN_2DX5D12_2X3SP_6W4L_HDR_2MM_VER_2X3_CONN_2DX5D12_2X.SLDPRT
CONN_2DX5D12_2X2SP_4WL_HDR_2MM_VER_2X2_CONN_2DX5D12_2X.SLDPRT
CONN_2DX4D1_2X8SP_16W4L_CON16A_CONN_2DX4D1_2X8SP_16W4L_.SLDPRT

CONN_2DX4D1_2X20SP_40W4L_HDR_2MM_VER_2X20_CONN_2DX4D1_2X.SLDPRT
CONN_2DX4D1_2X10SP_20W4L_CON20A_CONN_2DX4D1_2X10SP_20W4L.SLDPRT
CONN_2D54_6SP_15D24W2D41L_HEADER 6_CONN_2D54_6SP_15D24W2D.SLDPRT
CONN_2D54_3SP_7D62W2D41L_HDR_100M_VER_1X3_M_PBC36SAAN_CO.SLDPRT
CONN_2D54_1X2DP_5D08W8D54H_SWITCH_0_CONN_2D54_1X2DP_5D08W8.SLDPRT
CHOCK_13DX19D_4DP_26WL_FIL_CHOKE_POL_CHOCK_13DX19D_4DP.SLDPRT
Chassis_Top_8T8R_OFTI_Sub.SLDASM
Chassis_Top_8T8R_OFTI_Gasket.SLDPRT
Chassis_Top_8T8R_OFTI.SLDPRT
CHASSIS_SERVICE_PORT_GASKET.SLDPRT
Chassis_Bottom_8T8R_OFTI.SLDPRT
Chassis_Access_Panel.SLDPRT
CC2220_CAP_FIXED_CC2220_DISCRETE_100N_.SLDPRT
CC1210_GRM188R72A104KA35D_CC1210_10UF,.SLDPRT
CC1210_CAP_FIXED_CC1210_IC_2U2_CAP1003.SLDPRT
CC1206_CAP_NPOL_CC1206__10UF.SLDPRT
CC1206_CAP_FIXED_CC1206_DISCRETE_4U7_C.SLDPRT
CC1206_CAP_C113_CC1206_1UF,1206.SLDPRT
CC1206_CAP NP_CC1206_1NF,1206.SLDPRT
CC0805_CAP_CC0805_DISCRETE_4_7UF,50V,0.SLDPRT
CC0805_CAP_1_CC0805_DISCRETE_10UF_PAC_.SLDPRT
CC0805_CAP_1210_47UF_6.3V_20%_X6S_CC08.SLDPRT
CC0805_06035A681FAT2A_CC0805_22NF,0805.SLDPRT
CC0402_CAP_NPOL_CC0402_DISCRETE_0.1UF.SLDPRT
CC0402_CAP_NPOL_CC0402_DISCRETE_0.01UF.SLDPRT
CC0402_CAP_1_CC0402_DISCRETE_3.3NH_PAC.SLDPRT
CC0402_CAP_1_CC0402_DISCRETE_0.1UF_PAC.SLDPRT
CC0402_22X22_8OCT_RES_0402_20K5_0.1W_1%_SMD_CC040.SLDPRT
CC0402_22X22_8OCT_RES_0402_100R0_0.1W_1%_SMD_CC04.SLDPRT
CC0402_22X22_8OCT_RES_0402_0R0_0.1W_JMPR_SMD_CC04.SLDPRT
CC0402_22X22_8OCT_CAP_NPOL_CC0402_22X22_8OCT__1UF.SLDPRT
CC0402_22X22_8OCT_CAP_0402_4.7UF_6.3V_20%_X5R_CC0.SLDPRT
CC0402_22X22_8OCT_CAP_0402_1UF_35V_10%_X5R_CC0402.SLDPRT
CC0402_22X22_8OCT_CAP_0402_0.47UF_10V_10%_X5R_CC0.SLDPRT
CC0402_22X22_8OCT_CAP_0402_0.1UF_25V_10%_X5R_CC04.SLDPRT
CAP_1210_CAP1210_CX6_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO9E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO8E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO7E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO6E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO6D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO6C_CAP_1210_10UF.SLDPRT

CAP_1210_CAP1210_CO6B_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO6A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO5E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO5D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO5C_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO5B_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO5A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO4E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO4D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO4C_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO4B_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO4A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO3E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO3D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO3C_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO3B_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO3A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO2E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO2D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO2C_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO2A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO1E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO1D_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO1C_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO1B_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO1A_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_CO10E_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C93D_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C93C_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C93B_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C93A_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C92D_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C92C_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C92B_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C92A_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C91D_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C91C_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C91B_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C91A_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C3E_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C3D_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C3C_CAP_1210_10UF100V.SLDPRT

CAP_1210_CAP1210_C3B_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C3A_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C34_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C33_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C32_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C31_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C30_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C2E_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C2D_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C2C_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C2B_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C2A_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C29_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C28_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C27_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C26_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C25_CAP_1210_10UF.SLDPRT
CAP_1210_CAP1210_C1E_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C1D_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C1C_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C1B_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C1A_CAP_1210_10UF100V.SLDPRT
CAP_1210_CAP1210_C16E_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C15E_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C14E_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C105_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C104_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C103_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C102_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C101_CAP_1210_10UF100.SLDPRT
CAP_1210_CAP1210_C100_CAP_1210_10UF100.SLDPRT
CCAP_0805_CAP;100V;X7R;0805_C74_C74_CAP_0.SLDPRT
CAP_0805_CAP0805_C90D_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C90C_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C90B_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C90A_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C87D_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C87C_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C87B_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C87A_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4_C8_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4_C4_CAP_0805_1UF.SLDPRT

CAP_0805_CAP0805_C4E_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4D_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4C_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4B_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C4A_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C3_C5_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C3_C3_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C2_C2_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C1_C7_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0805_C13E_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C24_C24_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C23_C23_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C22_C22_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C21_C21_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C20_C20_CAP_0805_1UF.SLDPRT
CAP_0805_CAP0603_C19_C19_CAP_0805_1UF.SLDPRT
CAP_0603_CAP;25V;X7R;0603_CAP_0603_1UF.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C83_C83_CAP_06.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C82_C82_CAP_06.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C40_C85_CAP_06.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C40_C40_CAP_06.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C37_C37_CAP_06.SLDPRT
CAP_0603_CAP;16V;X6S;0603_C35_C35_CAP_06.SLDPRT
CAP_0603_CAP;100V;X7R;0603_C75_C75_CAP_0.SLDPRT
CAP_0603_CAP0603_C9_C9_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0603_C8E_CAP_0603_1UF16V.SLDPRT
CAP_0603_CAP0603_C88D_CAP_0603_100NF.SLDPRT
CAP_0603_CAP0603_C88C_CAP_0603_100NF.SLDPRT
CAP_0603_CAP0603_C88B_CAP_0603_100NF.SLDPRT
CAP_0603_CAP0603_C88A_CAP_0603_100NF.SLDPRT
CAP_0603_CAP0603_C7E_CAP_0603_1UF16V.SLDPRT
CAP_0603_CAP0603_C65_C65_CAP_0603_4_7UF.SLDPRT
CAP_0603_CAP0603_C64_C64_CAP_0603_4_7UF.SLDPRT
CAP_0603_CAP0603_C63_C63_CAP_0603_470NF.SLDPRT
CAP_0603_CAP0603_C62_C62_CAP_0603_470NF.SLDPRT
CAP_0603_CAP0603_C20E_CAP_0603_100PF.SLDPRT
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CAP_0603_CAP0603_C15_C15_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0603_C14_C14_CAP_0603_220NF.SLDPRT

CAP_0603_CAP0603_C13_C13_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0603_C12_C12_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0603_C11_C11_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0603_C10_C10_CAP_0603_220NF.SLDPRT
CAP_0603_CAP0402_C9E_CAP_0603_47NF.SLDPRT
CAP_0603_CAP0402_C7A_CAP_0603_390PF.SLDPRT
CAP_0603_CAP0402_C51_C51_CAP_0603_4_7UF.SLDPRT
CAP_0603_CAP0402_C50_C50_CAP_0603_4_7UF.SLDPRT
CAP_0603_CAP0402_C49_C49_CAP_0603_470NF.SLDPRT
CAP_0603_CAP0402_C48_C48_CAP_0603_470NF.SLDPRT
CAP_0603_CAP0402_C11E_CAP_0603_220NF, 0.SLDPRT
CAP_0603_CAP0402_C10A_CAP_0603_390PF.SLDPRT
CAP_0603_CAP0201_C69_C69_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C68_C68_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C67_C67_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C66_C66_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C55_C55_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C54_C54_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C53_C53_CAP_0603_NS.SLDPRT
CAP_0603_CAP0201_C52_C52_CAP_0603_NS.SLDPRT
CAP_0402_CAP;50V;X7R;0402_CAP_0402_NS.SLDPRT
CAP_0402_CAP;50V;X7R;0402_CAP_0402_0_1UF.SLDPRT
CAP_0402_CAP;25V;X7R;_C92_CAP_0402_NS.SLDPRT
CAP_0402_CAP;25V;X7R;_C72D_CAP_0402_100P.SLDPRT
CAP_0402_CAP;25V;X7R;_C72C_CAP_0402_100P.SLDPRT
CAP_0402_CAP;25V;X7R;_C72B_CAP_0402_100P.SLDPRT
CAP_0402_CAP;25V;X7R;_C72A_CAP_0402_100P.SLDPRT
CAP_0402_CAP;25V;X6S;0402_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP;16V;X7R;0402_CAP_0402_680PF.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C84_C84_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C81_C81_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C76_C76_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C41_C86_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C41_C41_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C38_C38_CAP_04.SLDPRT
CAP_0402_CAP;16V;X7R;0402_C36_C36_CAP_04.SLDPRT
CAP_0402_CAP0603_C6E_CAP_0402_220NF, 04.SLDPRT
CAP_0402_CAP0603_C18E_CAP_0402_2_2UF10.SLDPRT
CAP_0402_CAP0402_C9D_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP0402_C9C_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP0402_C9B_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP0402_C9A_CAP_0402_2_2UF.SLDPRT

CAP_0402_CAP0402_C94_CAP_0402_560PF.SLDPRT
CAP_0402_CAP0402_C89C_CAP_0402_100NF.SLDPRT
CAP_0402_CAP0402_C89B_CAP_0402_100NF.SLDPRT
CAP_0402_CAP0402_C80_CAP_0402_560PF.SLDPRT
CAP_0402_CAP0402_C79_CAP_0402_560PF.SLDPRT
CAP_0402_CAP0402_C78_C78_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP0402_C78_C71_CAP_0402_2_2UF.SLDPRT
CAP_0402_CAP0402_C77_C77_CAP_0402_NS.SLDPRT
CAP_0402_CAP0402_C73_CAP_0402_560PF.SLDPRT
CAP_0402_CAP0402_C70_CAP_0402_1UF.SLDPRT
CAP_0402_CAP0402_C6D_CAP_0402_0_22UF.SLDPRT
CAP_0402_CAP0402_C6C_CAP_0402_0_22UF.SLDPRT
CAP_0402_CAP0402_C6B_CAP_0402_0_22UF.SLDPRT
CAP_0402_CAP0402_C6A_CAP_0402_0_22UF.SLDPRT
CAP_0402_CAP0402_C5E_CAP_0402_100PF.SLDPRT
CAP_0402_CAP0402_C57_CAP_0402_0_01UF.SLDPRT
CAP_0402_CAP0402_C56_CAP_0402_1UF.SLDPRT
CAP_0402_CAP0402_C44_C91_CAP_0402_100NF.SLDPRT
CAP_0402_CAP0402_C44_C90_CAP_0402_100NF.SLDPRT
CAP_0402_CAP0402_C44_C89D_CAP_0402_100N.SLDPRT
CAP_0402_CAP0402_C44_C89A_CAP_0402_100N.SLDPRT
CAP_0402_CAP0402_C44_C44_CAP_0402_1UF.SLDPRT
CAP_0402_CAP0402_C42_C42_CAP_0402_1UF.SLDPRT
CAP_0402_CAP0402_C39_C39_CAP_0402_1UF.SLDPRT
CAP_0402_CAP0402_C17E_CAP_0402_0_22UF1.SLDPRT
CAP_0402_CAP0402_C12E_CAP_0402_NS.SLDPRT
CAP_0402_CAP0402_C10E_CAP_0402_100PF.SLDPRT
CAP_0402_CAP0201_C45_C93_CAP_0402_10NF.SLDPRT
CAP_0402_CAP0201_C45_C87_CAP_0402_1NF.SLDPRT
CAP_0402_CAP0201_C45_C45_CAP_0402_100PF.SLDPRT
CAP_0402_CAP0201_C45_C43_CAP_0402_NS.SLDPRT
CAPALE_6D3PI_5D6_2SP_6D6WL8H_CAPELCSMD6X6MM_CO12A_0_CAPAL.SLDPRT
C09131T0081002U.SLDPRT
c-2299056-1-a-3d.stp
BREP_WITH_VOIDS_1.SLDPRT
BGA_D8_16C_96SP_7D5W13D5L_MT40A512M16JY075E_BGA_D8_16C_9.SLDPRT
BGA_1D_20C_1156SP_35WL4D01H_XZCU67_0_BGA_1D_20C_1156SP_35WL.SLDPRT
BGA153_0O5MM_14X14_H1_ISSI_IC_EMMC4GB_2V73V6_153BGA_BGA.SLDPRT
BATTERY_6D8PI_1D5_2SP_2D8H_BATTERY_1_BATTERY_6D8PI_1D5_2SP.SLDPRT
ATS-HP-F8L300S19W-359.SLDPRT
91595A112_Dowel Pin.SLDPRT
90991A121_18-8 Stainless Steel Button Head Torx Screws.SLDPRT

8T8R_OF_SUB_CERAMIC.SLDASM
8T8R_OF_SUB.SLDASM
8T8R_OF_GASKET.SLDPRT
8T8R_OF_COVER_PLATE_GASKET.SLDPRT
8T8R_OF_COVER_PLATE_CERAMIC_GASKET.SLDPRT
8T8R_OF_COVER_PLATE_CERAMIC.SLDPRT
8T8R_OF_COVER_PLATE.SLDPRT
8T8R_OF_COVER_CERAMIC.SLDPRT
8T8R_OF.SLDPRT
87832_1420_HDR_100M_RA_2X7_2MM_878321420_.SLDPRT
3POS-SOCKET-RTS014N3S03.SLDPRT
225-97-08SBEX.SLDPRT
225-97-08SBEX(Without CAP).stp
225-97-03SBEX.SLDPRT
225-97-03SBEX(Without CAP).stp
225-97-02SBEX.SLDPRT
225-97-02SBEX(Without CAP).stp
164S164-K0A_SW0002_1_ASM.SLDASM
164S164-K0A_SW0002_1_2.SLDPRT
164S164-K0A_SW0002_1_1.SLDPRT
1224_SMT_LVK25R005FER.SLDPRT
1210_GRJ32ER71C226KE11L_1210__22UF_G.SLDPRT
1210_CAP_1210_10UF_25V_10%_X5R_1210_.SLDPRT
1210_CAP_1210_100UF_6.3V_20%_X6S_121.SLDPRT
1206_CAP_1206_22UF_25V_10%_X5R_1206_.SLDPRT
1206_CAP_1206_100UF_4V_20%_X6S_1206_.SLDPRT
119s202-40m_c00_confidential.stp
119s202-40m_c00.SLDPRT
0805_FER_600_500MA_SM_HZ0805E601R10.SLDPRT
0805_ECJ0EF1C104Z_0805__4.7UF_ECJ0.SLDPRT
0805_CAP_0805_10UF_10V_10%_X5R_080_1.SLDPRT
0805_CAP_0805_10UF_10V_10%_X5R_0805_.SLDPRT
0805_BLM21BB050SN1D_0805__BLM21PG220.SLDPRT
0805_BLM21BB050SN1D_0805__BLM21BB050.SLDPRT
0603_RES_0603_100R0_0.1W_1%_SMD_0603.SLDPRT
0603_IND_MAG_HIND_MAG_2PIN_0603_1.5U.SLDPRT
0603_FER_120_3A_SM_BLM18SG121TN1D_06.SLDPRT
0402_ERJ2GEJ470X_0402__47E_ERJ2GEJ.SLDPRT
0402_ECJ0EF1C104Z_0402__0.1UF_ECJ0.SLDPRT

B-6: Digital Pre-Distortion (DPD) Modeling

huber_suhner_cable.s2p
CLIN.s2p
CABLE GORE LIN.s2p
scpi_sockets.py
README.txt
IQServer_Test.py
IQServer_Test_Single_Pulse.py
IQServer_Test_Commands.py
IQServer_Test_Combine_Pulse.py
IQServer_Test_Calibration.py
IQServer_Scratch.py
vsafunc.p
vsafunc.m
sym2wfm.m
ssprq.csv
seqtest5.m
seqtest3.m
seqtest3_gui.m
seqtest3_gui.fig
seqtest2.m
seqtest1.m
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README.docx
ptrnfile2data.m
overlapsave.m
osc2data.m
nlSynthesizer.p
nlSynthesizer.m
nlEmulator.p
nlEmulator.m
MultiPDWProcessExample.m
multiCarrierControl_GUI.m
multiCarrierControl_GUI.fig
multiCalDialog_GUI.m
multiCalDialog_GUI.FIG
multi_pulse.m
multi_pulse_gui.m
multi_pulse_gui.fig

multi_pdw.p
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multi_channel_sync.m
multi_channel_sync_gui.m
multi_channel_sync_gui.fig
multi_16channel_sync.m
multi_16channel_sync_gui.m
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mpulse_demo.m
mma_demo.m
mma_demo.fig
loadArbConfig.m
keysight.m
keysight_logo.png
keysight_gui.m
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isplot.m
iseriallarge.m
iserial.m
iserial_gui.m
iserial_gui.fig
iqwaitbar.m
iqvsamultical.m
iqvsamtone.p
iqvsamtone.m
iqvsacqm.m
iqvsacal.p
iqvsacal.m
iqvsabandpower.m
iqtosound_gui.m
iqtosound_gui.fig
IQToolsServerCommands.html
IQToolsServer.m
IQToolsServer_Launch.m
iqtools.m
iqtools.exe.config
iqtone.m
iqtone_gui.m
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iqstreamtool.m
iqstreamtool_gui.m
iqstreamtool_gui.fig

iqsteptone.m
qstepresample.p
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iqsteppulse.m
iqsplitchan.m
iqshowcorr.m
iqseq.m
iqseq_M8195A_gui.m
iqseq_M8195A_gui.fig
iqseq_M8190A_gui.m
iqseq_M8190A_gui.fig
iqseq_gui.m
iqseq_gui.fig
iqScratchDir.m
iqsavewaveform.m
iqsavesettings.m
iqrsim.m
iqrsim_gui.m
iqrsim_gui.fig
iqresample.p
iqresample.m
iqrecordinglist.m
iqrecordinglist.fig
iqreadscope.m
iqreadM8131A.m
iqreadM3xxxA.m
iqreaddcatek.m
iqreaddca.m
iqpulsegen.m
qpulsegen_gui.m
iqpulsegen_gui.fig
iqpulse.m
iqpulse_gui.m
iqpulse_gui.fig
iqpowersensorcal.m
iqplot.m
iqoptcheck.m
iqopen.m
iqofdm.m
iqofdm_gui.m
iqofdm_gui.fig
iqnoiseseq.m

iqnoiseseq_gui.m
iqnoiseseq_gui.fig
iqnoise.m
iqmtcal.p
iqmtcal.m
iqmtcal_gui.m
iqmtcal_gui.fig
iqmohawk.m
iqmohawk_gui.m
iqmohawk_gui.fig
iqmod.m
iqmod_gui.m
iqmod_gui.fig
iqmergecorr.m
iqmain8070.m
qmain8070.fig
iqmain.m
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iqloadsettings.m
iqloadfile.m
iqloadfile_gui.m
iqloadfile_gui.fig
iqkandoupattern.p
iqkandoupattern.m
iqkandou_gui.m
iqkandou_gui.fig
iqinterleavecal.m
iqinterleavecal_gui.m
iqinterleavecal_gui.fig
iqgeneratecode.m
iqfsk.m
iqfsk_gui.m
iqfsk_gui.fig
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iqseq_M8195A_gui.m
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iqseq_M8190A_gui.m
iqseq_M8190A_gui.fig
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iqcorrmgmt.m
iqcorrmgmt.fig
iqcorrection.phuber_suhner_cable.s2p
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Single_Pulse_Vector.setx
Single_Pulse_Pulse.setx
Multi_Pulse_Vector.setx
scpi_sockets.py
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IQServer_Test.py
IQServer_Test_Single_Pulse.py
IQServer_Test_Commands.py
IQServer_Test_Combine_Pulse.py
IQServer_Test_Calibration.py

IQServer_Scratch.py
Single_Pulse_Vector.setx
Single_Pulse_Pulse.setx
Multi_Pulse_Vector.setx

B-7: Crest Reduction Factor (CFR) Modeling

LP_12288M_9_1M_9_7M_05dB_20dB.m
LP_12288M_9_1M_9_7M_div2_05dB_20dB.m
LP_12288M_9_1M_9_7M_div4_05dB_20dB.m
cfrFix_itr2_v2.m
cfrFix_itr2_v3.m
cordicMagFix2.m
cordicMagFix2FastVec_v2_mex.mexmaci64
cordicMagFix2FastVec_v2.asv
cordicMagFix2FastVec_v2.m
cordicMagFix2FastVec.asv
cordicMagFix2FastVec.m
cordicMagFix2Vec.m
cordicMagFix3.asv
cordicMagFix3.m
cordicRotate.m
cordicRotate2.m
cordicRotate3.m
cordicMagFix2FastVec_v2_mex.mexmaci64
cordicMagFix2FastVec_v2_single_mex.mexmaci64
cordicMagFix2FastVec_v2_single.mexmaci64
cordicMagFix3_mex.mexmaci64
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Fil_NR_0p1_30dB.m
Fil_NR_122M_0p1_30dB.m
Fil_NR_491M_0p1_20dB.m
Fil_NR_491M_0p1_30dB.m
fil5.mat
LP_12288M_2_4M_2_6M_div1_01dB_60dB.m
LP_12288M_9_1M_9_7M_05dB_20dB.m
LP_12288M_9_1M_9_7M_div2_05dB_20dB.m
LP_12288M_9_1M_9_7M_div4_05dB_20dB.m
cfr_itr2.m
dpdAutomationMain2_Ref_Lut8_GoldenPerformance_n78.m
Multicarrier_CFR_v9_shaping.m
Multicarrier_CFR_v10_shaping.m
DF204532. Multicarrier CFR Pulse Cancellation with Customized EVM and Power Boost per

Carrier
IDF207003. Customized CFR Noise Shaping over Spectrum
IDF204340 CFR Pulse Cancellation Design with Configurable Bandwidth and Center Frequency

B-8: Massive MIMO (mMIMO)

Scenario.py
TVG Automation3.csv
pscript.py
outfile.m
Matfile.m
ulaninput.txt
ulaninput_ulan_error_messages.txt
ulaninput_ulan_debugParams.txt
ulaninput_ulan_analysis_results.txt
ulan_nr5g_oran_mat.exe
oranToMat_v4.exe
oranToMat_FreqDomain_v4_usage.txt
create_mat2.bat
create_mat1.bat
Create_mat_34layers.bat
Nov03.zip
MatlabPRACHSimulator_shared.zip
Viavi_DU_PRACH_parser_V3.m
PRACH_interference.m
powercapture1_Oct18_164524.mat
IOT_hNRPRACHWaveformGenerator.m
hPRACHDetectionResults.m
hPRACHDetect.m
hNRPRACHWaveformGenerator.m
FB_IOT_PRACH_Detection.m
FB_IOT_hPRACHDetect.m
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Viavi_DU_PRACH_parser_V2.m
PRACH_interference.m
NewRadioPRACHDetectionExample.m
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hPRACHDetectionResults.m
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FB_IOT_PRACH_Detection.m
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FB_IOT_PRACH_Detection_encapsulated.m
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AddAwgn_regularMIMO.m
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ueInterfSignalGenerator.m
ueDesiredSignalGenerator.m
Meta_nrRxTimeDomainProcessing.m
Meta_nrFreqDAGC.m
Meta_nrRxCore.p
Meta_nrRnnCov.p
Meta_nrPuschDmrsLsChest.p
Meta_nrLSExtrapMatrix.p
Meta_nrLS_StoredExtrapMatrixNdNe12.mat
Meta_nrLS_StoredExtrapMatrixNdNe6.mat
Meta_nrExtrapMatrixGenerator.p
Meta_nrEqualizeMMSE.p
Meta_nrEqualizeIRC2_Cholesky.p
Meta_nrEqualizeIRC1_Cholesky.p

Meta_nrDmrsMeasurement.p
Meta_nrDmrsChestTDnoiseReduction.p
Meta_nrDmrsChestNoiseReduction.p
Meta_nrDmrsChannelEstimate.p
Meta_nrDmrs2DataChestInterpCoefLinear.p
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hPUSCHDecode.m
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PUSCH_capture_slot_8.mat
pcodeScript.m
gNB_PuschLinkSimSystem.m
gNB_PuschLinkSimSystem_SISO_TC1.m
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mergeimports.m

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